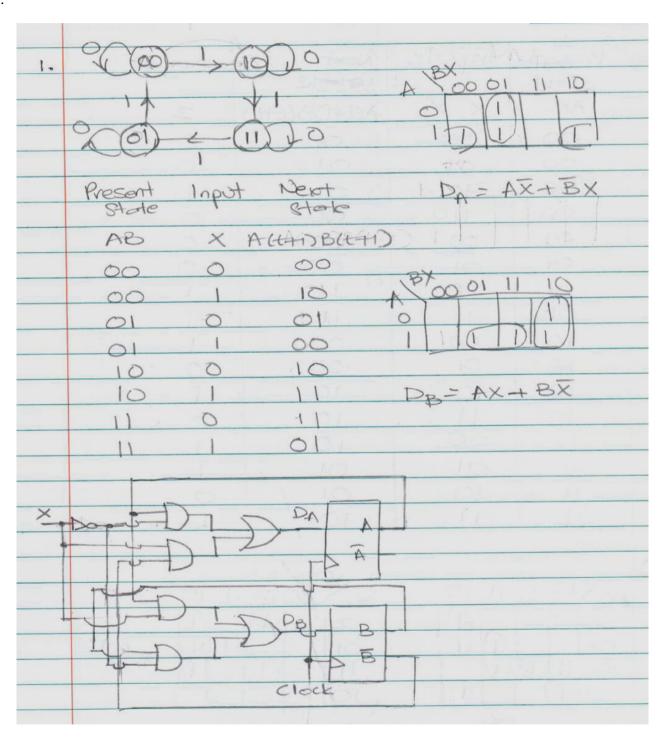
The University of New South Wales

## **ELEC2141: Digital Circuit Design**

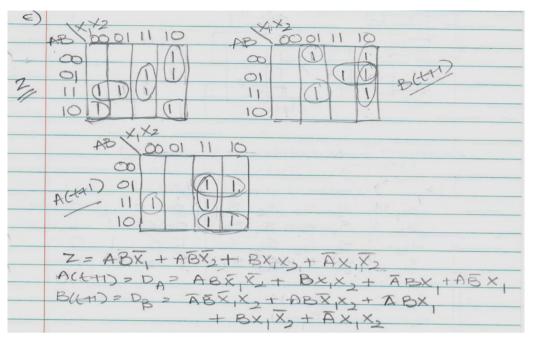
## **Tutorial Week 7 - Sequential Circuit Design**

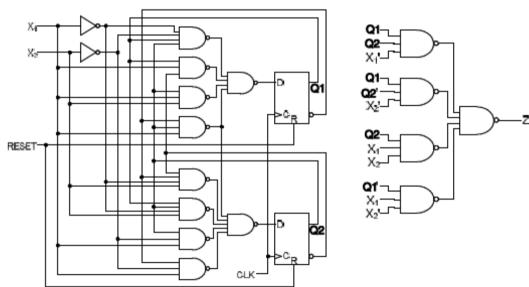
1.



topto	Next state	Present	2.
4	ALHI) B(LH)	Stale	6
x=00 01 10 11	X,X,200 01 10 11		
0010	5, 5, 5, 5,	5.	
0011	S S S S4 S4	S,	
1010	5, 5, 53 S3	Sa	
1101		54	
E DECEMBE	XX4 4 XX5	I BARA	100
Character Charac	53=10	S,=00	
	5,211	52=01	
1010	S <sub>1</sub> S <sub>2</sub> S <sub>3</sub> S <sub>3</sub> S <sub>4</sub> S <sub>3</sub> S <sub>3</sub> S <sub>3</sub> S <sub>5</sub> S <sub>3</sub> S <sub>3</sub>	S <sub>1</sub> =00	

5)	Present	inpots	Next	output
	state	in con Title	state	7
	ARS	X, X2	ACHOB(+N)	00 2 10 11
	00	00	00.	0
	00	01	01	0
	00	10	01	
	00	11.	00	0
	01	60	00.	0
	01	01	00	0
	01	10	1.152	1 1 -
	01	1)	11:	
	10	00	00	\ .
	10	01	00	0.
	10	10	10	1
	10	11	10	0
	1)	00	10	1.
	- 11	01	. 01	1 =
	11	10	01	0
		11	10	1





11	5-0001		0010;5=0100;5	=1000
9)	3=200.	) 2	3	,
	Present	Inputs	Next state	tugtoo
	State	,		
	ABCD	XIX2	ALL+10B(HM)C(HM)D(HM)	2
	0001	00	0001	0
	0001	01	0010	0
	0001	10	0010	
	1000	11	1000	0
	0010	00	1000	0
	0010	01	0001	0
	0010	10	1000	1
	0010	11	1000	1
-	0100	00	0001	1
	0100	01	1000	0
	0100	10	0100	1 -
	0100	11	0100	0
	1000	00	0100	
	1000	01	0010	
	1000	10	0010	0
	1000.	11	0100	

$$A(t+1) = CX,$$

$$B(t+1) = BX_1 + AX_1X_2 + AX_1X_3$$

$$C(t+1) = DX_1X_2 + DX_1X_3 + AX_1X_3 + AX_1X_3$$

$$D(t+1) = DX_1X_2 + DX_1X_3 + CX_1 + BX_1$$

$$2 = DX_1X_2 + CX_1 + BX_2 + AX_1 + AX_1X_2$$

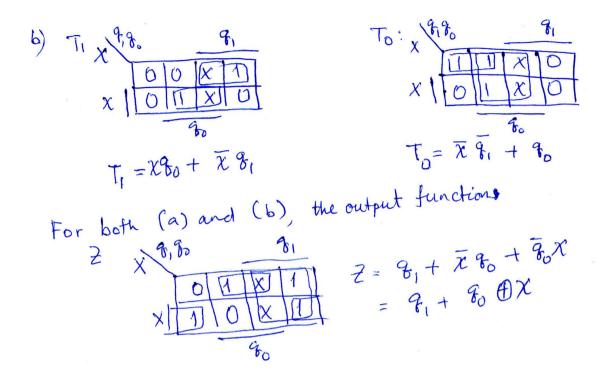
Circuit not shown – but can be drawn based on equations

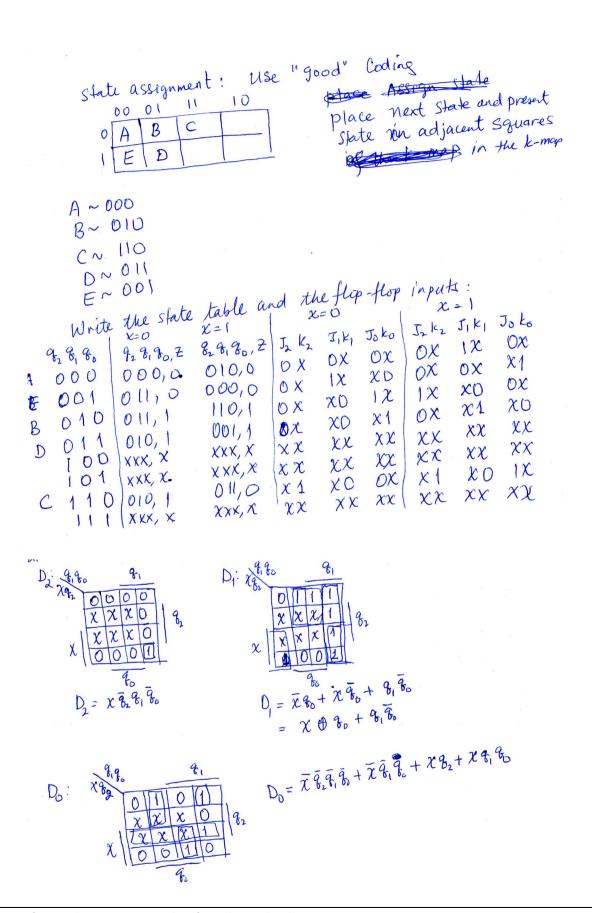
3.

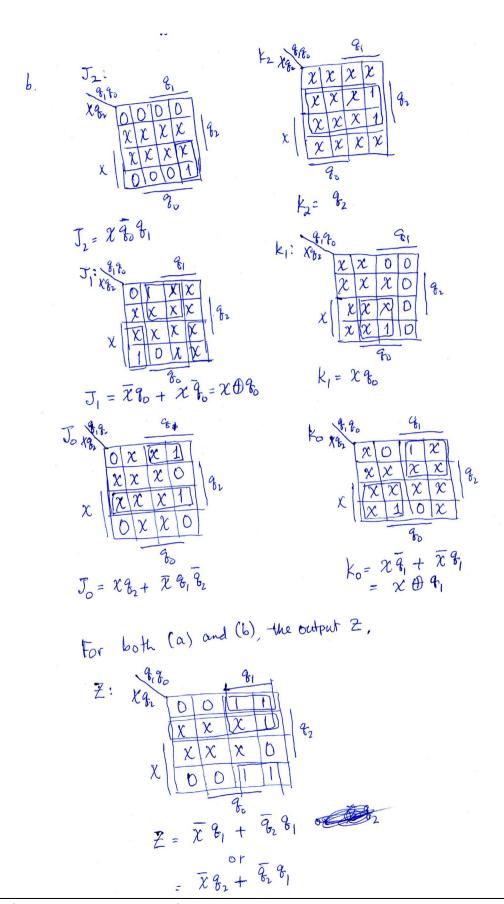
3.		Present	Next	state	Dutput
0)	000 A	State	X=0	X=1	2
	B 001	A	C	E	0
	C 010	В	E	D	(
	D 011	C	c	E	0
	E 101	D	F	A	1
	F 110	E	B	D	1
		F	C	E	0

. \	R X
(d	C / X
/A	D X DX ANCNF
	FYXXX
	I A'BCDE
	Present New Stale Output
	charle X=0 X=1
	A A B O
	B B D
	D A A
	choosing assignment so one variable
	matches coutput
	A = 72 = 00; B = 61; D=11
	$\Rightarrow$ Z = output
	Present Input Next Output
	12 Y(t+1)2(t+1) Z
	00 0 00 0
	00 1 01 0
	01 0 01
	3(t+1)= D,= x 92
	2(+41) = D2 = x 92 + x 92 + x 92
	$= \overline{x} \overline{y} \overline{z} + \overline{y} \overline{z} = \overline{y} (\overline{x} \overline{z} + \overline{z}) = \overline{y} (\overline{x} + \overline{z})$
	= XY+ YZ
	·

	1	hinary coding		
A B C	State assignment.  A ~ 00  Write the state  Present X=0  9, 80  01, 0  01, 0  01, 0  01, 0  01, 0  XX, X	binary com  B~01 C~10  B~01 C~10  Lable and the file  2 x=1 D. Do  00, 1 01  10, 0 00  10, 1 00  XX, X XX	ip-flop input  X=1	x=1 T, To 00 11 00 xx
m) D1	$ \begin{array}{c c} x & & & & & & & & & & & \\ x & & & & & & & & & & \\ x & & & & & & & & & \\ x & & & & & & & & \\ x & & & & & & & & \\ x & & & & & & & \\ x & & & & & & & \\ x & & & & & & & \\ x & & & & & & & \\ x & & & & \\ x & & & $	$D_0 \approx \frac{9.80}{2} = \frac{9.9}{9.9} \times \frac{9.9}{9.$		







Use implication Chart H~D X D States Read of equivalent

We implication Chart

B

C

X

C

C

C

A

B

C

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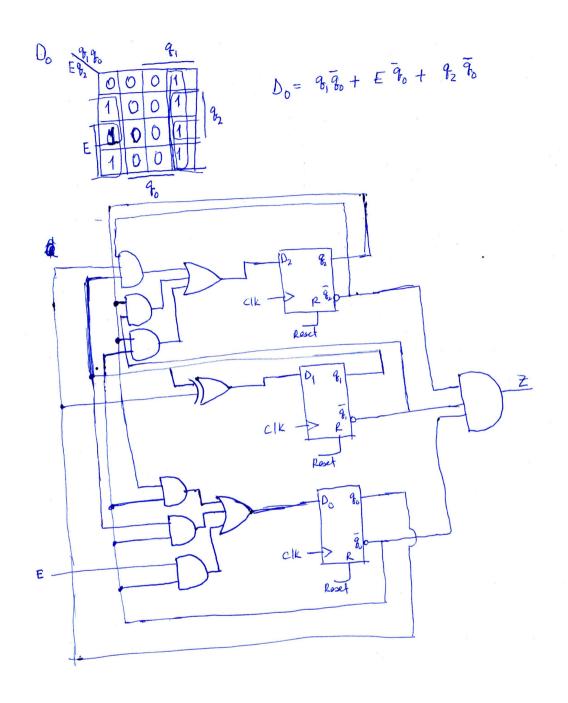
C

D

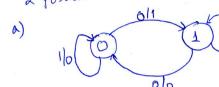
1. When E=1, the Circuit will output the required

Sequence regardless of input E

When E=0, the circuit outputs constant 1 and remains in that same state 0,1 use binary coding assignment. The state table is Q2 Q1 Q0 Q2 Q1 Q0 g, 9, 1 00 0 010 000 0 D 011 001 Z = 92.9,90 0 100 0 101 0 110 110 O 111 111 0 000 10 000 D, E92 Di= 4,80 + 8, 40 = 80 81



Define the States as the last value at the NRZI message. 2 possible states: D and 1



e) Derive flip-flop input equation and output equation:

