## ELEC 2141 Final Dam, Session 1, 2009.

## Question 1

i)
$$F = (A + B)(A + C + D) + (\overline{A} + B)(B + C + \overline{D})A$$

$$= A + AC + AB + BC + BD + AB + ABC + ABD$$

$$= A + B(C + D)$$

$$A + B + B + BC + BD + AB + ABC + ABD$$

$$= A + B(C + D)$$

$$F = Em(5, 6, 7, 3, 9, 10, 11, 12, 13, 14, 15)$$

$$= A + B + B + BC + BD + AB + ABC + ABD$$

(i)
$$X+Y=X\oplus Y+XY$$

$$X\oplus Y+XY=X\overline{Y}+\overline{X}Y+XY$$

$$=X\overline{Y}+XY+XY+\overline{X}Y$$

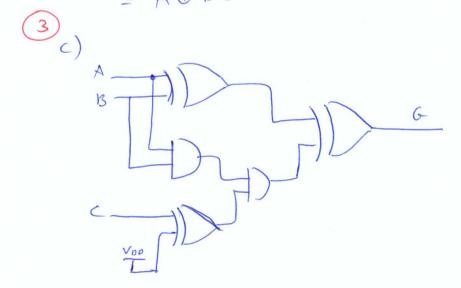
$$=X(\overline{Y}+Y)+Y(X+\overline{X})$$

$$=X+Y.$$

$$G = AB + ABZ + \overline{ABZ}$$

$$= ABB + ABZ + \overline{ABZ}$$

$$= ABB + ABZ$$



3)

1 1	hex	+ state,	cutput
State	X= 0	X =	
A	13,1	C,1	
B	6,0	G, 0	
C	<b>Æ</b> , 1	A, 0	
D	0,0	B, 1	
E	F, I	<b>(5</b> )	
F	H, 0	D, 0	
6	6,0	F, I	
H	F, I	c, 1	

(1)

B	X	7					
C	X	×					
0	X	×	$\times$				
6	BAF	×	X	×			
F	X	62H	×	X	X	7	
G	X	X	×	10~6 13~F	×	×	
1-1	8~F	X	X	×	Fre	X	X
	A	13	C	D	E	F	6

A~E~H

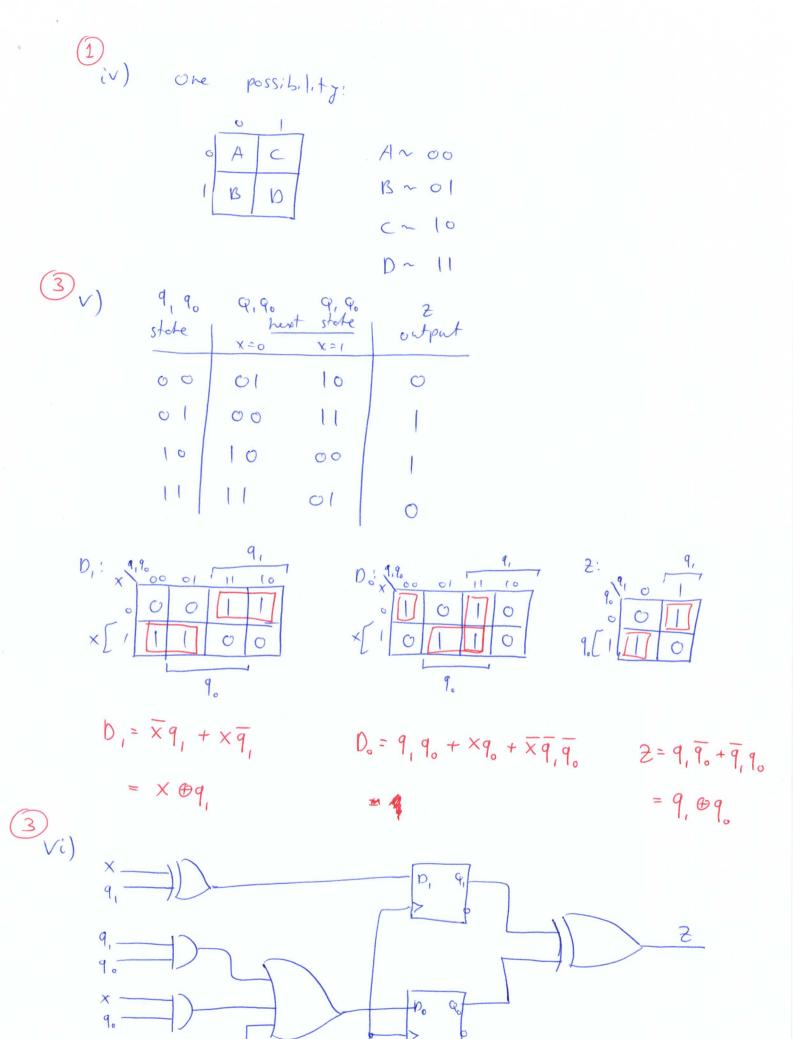
13 ~ F

D~ G

1 /	hext	output
State	X=0	X = 1
A	B,1	C, 1
B	A, 0	b, o
_	c, 1	A,0
0	0,0	B, 1

(iij

1 /	next s	output	
state	X ≈ Ø	X=I	
A	B	C	0
B	A	Ŋ	(
C	C	А	
0	р	B	0



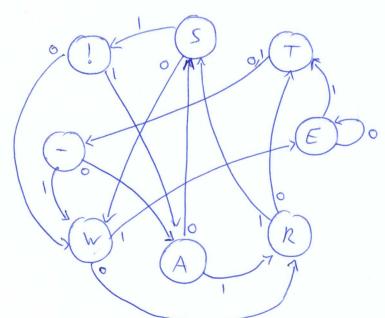
CLK

(3) (1)
$$T_{2} = \overline{q}_{1}$$

$$T_{1} = X + q_{0} + q_{2}q_{1}$$

$$T_{0} = X + \overline{q}_{2}q_{1}$$

(4)



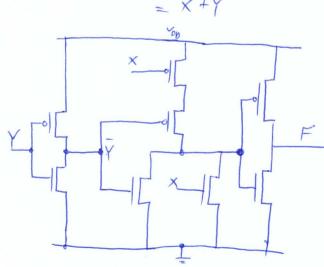
(3) iv)

(5) i) 
$$27/2 = 13$$
  $R = 1$   $13/2 = 6$   $1$   $27_{10} = 11011_{2}$   $6/2 = 3$   $0$   $1/2 = 1$ 

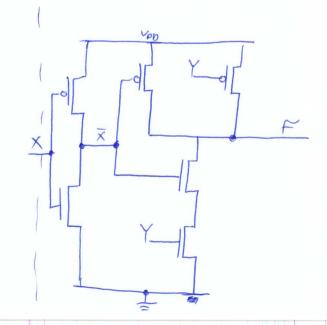
$$0.615 \times 2 = 1.25$$

$$= X + \overline{Y} + \overline{Y} \ge$$

$$= x + \overline{y}$$



Oh: 
$$F = \overline{X + Y} = \overline{X}Y$$



$$G = \overline{C(AB+D)} = \overline{ABC+CD} = (\overline{A}+\overline{B}+\overline{c})(\overline{c}+\overline{D})$$

$$= \overline{AC}+\overline{AD}+\overline{BC}+\overline{CD}$$

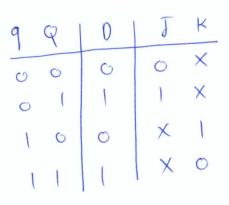
$$= \overline{C}+\overline{D}(\overline{A}+\overline{B})$$

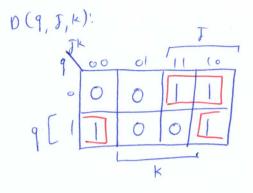
$$= \overline{C}+\overline{D}(\overline{A}+\overline{B})$$

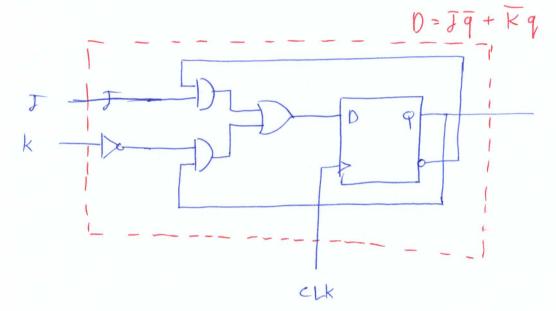
ABCD	G
0000	1
0001	
0010	1
0011	0
0 (00	1
0101	1
0110	
0111	O
1000	1
1001	(
1010	1
1011	0_
1100	(
101	
1110	0
1111	0

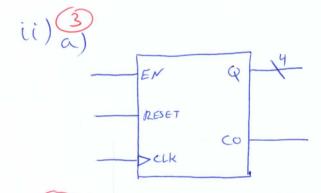












b) Four-bit (ounter with an asynchronous Reset.

(5)

## Inputs:

CLK-A clock Signal to Synchronize the counter

EN- Enoble Signal - the counter increments

When EN=1 and keep steady Men EN=0.

RESET - Asynchronous signal to reset the counter to 0,

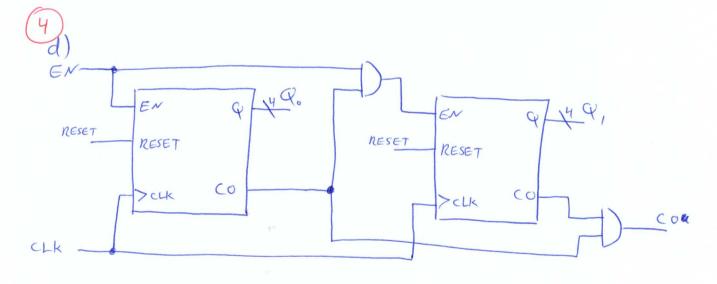
only resels on the positive edge of the signal.

## outputs:

Q- The 4-bit counter value.

Co - Carry-out Signal. Gods HIGH when the counter is at its highest value (1111) and Low otherwise.

Can be used to cascade two counters.



always Q(posedge s) begin  $Q \le 1$ ;

end

always @ (pos edge R) begin  $Q \le 0$ ;

end

always @ (negedge CLK) begin  $Q \le D$ ;

end

(2) No overflow!

$$\frac{10010001}{001000} \Rightarrow \frac{10010001}{11011100}$$

$$(2)_{b})$$
  $N = 0$   $C = 1$   
 $2 = 0$   $V = 1$