

Final Exam, Session 1, 2011Question 1

c)

$$F = \cancel{AB} + AC + \cancel{A\bar{B}} + \underline{B} + \cancel{BC} + \bar{A}D + \cancel{BD} + CD \\ + \cancel{A\bar{B}\bar{C}} + \cancel{A\bar{B}D} + \cancel{A\bar{B}D} + A\bar{C}D + AD$$

$$= B + AC + D(\underline{\bar{A}} + C + \cancel{A\bar{B}} + \cancel{A\bar{C}} + \underline{A})$$

$$= AC + B + D$$

(2) a)

A	B	C	D	F
0	0	0	0	0
0	0	0	1	1
0	0	1	0	0
0	0	1	1	1
0	1	0	0	1
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	0
1	0	0	1	1
1	0	1	0	1
1	0	1	1	1
1	1	0	0	1
1	1	0	1	1
1	1	1	0	1
1	1	1	1	1

(1) b)

$$F = \sum m(1, 3, 4, 5, 6, 7, \\ 9, 10, 11, 12, 13, \\ 14, 15)$$

(1) c)

$$F = \prod M(0, 2, 8)$$

K-map:

		A			
		AB			
C	D	00	01	11	10
	00	0	1	1	0
	01	1	1	1	1
	11	1	1	1	1
	10	0	1	1	1
		B			

① d) B, D, AC

② e) B, D, AC

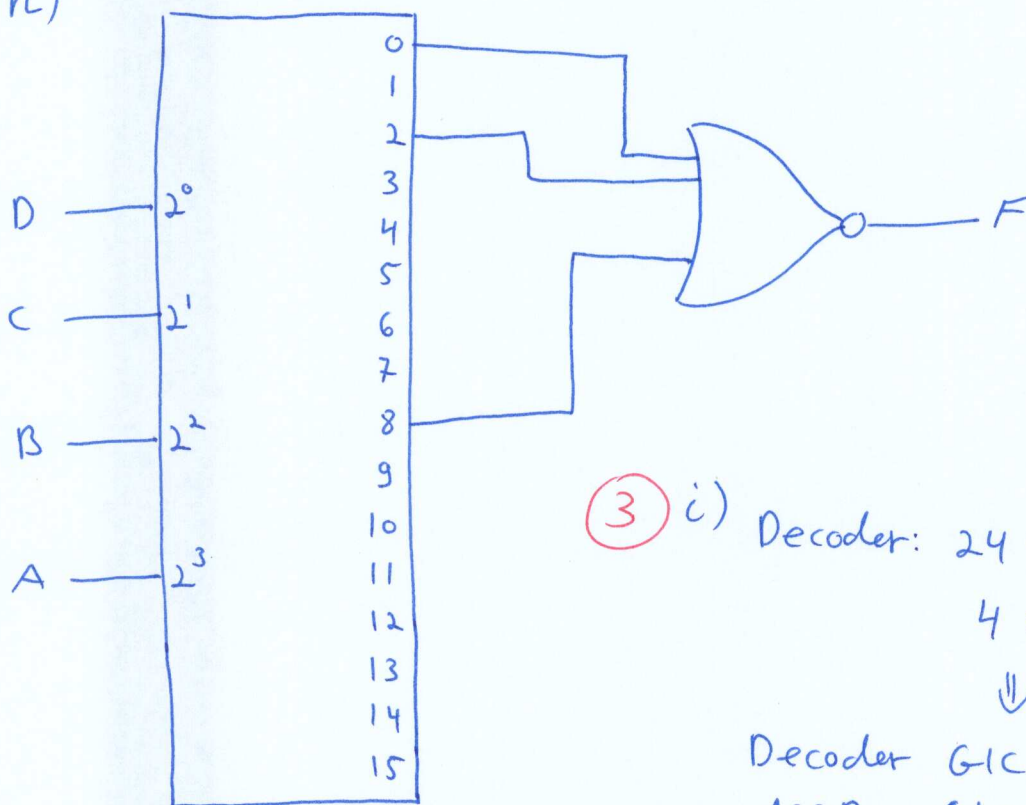
① f)  $F = AC + B + D$

② g)  $\bar{F} = \bar{A}\bar{B}\bar{D} + \bar{B}\bar{C}\bar{D}$

$\Downarrow$

$$F = (A + B + D)(B + C + D)$$

③ h)



③ i) Decoder:  $2^4 \times 2$ -input AND  
4 x inverter

$\Downarrow$

Decoder GIC = 52

NOR GIC = 3

Total GIC = 55

④ ii)  $BC + \bar{A}\bar{B} + \bar{A}\bar{C} = ABC + \bar{A}BC + \bar{A}\bar{B} + \bar{A}\bar{C}$   
 $= ABC + \bar{A}(BC + \bar{B} + \bar{C})$   
 $= ABC + \bar{A}(\bar{B} + C + \bar{C})$   
 $= ABC + \bar{A} \checkmark$

## Question 2

i)

(2) a)  $\min = 1$        $\max = 2^L$

(2) b)  $\min = 2^m$        $\max = 2^m$

(2) c)  $\min = 0$        $\max = 2^L \times 2^M = 2^{L+M}$

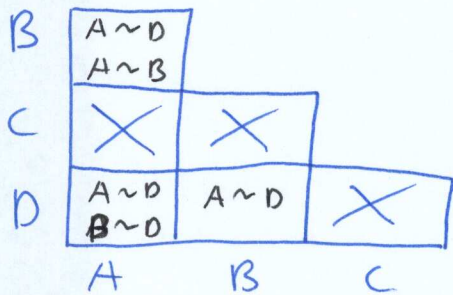
② d)  $\min = 1$

$$\max = \begin{cases} 2^N & \text{if } N \leq (L+M) \\ 2^{L+M} & \text{if } N > (L+M) \end{cases}$$

(ii)

(Easier to do (b) and then (a)):

④ b)


$$\Rightarrow \begin{array}{ll} A \sim B \sim D & (\text{state A}) \\ C & (\text{state c}) \end{array}$$

↓

next, output

Current	$xy=00$	$01$	$10$	$11$
A	A, 0	C, 0	A, 0	A, 1
C	A, 1	A, 0	C, 0	A, 0

(4) a) split state A to: A/0 and B/1.

Current	next				output z
	xy = 00	01	10	11	
A	A	C	A	B	0
B	A	C	A	B	1
C	B	A	C	A	0



Alternatively, Do (a) first and then (b):

- ④ a) split B to  $B_0/0$  and  $B_1/1$   
split D to  $D_0/0$  and  $D_1/1$

current	next				output z
	xy = 00	01	10	11	
A	A	C	$B_0$	$B_1$	0
$B_0$	$D_0$	C	A	$B_1$	0
$B_1$	$D_0$	C	A	$B_1$	1
C	$D_1$	$B_0$	C	$D_0$	0
$D_0$	$D_0$	C	$D_0$	$B_1$	0
$D_1$	$D_0$	C	$D_0$	$B_1$	1

- ④ b)

$B_0$	$A \sim D_0$ $A \sim B_0$				
$B_1$	X	X			
C	$A \sim D_1$ $B_0 \sim C$ $B_1 \sim D_0$	$D_0 \sim D_1$ $B_0 \sim C$ $A \sim C$ $B_1 \sim D_1$	X		
$D_0$	$A \sim D_0$ $B_0 \sim D_0$	$A \sim D_0$	X	$D_0 \sim D_1$ $B_0 \sim C$ $C \sim D_0$ $D_0 \sim B_1$	
$D_1$	X	X	$A \sim D_0$	X	X
	A	$B_0$	$B_1$	C	$D_0$

$\Rightarrow$

$A \sim B_0 \sim D_0$  [A]  
 $B_1 \sim D_1$  [B]  
 C [C]

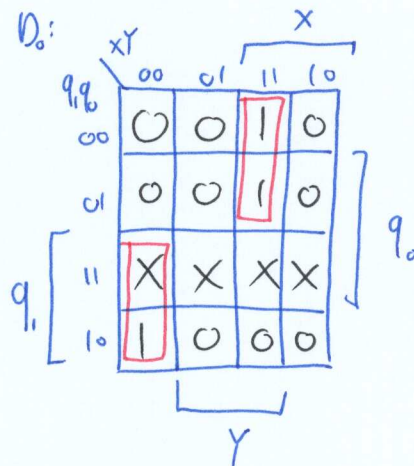
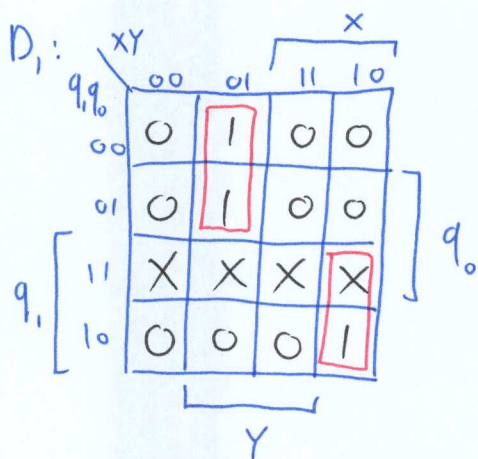
current	next				output z
	xy = 00	01	10	11	
A	A	C	A	B	0
B	A	C	A	B	1
C	B	A	C	A	0

① c) Assign:

$A \sim 00$      $B \sim 01$      $C \sim 10$

③ d)

current $q_1, q_0$	next ( $Q_1, Q_0$ )				output $z$
	$xy = 00$	$01$	$10$	$11$	
$00$	$00$	$10$	$00$	$01$	$0$
$01$	$00$	$10$	$00$	$01$	$1$
$10$	$01$	$00$	$10$	$00$	$0$
$11$	$xx$	$xx$	$xx$	$xx$	$x$



Flip-flop equations:

$$D_1 = \bar{x} Y \bar{q}_1 + x \bar{Y} q_1$$

$$D_0 = \bar{x} \bar{Y} \bar{q}_1 + x Y \bar{q}_1$$

output equation:

$$z = q_0$$

### Question 3

(3) i)  $J_2 = x \oplus q_1$        $J_1 = x(q_2 + q_0)$        $J_0 = 1$   
 $K_2 = x \oplus q_1$        $K_1 = x \bar{q}_0$        $K_0 = 1$

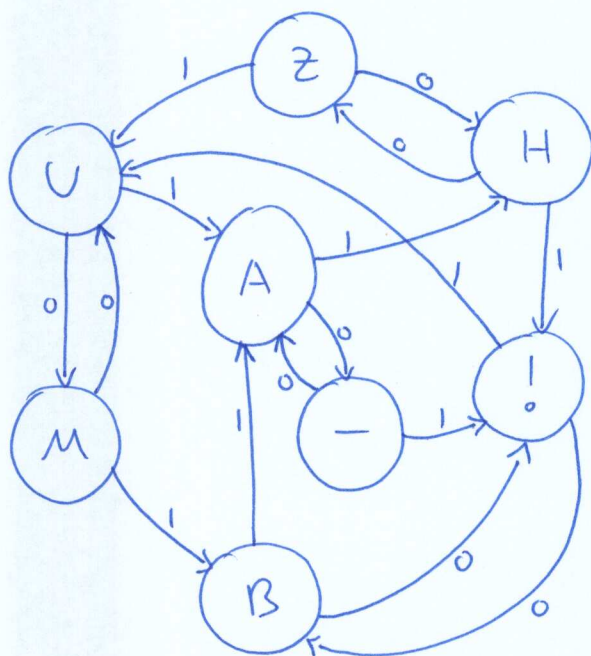
ii)

(10)

	current $q_2 q_1 q_0$	$x=0$			$x=1$			next	
		$J_2 K_2$	$J_1 K_1$	$J_0 K_0$	$J_2 K_2$	$J_1 K_1$	$J_0 K_0$	$x=0$ $q_2 q_1 q_0$	$x=1$ $q_2 q_1 q_0$
Z	0 0 0	0 0	0 0	1 1	1 1	0 1	1 1	H 0 0 1	U 1 0 1
H	0 0 1	0 0	0 0	1 1	1 1	1 0	1 1	Z 0 0 0	! 1 1 0
A	0 1 0	1 1	0 0	1 1	0 0	0 1	1 1	- 1 1 1	H 0 0 1
B	0 1 1	1 1	0 0	1 1	0 0	1 0	1 1	! 1 1 0	A 0 1 0
M	1 0 0	0 0	0 0	1 1	1 1	1 1	1 1	U 1 0 1	B 0 1 1
U	1 0 1	0 0	0 0	1 1	1 1	1 0	1 1	M 1 0 0	A 0 1 0
!	1 1 0	1 1	0 0	1 1	0 0	1 1	1 1	B 0 1 1	U 1 0 1
-	1 1 1	1 1	0 0	1 1	0 0	1 0	1 1	A 0 1 0	! 1 1 0

iii)

(4)



(3) iv)

state	Z	U	M	B	A	-	A	H	!
x	1	0	1	1	0	0	1	1	1



# Question 4

i) a) Truth table for the subtractor:

$B_I$	X	Y	D	$B_L$
0	0	0	0	0
* 0	0	1	1	1
0	1	0	1	0
0	1	1	0	0
1	0	0	1	1
1	0	1	0	1
* 1	1	0	0	0
1	1	1	1	1

D:

$B_I \backslash XY$	00	01	11	10
0	0	1	0	1
1	1	0	1	0

Y

$B_L$ :

$B_I \backslash XY$	00	01	11	10
0	0	1	0	0
1	1	1	1	0

Y

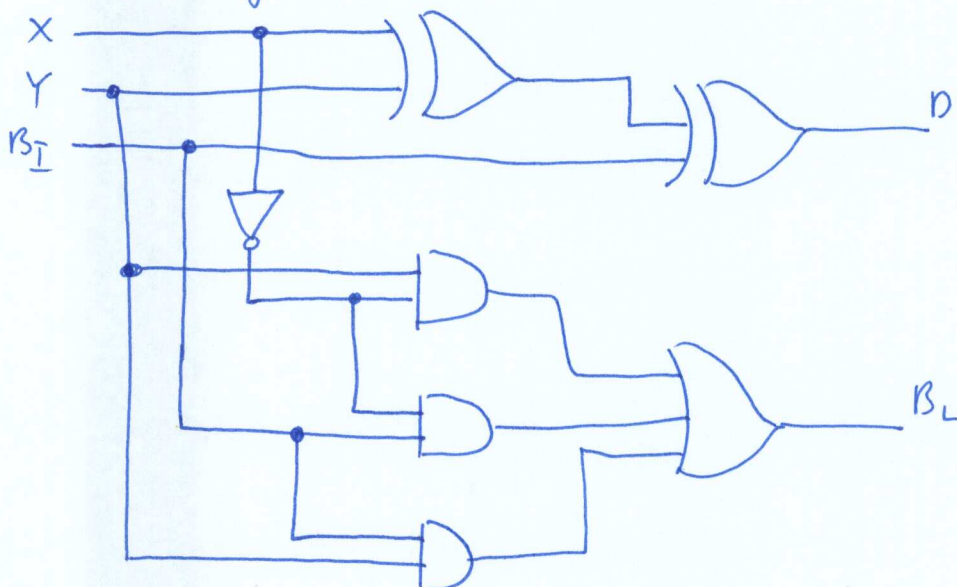
From k-maps:

$$D = \bar{X}\bar{Y}B_I + \bar{X}Y\bar{B}_I + XYB_I + X\bar{Y}\bar{B}_I$$

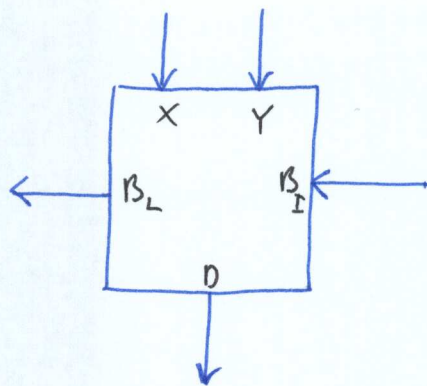
$$= X \oplus Y \oplus B_I$$

$$B_L = \bar{X}Y + \bar{X}B_I + YB_I$$

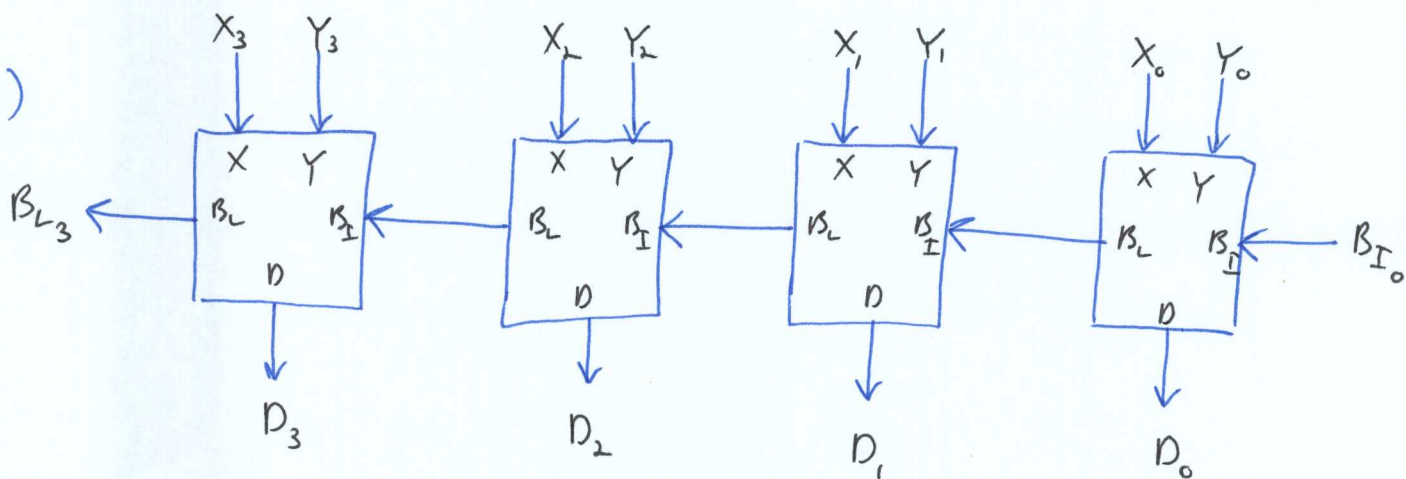
Logic Diagram:



② b)



③ c)



③ d) Underflow will occur when subtracting a positive number from a negative one and the result is positive or when subtracting a negative number from a positive one and the result is negative. Looking at the MSB, this is one of two cases:

$$\begin{array}{r} X_3 \\ - Y_3 \\ \hline D_3 \end{array} \Rightarrow \begin{array}{r} 1 \\ - 0 \\ \hline 0 \end{array} \quad \text{or} \quad \begin{array}{r} 0 \\ - 1 \\ \hline 1 \end{array}$$

The two cases are marked with a "\*" on the subtractor truth table, and can be flagged by the function:

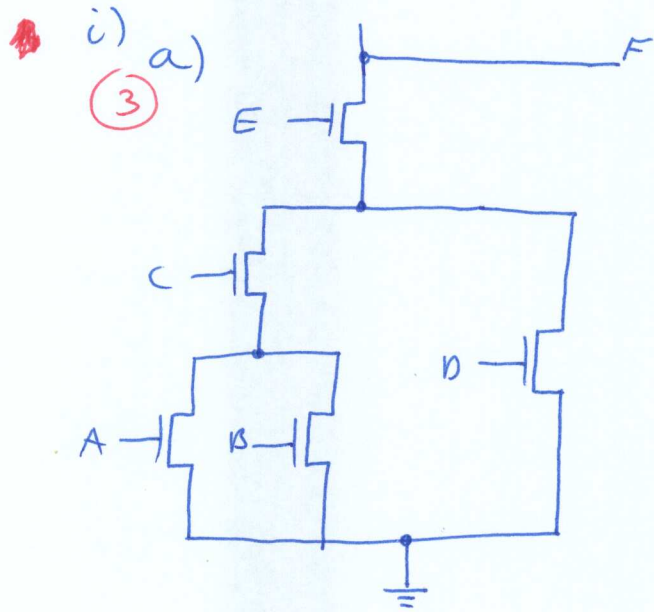
$$UF = B_{I_3} \oplus B_{L_3}$$



(ii) 4 A datapath can be modeled as a state machine as it has memory elements to store the current state (the registers). It has some number of inputs (such as the control inputs, data input, constant input, etc.) and produces outputs that ~~depend~~ depend on the inputs and the current machine state (Address output, data output, NZCV flags).

The registers are clocked elements such that a clock trigger will cause a state transition.

## Question 5

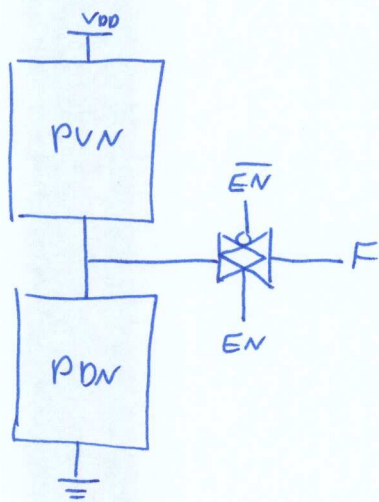


b) (3)

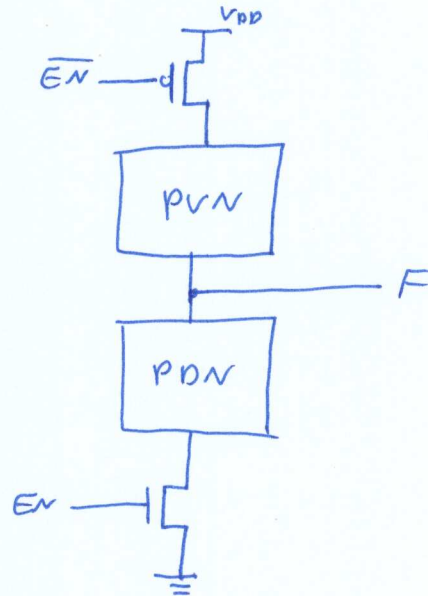
$$F = \overline{[(A+B)C + D] \cdot E}$$

$$= (\bar{A}\bar{B} + \bar{C})\bar{D} + \bar{E}$$

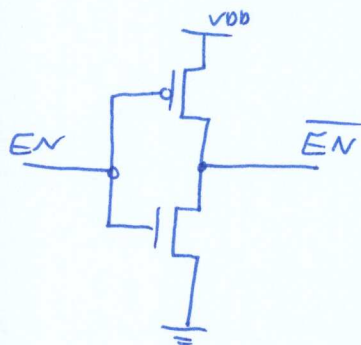
(4) c) One of two possibilities:



or



Where  $\overline{EN}$  is derived from:



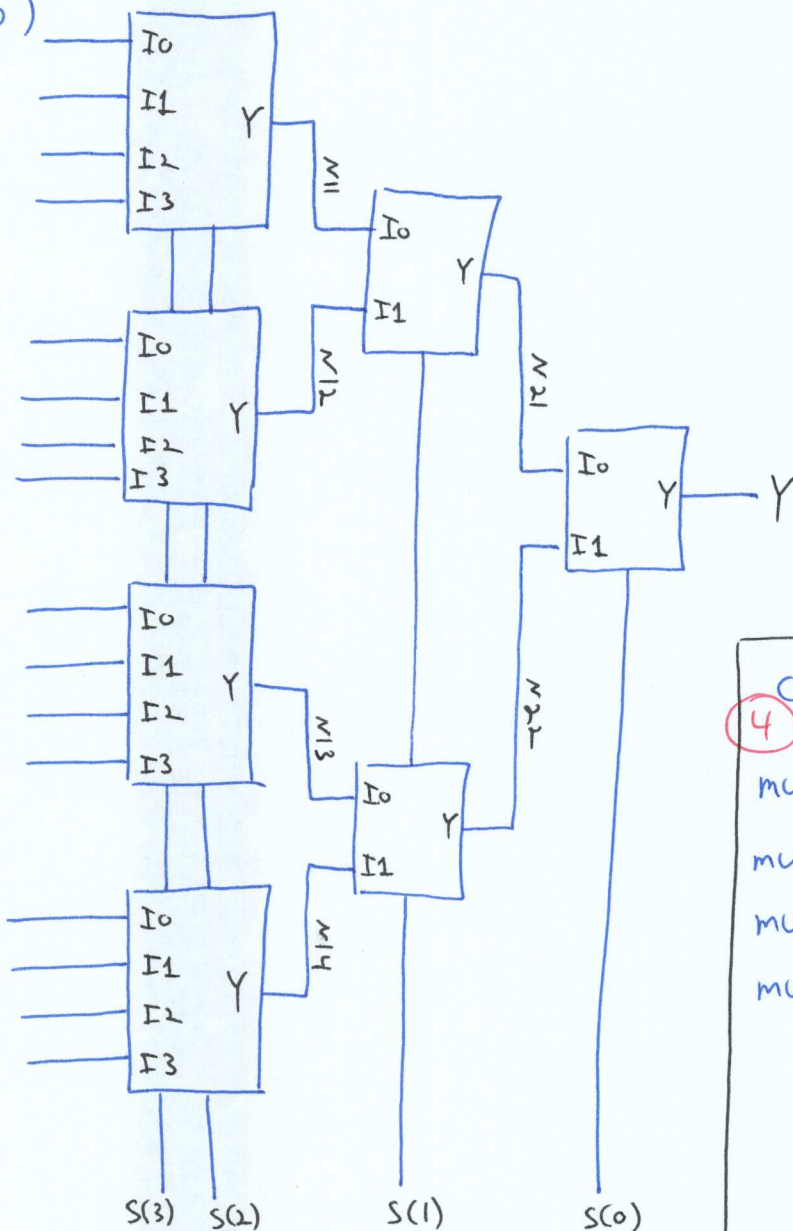
i i)

③ a) Both multiplexer implementations are missing the 'I' inputs in the always sensitivity list.

Currently, if a certain input is selected, the output will take the input's initial value but will not track it in case the input changes. The bug can be fixed by adding the 'I' inputs to the sensitivity list. Example for mux2:

always @(s or I0 or I1) begin

③ b)



④ c)

```
mux4 mx11 (S[3:2], I0, I4, I8, I12, ~11);
mux4 mx12 (S[3:2], I2, I6, I10, I14, ~12);
mux4 mx13 (S[3:2], I1, I5, I9, I13, ~13);
mux4 mx14 (S[3:2], I3, I7, I11, I15, ~14);
```