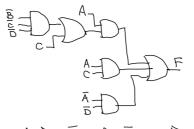
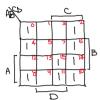


Students may draw hopic diagram to determine the GIC.



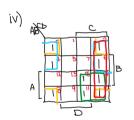
A-Do-Ā B-Do-B C-Do-C
D-Do-B

ii) Students can use the K-map to find the minterns for F or use algebraic expansion

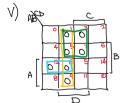


 $F = \sum m(0, 2, 4, 6, 8, 10, 11, 14, 15)$

Ĭti)	Α	В	\mathcal{C}	D	F	mi
•	0	0	0	0	t	M₀
	0	0	0	١	٥	mı
	0	0	1	0	1	Mz
	0	0	١	l	0	M_3
	0	l	0	0	١	Mt
	0	١	0	١	0	Ms
	Ö	ı	l	0	1	Me
	0	Ţ	l	ı	0	M_7
	1	0	0	0	1	₩
	l	0	O	(0	Mq
	l	0	١	0	1	Ww
	l	0	l	1	l	W
	l	l	0	0	O	Wis
	l	1	0	l	00	M ₁₃
	١	١	l	D	1	M ₁₄
	١	l	١	1	١	Wiz

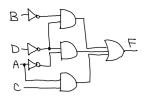


F= BD+ AD+ AC 4-50P PI: BD, AD, AC, CD EM: BD, AD, AC



 $F = (\overline{A} + \overline{B} + C)(C + \overline{D})(A + \overline{D}) \qquad 4 - POS$ $PI : (\overline{A} + \overline{B} + C), (C + \overline{D}), (A + \overline{D})$ $EPI : (\overline{A} + \overline{B} + C), (C + \overline{D}), (A + \overline{D})$

Reduction of G GIC after optimisation

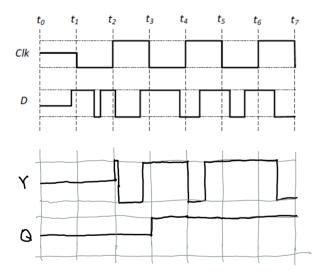


b) binary hexadecimal octal

Hex -> binary

Binary -> Octal

C) A-D latch
B-Porture edge traggered D-Aipflop



Question 2 B

Saturday, 7 March 2020 8:31 pm

a) i)
$$x+y=x\oplus y+xy$$

 $x\oplus y+xy=x\overline{y}+\overline{x}y+xy$
 $=x\overline{y}+xy+xy+\overline{x}y$
 $=x(\overline{y}+y)+y(x+\overline{x})$
 $=x+y$

ii) $H(A,B,C) = A\overline{B} + AB\overline{C} + \overline{A}B$

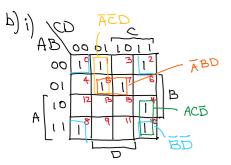
= ABB + ABC

X+Y- X@Y+XY

= A@B@ABZ + (A@B)(ABZ)

= $A \oplus B \oplus AB\overline{C} + (A\overline{B} + \overline{A}B)(AB\overline{C})$

= AOBOABC



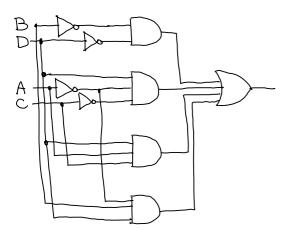
$$G = TM(3,4,6,9,11,12,13,15)$$

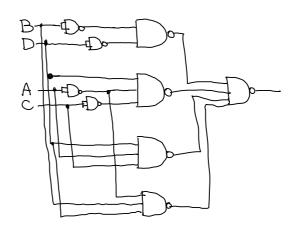
= $\Sigma_m(0,1,2,5,7,8,10,14)$

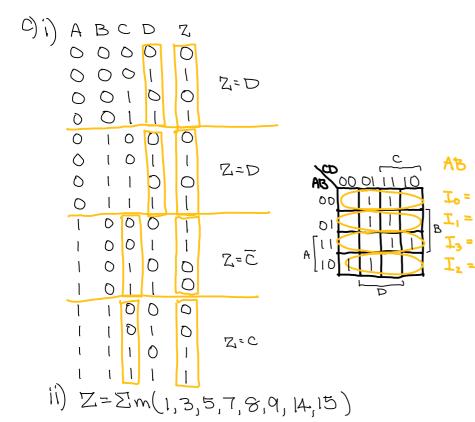
 $+ A\overline{B}C\overline{D} + \overline{A}B\overline{C}D$

= BD(AC +AC + AC + AC) + ACD(B+B) + ACD(B+B) + ABD(C+Z)

= BD+ACD+ACD+ABD







CD select

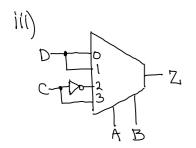
Is= AB

I = A+B

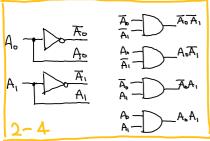
Iz= AB

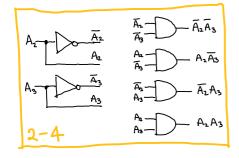
L = A+B

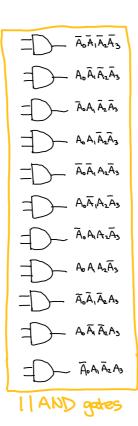
Note: Using CD as select would not give the simplest design.



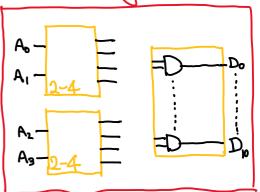
d) Not required







Block diagram



Refer to Week3 stide 51

I held to well strokes, n=4.

Use 2° AND gates driven

by two decoders of cutput

Size 2° = 4

Since BCD is only from 0 to X,

16-X-1 AND gates will be

redundant.