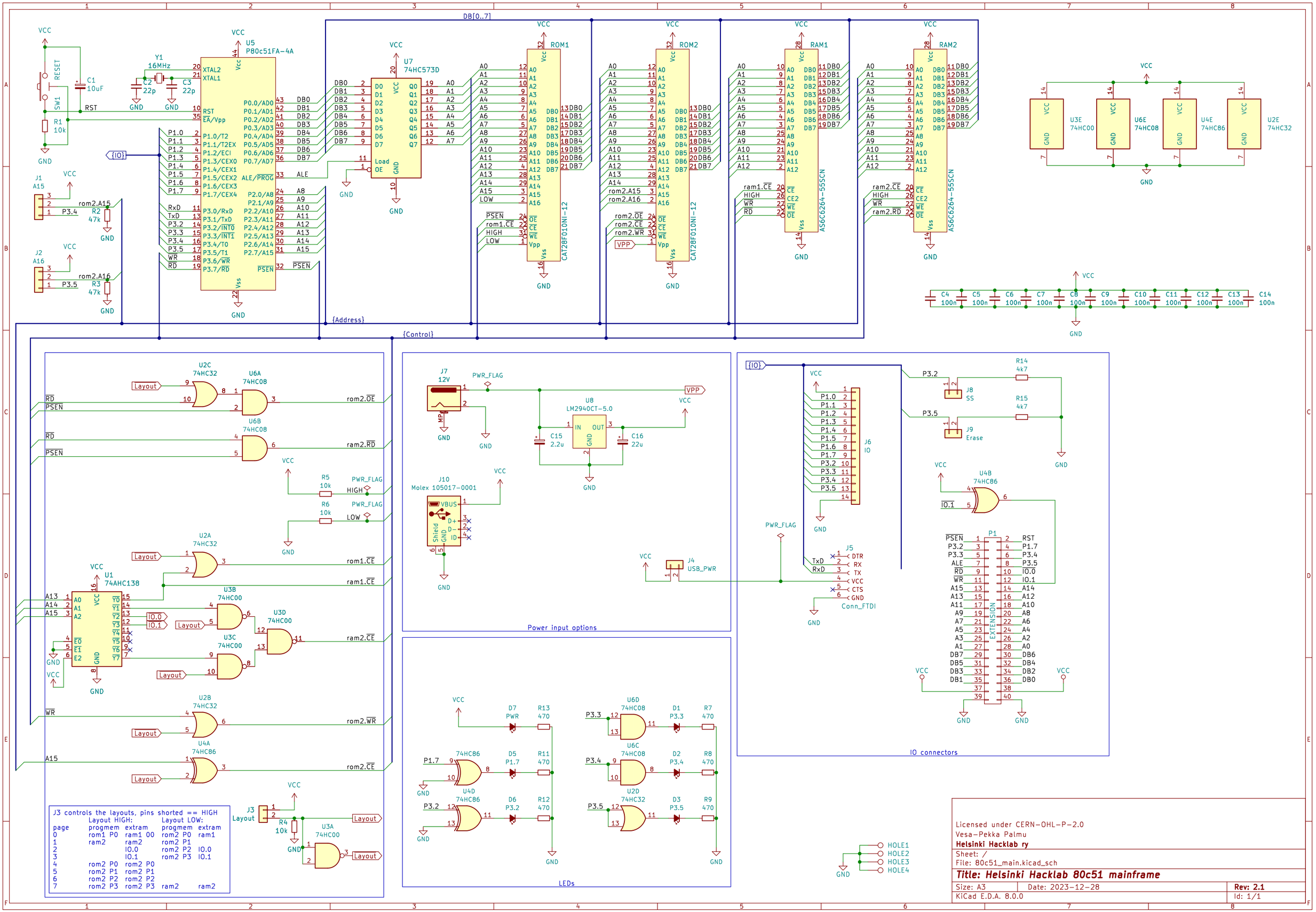
U5
P80c51FA-4A
U7
74HC573D
ROM1
ROM2
RAM1
RAM2
CAT28F010NI-12
AS6C6264-55SCN
U3E
74HC00
U6E
74HC08
U4E
74HC86
U2E
74HC32
DB[0..7]
{Address}
{Control}
{IO}
U2C
74HC32
U6A
74HC08
U6B
74HC08
U2A
74HC32
U1
74AHC138
U3B
74HC00
U3D
74HC00
U3C
74HC00
U2B
74HC32
U4A
74HC86
J3 controls the layouts, pins shorted == HIGH
Layout HIGH: Layout LOW:
page progmem extram progmem extram
0 rom1 P0 ram1 00 rom2 P0 ram1
1 ram2 ram2 rom2 P1
2 IO.0 rom2 P2 IO.0
3 IO.1 rom2 P3 IO.1
4 rom2 P0 rom2 P0
5 rom2 P1 rom2 P1
6 rom2 P2 rom2 P2
7 rom2 P3 rom2 P3 ram2 ram2
Power input options
LEDs
IO connectors
U4B
74HC86
P1
EXTENSION
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Vesa-Pekka Palmu
Helsinki Hacklab ry
Sheet: /
File: 80c51_main.kicad_sch
Title: Helsinki Hacklab 80c51 mainframe
Size: A3
Date: 2023-12-28
Rev: 2.1
KiCad E.D.A. 8.0.0
Id: 1/1