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REVISION	DESCRIPTION	DATE	APPROVED

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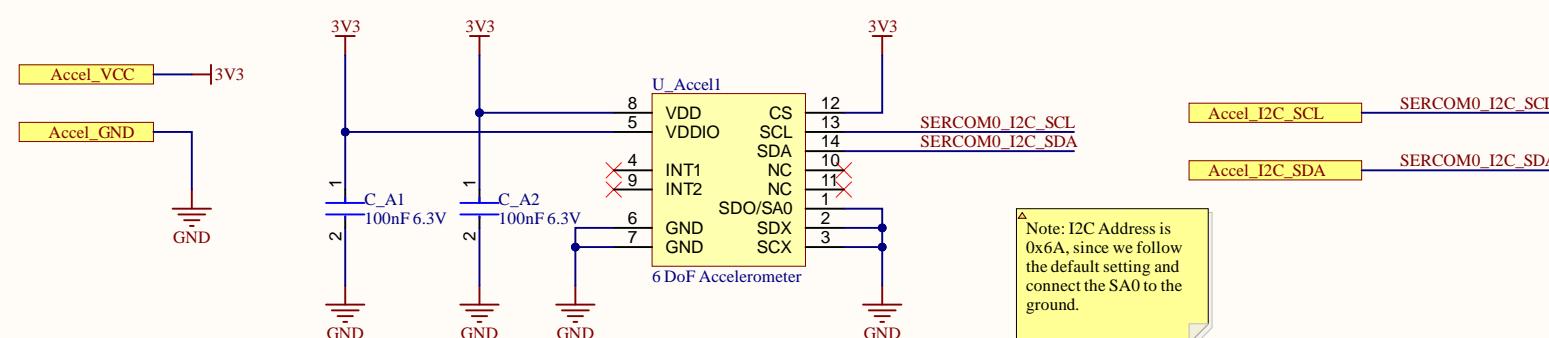
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APPROVALS	DATE	PROJECT	Penn Engineering	*
ENG: *		UNIVERSITY OF PENNSYLVANIA	*	*
DSN: *		PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
CHK: *		TITLE		
REFERENCE DOCUMENTS		Accelerometer		
BOM:				
ASSY DWG:		SIZE	CAGE CODE	DWG NO.
FAB DWG:		B		REV 1.0
PCB DWG:		SCALE:	FILE NAME Accelerometer.SchDoc	SHEET 1 OF 1

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REVISION	DESCRIPTION	DATE	APPROVED

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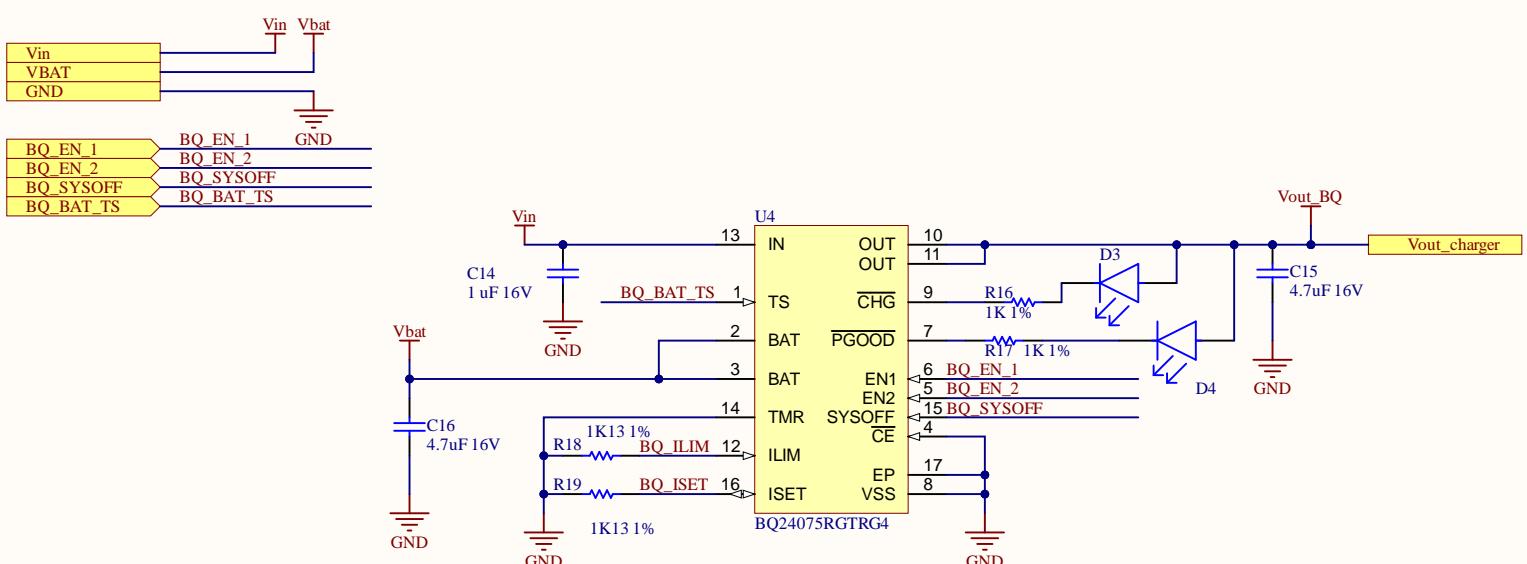
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APPROVALS	DATE	PROJECT	Penn Engineering
ENG:	*	UNIVERSITY OF PENNSYLVANIA	*
DSN:	*	PROJECT REVISION:	DOCUMENT REVISION:
CHK:	*	DESIGN ITEM:	
REFERENCE DOCUMENTS		TITLE	
BOM:	LiPo Charger		
ASSY DWG:	SIZE	CAGE CODE	DWG NO.
FAB DWG:	B		
PCB DWG:	SCALE:	FILE NAME	LiPo_Charger.SchDoc
		SHEET	1 OF 1

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T.	REVISION	DESCRIPTION	DATE	APPROVED

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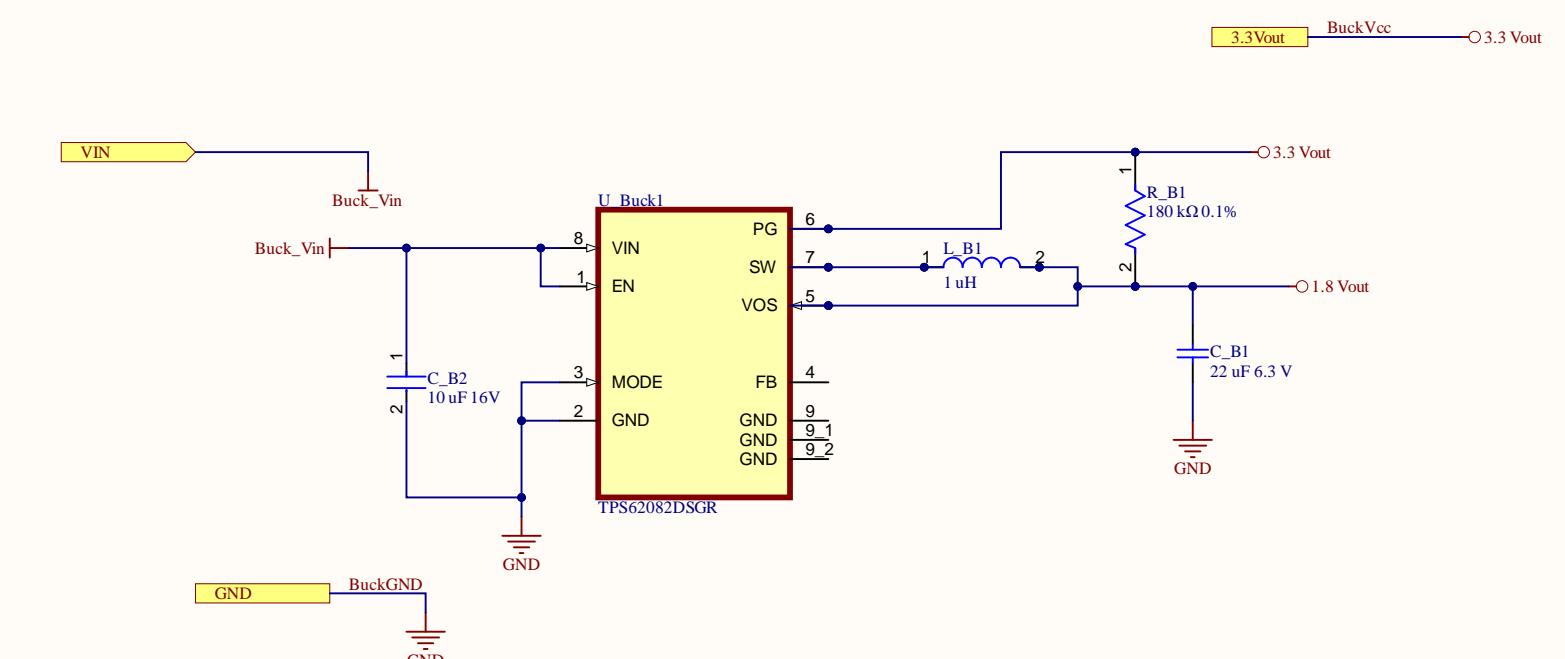
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APPROVALS	DATE	PROJECT	 <b>Penn</b> <b>Engineering</b> <small>UNIVERSITY OF PENNSYLVANIA</small>	*	
ENG: *				*	
DSN: *		PROJECT REVISION:	DOCUMENT REVISION:	*	
CHK: *				*	
REFERENCE DOCUMENTS		TITLE			
		<b>Buck Converter</b>			
BOM:		SIZE	CAGE CODE	DWG NO.	REV
ASSY DWG:					B
FAB DWG:					
PCB DWG:		SCALE:	FILE NAME	BuckConverter.SchDoc	SHEET 1 OF 1

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DWG. NO. 1.0.1 REV. SHT.		REVISION	DESCRIPTION	DATE	APPROVED

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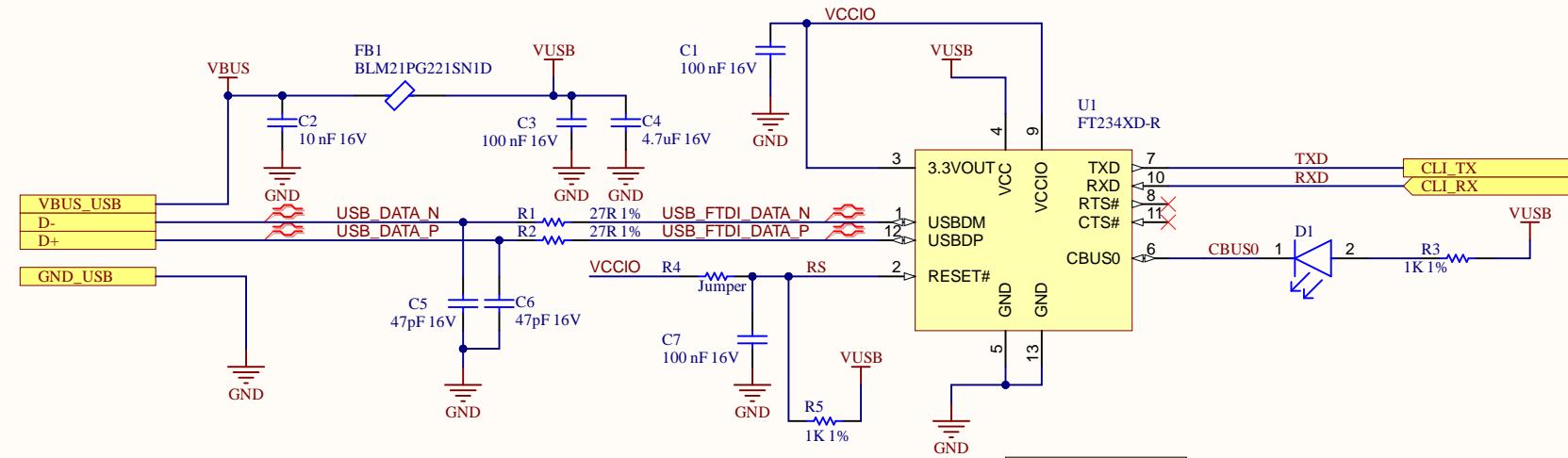
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## FTDI CHIP



## USB ESD PROTECTION



APPROVALS	DATE	PROJECT	Penn Engineering
ENG:	*	DOCUMENT REVISION:	*
DSN:	*	DESIGN ITEM:	*
CHK:	*	TITLE	
REFERENCE DOCUMENTS		USB Slot + FTDI	
BOM:			
ASSY DWG:			
FAB DWG:			
PCB DWG:	SIZE	CAGE CODE	DWG NO.
	B		
SCALE:	FILE NAME		REV
	FTDI.SchDoc		1.0
FILE NAME	SHEET		1 OF 1

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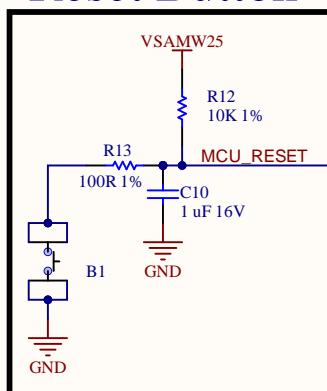
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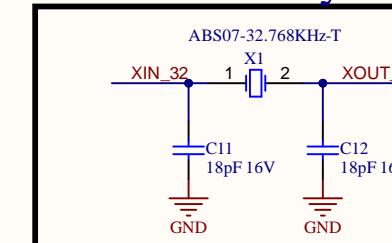
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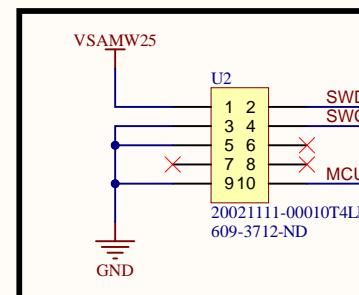
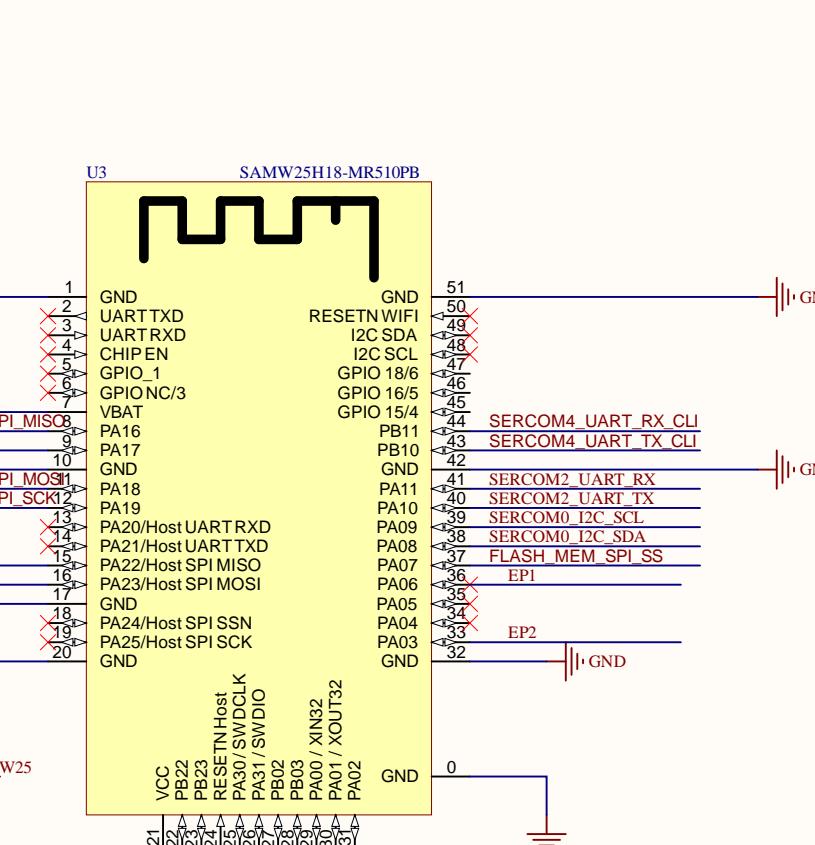
## Reset Button



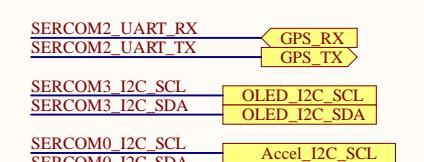
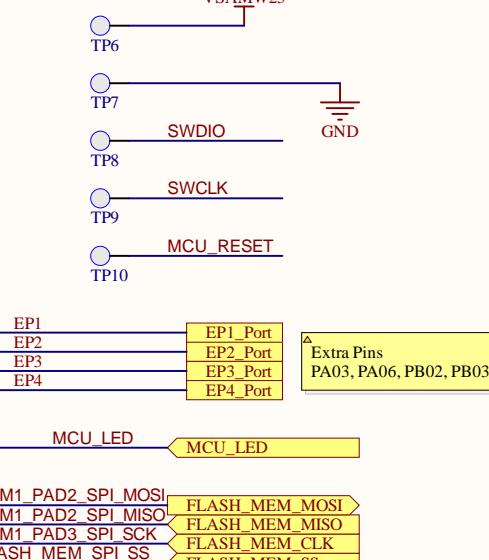
## 32.768 Crystal



Calculation of crystal load capacitors:  
 $C_{ext} = 2 \times (C_{crystal} - C_{para} - C_{pcb})$   
 Ccrystal = 12.5pF (from crystal - Cdatasheet)  
 Cpara = 3.15pF (from MCU datasheet)  
 Cpcb = 0.5pF (estimate)  
 $C_{ext} = 2 \times (12.5pF - 3.15pF - 0.5pF) = 17.7pF$



## DEBUGGER PORT



APPROVALS	DATE	PROJECT	Penn Engineering
ENG:	*	DOCUMENT REVISION:	UNIVERSITY OF PENNSYLVANIA
DSN:	*	DESIGN ITEM:	
CHK:	*	MCU Module	
REFERENCE DOCUMENTS			
BOM:			
ASSY DWG:			
FAB DWG:			
PCB DWG:			
SCALE:	FILE NAME	MCU.SchDoc	REV
			1.0

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DWG. NO.	1.0.1	REV. SHT.	
REVISION	DESCRIPTION	DATE	APPROVED

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## TESTPOINTS

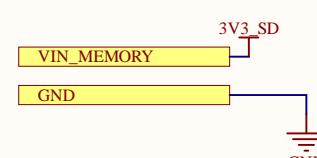
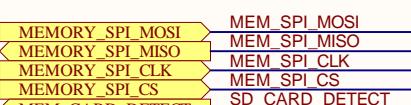
TP11	3V3_SD
TP12	SD_CARD_DETECT
TP13	MEM_SPI_MOSI
TP14	MEM_SPI_MISO
TP15	MEM_SPI_CLK
TP16	MEM_SPI_CS

B

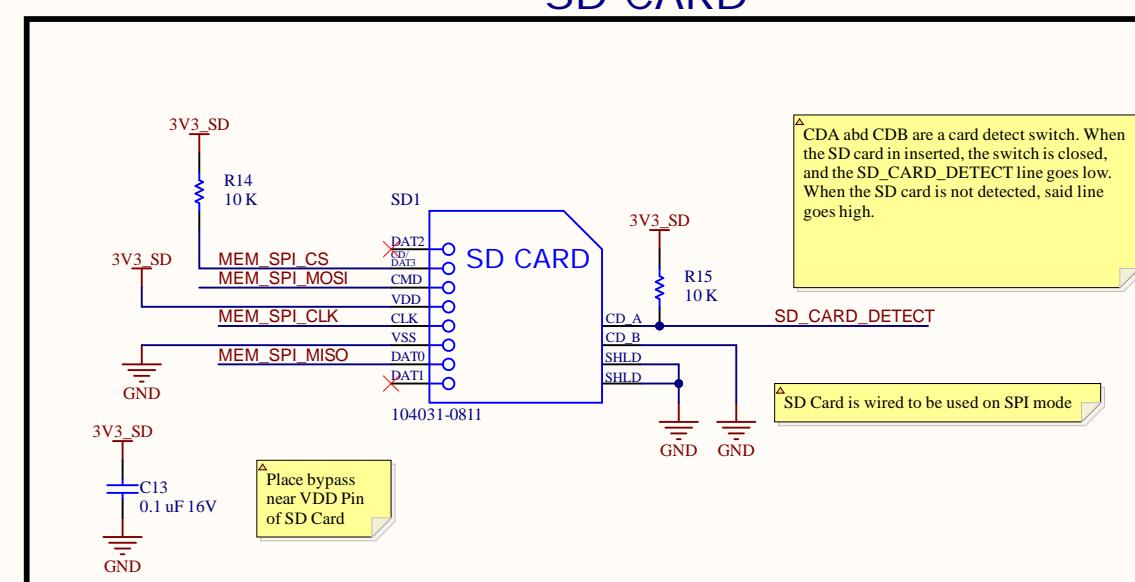
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## SD CARD

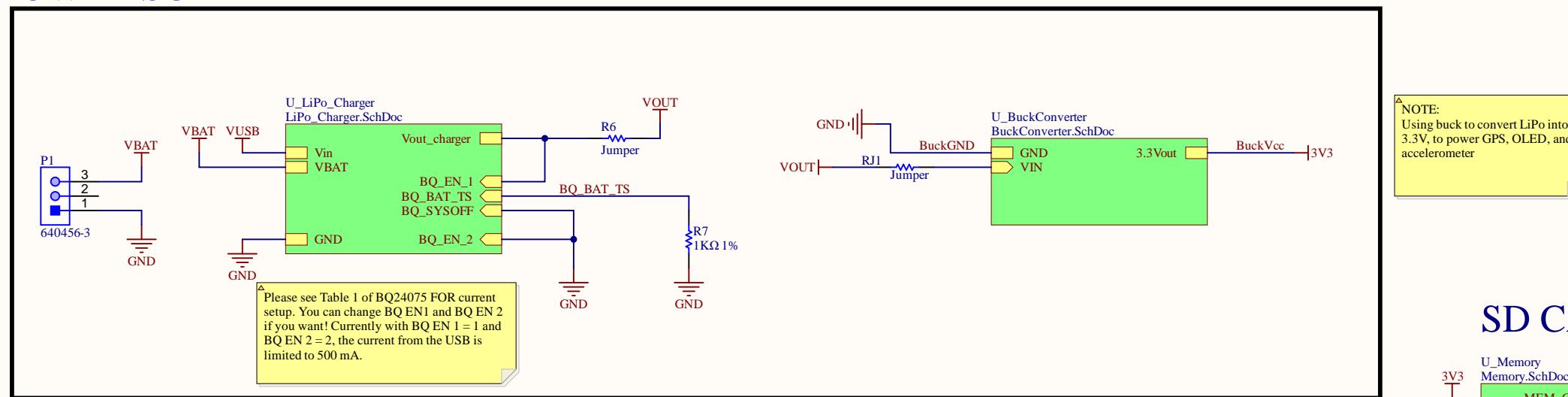


APPROVALS	DATE	PROJECT	Penn Engineering	*
ENG: Weihsio Huang				*
DSN: *		PROJECT REVISION:	DOCUMENT REVISION:	DESIGN ITEM:
CHK: *				
REFERENCE DOCUMENTS		TITLE	SD Card Module	
BOM:				
ASSY DWG:		SIZE	CAGE CODE	DWG NO.
FAB DWG:				
PCB DWG:		REV		1.0
SCALE:	FILE NAME	Memory.SchDoc	SHEET	1 OF 1

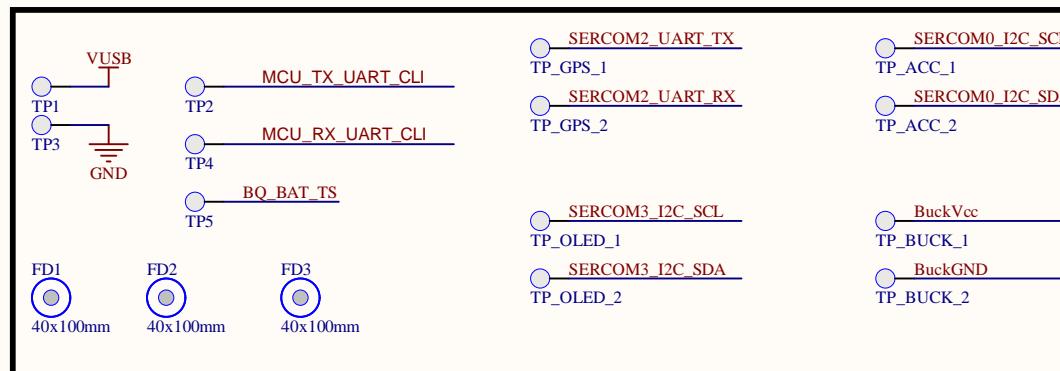
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DWG. NO.	REV.	SHT.	REVISION	DESCRIPTION	DATE	APPROVED
1.0.1						

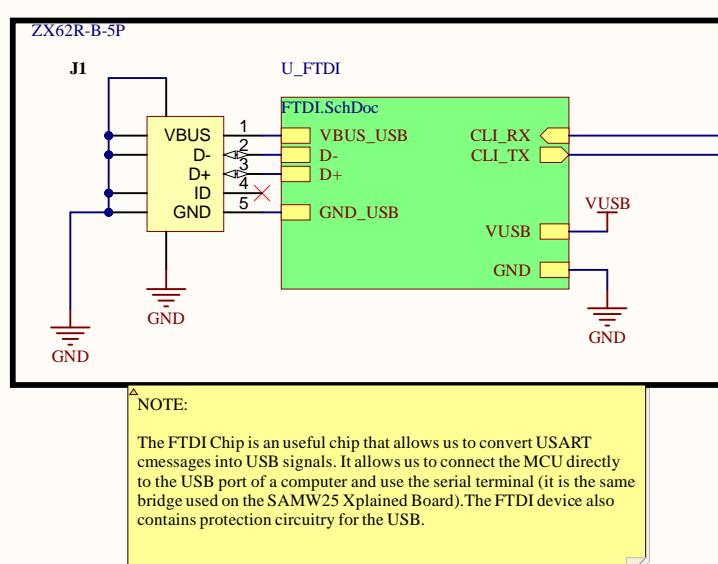
## F POWER SUPPLY



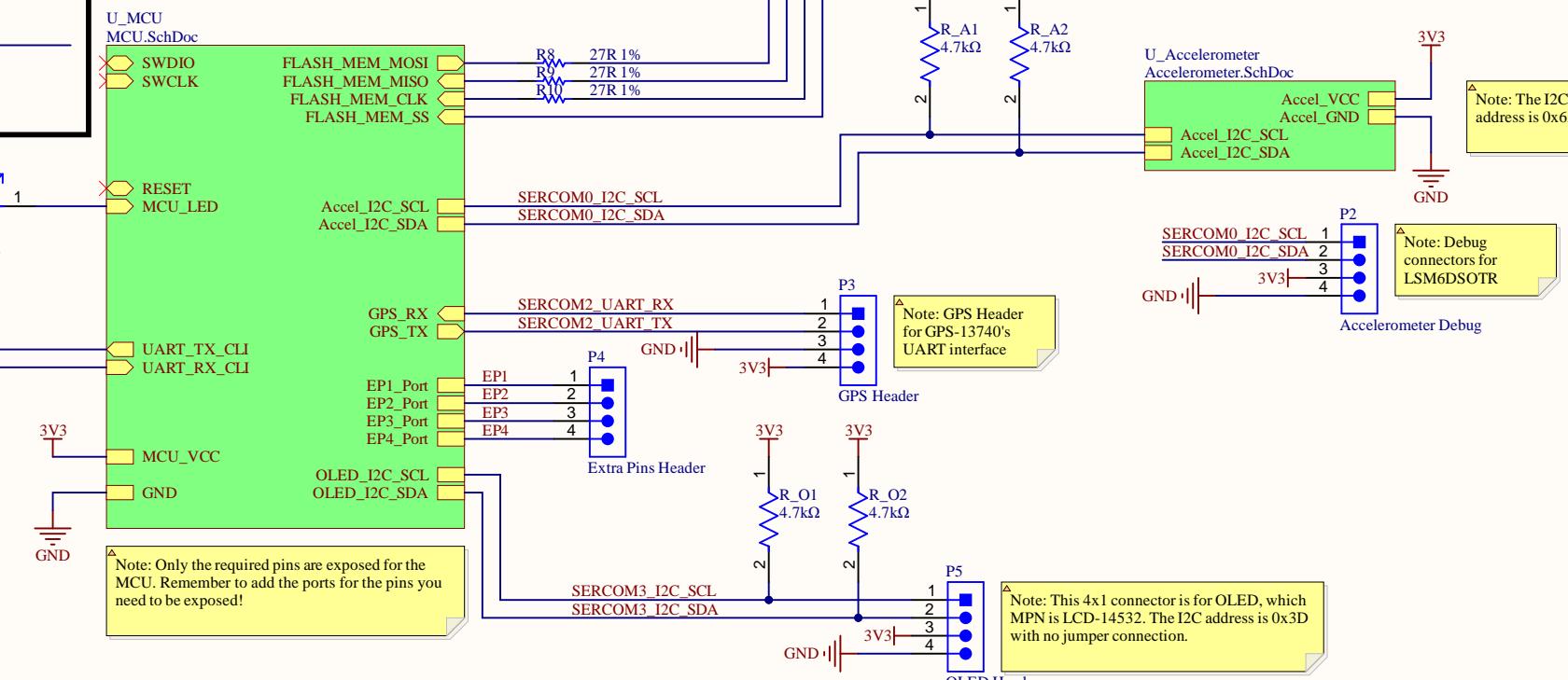
## D TEST POINTS AND FIDUCIALS



## B USB CONNECTOR + FTDI



## C MCU



APPROVALS		DATE	PROJECT	Penn Engineering
ENG:	Weihao Huang			UNIVERSITY OF PENNSYLVANIA
DSN:	*			*
CHK:	*			*
REFERENCE DOCUMENTS			DOCUMENT REVISION:	DESIGN ITEM:
BOM:				Mainboard
ASSY DWG:				
FAB DWG:				
PCB DWG:				
SCALE:			FILE NAME	MAIN.SchDoc
			SHEET	1 OF 1

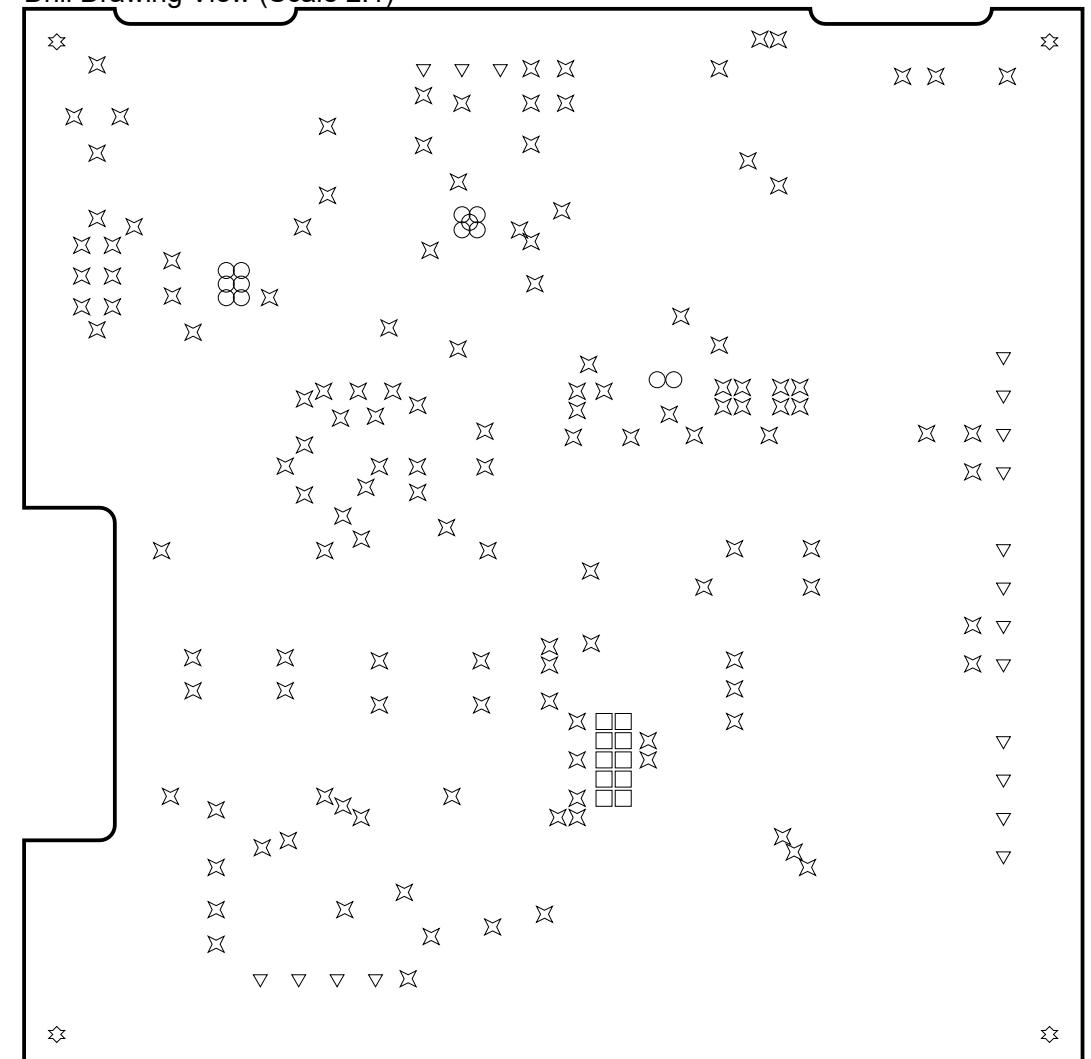
IoTracking\_Main



REV STATUS OF SHEETS	REV					DWG NO.: <input type="text" value="=DOC_NO_ASSY_DWG"/> .lfe	REV:
SHEET							

REVISIONS	
DESCRIPTION	DATE

1 Drill Drawing View (Scale 2:1)



2 Drill Table

Symbol	Count	Hole Size	Plated	Hole Tolerance
○	13	0.20mm	Plated	
✗	137	0.20mm	Plated	
□	10	0.65mm	Plated	
▽	19	1.27mm	Plated	
☆	4	2.70mm	Plated	
183 Total				

4

APPROVALS DATE

ENGINEER: Weihao Huang	2/26/2022
DESIGNER: Weihao Huang	2/26/2022
CHECKER: Weihao Huang	2/26/2022
Reference Documents	
THIRD ANGLE PROJECTION	
NEXT ASSY	USED ON
APPLICATION	

DESIGN ITEM: PCB\_Board DESIGN ITEM REVISION: 1.0

DESIGN ITEM: IoTracking

SIZE: B DWG NO: REV:

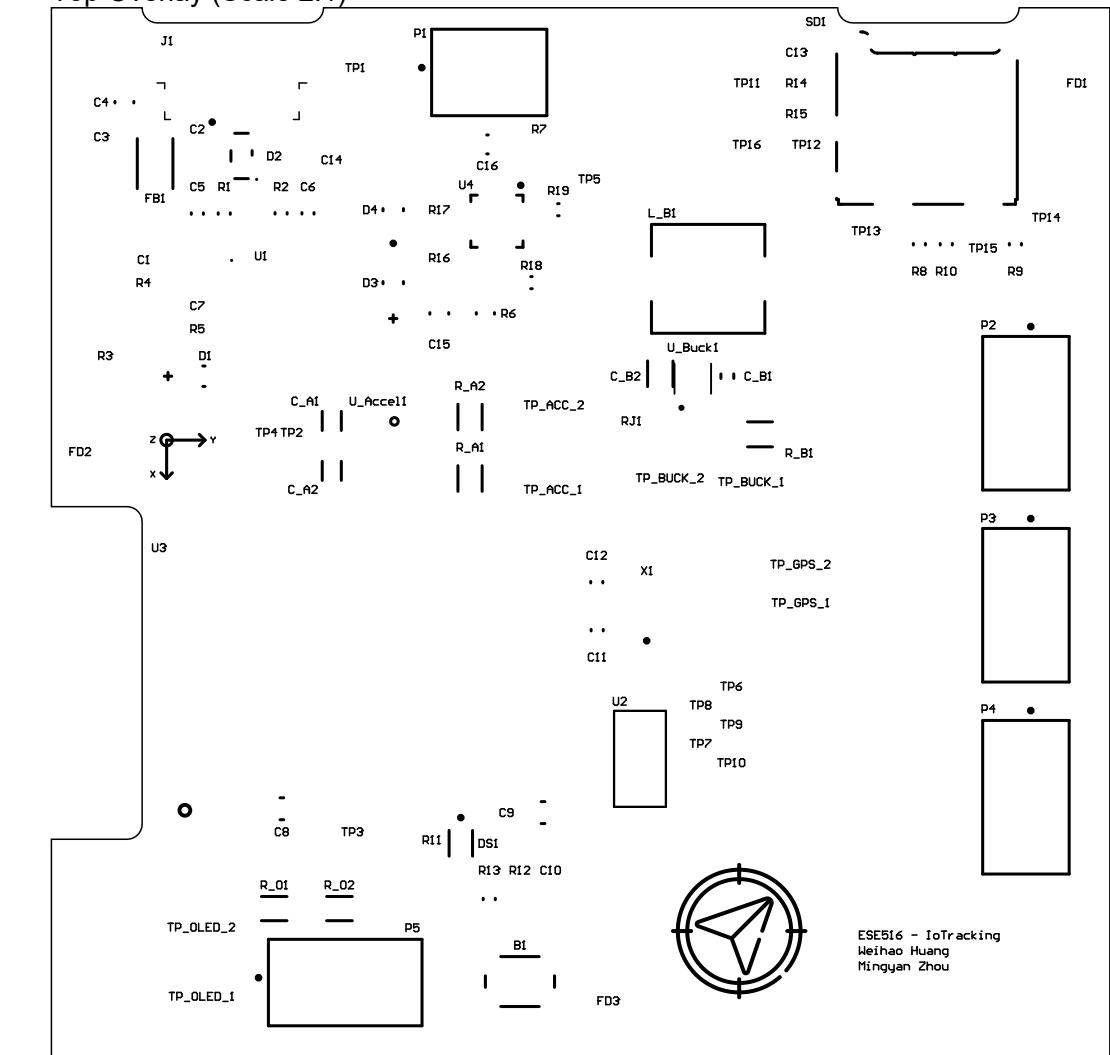
IoTracking Board Fabrication.PCBDw FILE NAME: SHEET: 2 OF 12

Altium™

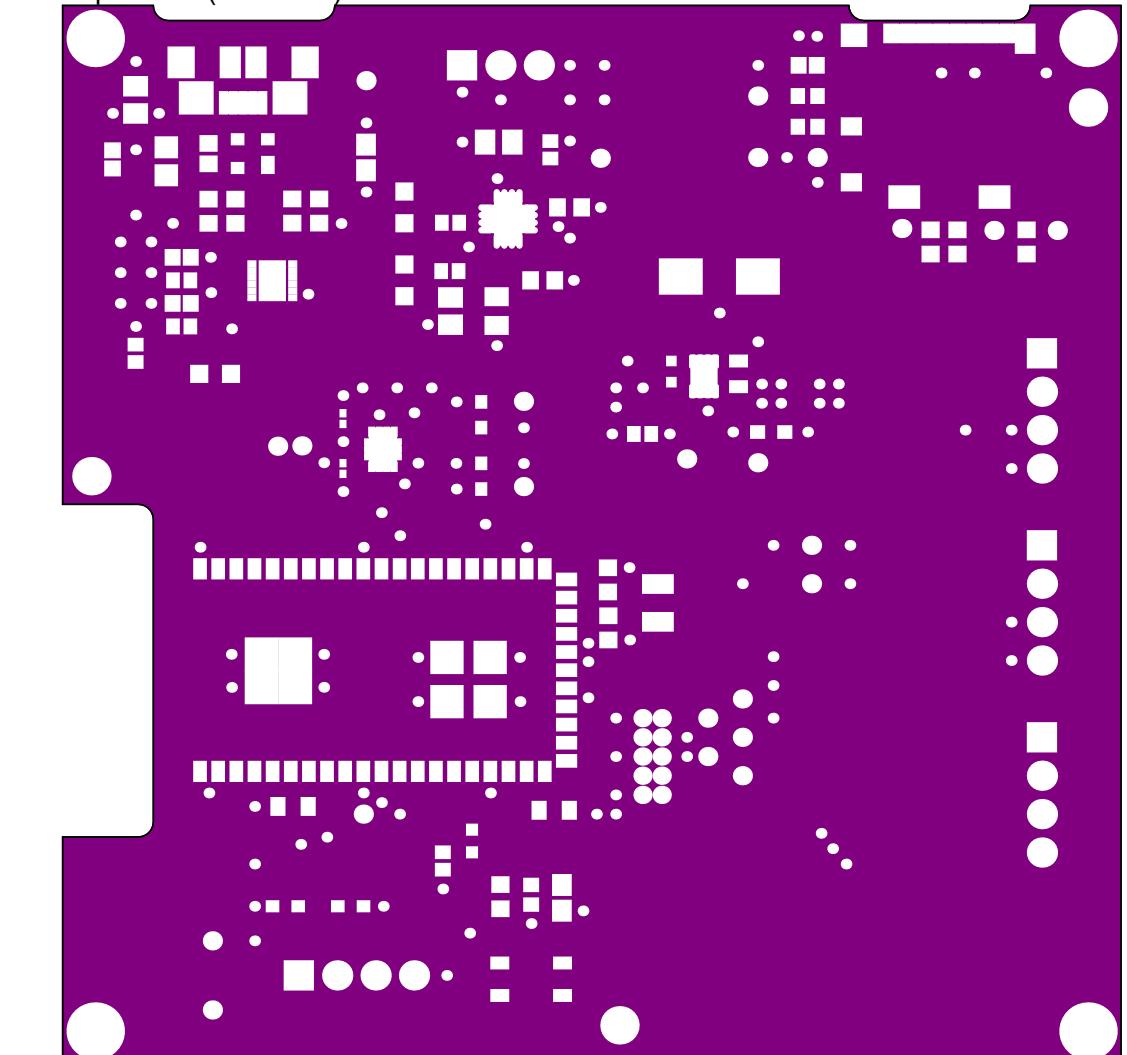
REV STATUS OF SHEETS		REV							
SHEET									

REVISIONS		DESCRIPTION	DATE	APPROVED

Top Overlay (Scale 2:1)



Top Solder (Scale 2:



.It

 	APPROVALS		DATE
	ENGINEER:	Weihao Huang	2/26/2022
	DESIGNER:	Weihao Huang	2/26/2022
	CHECKER:	Weihao Huang	2/26/2022
	Reference Documents		
NEXT ASSY	USED ON		
APPLICATION			


  
 DESIGN ITEM: PCB Board DESIGN ITEM REVISION: 1.0
   
 TITLE: IoTTracking
   
 SIZE: B DWG NO:
   
 REV:
   
 SCALE: FILE NAME: IoTTracking\_Board\_Fabrication.PCBDw SHEET: 4 OF 12

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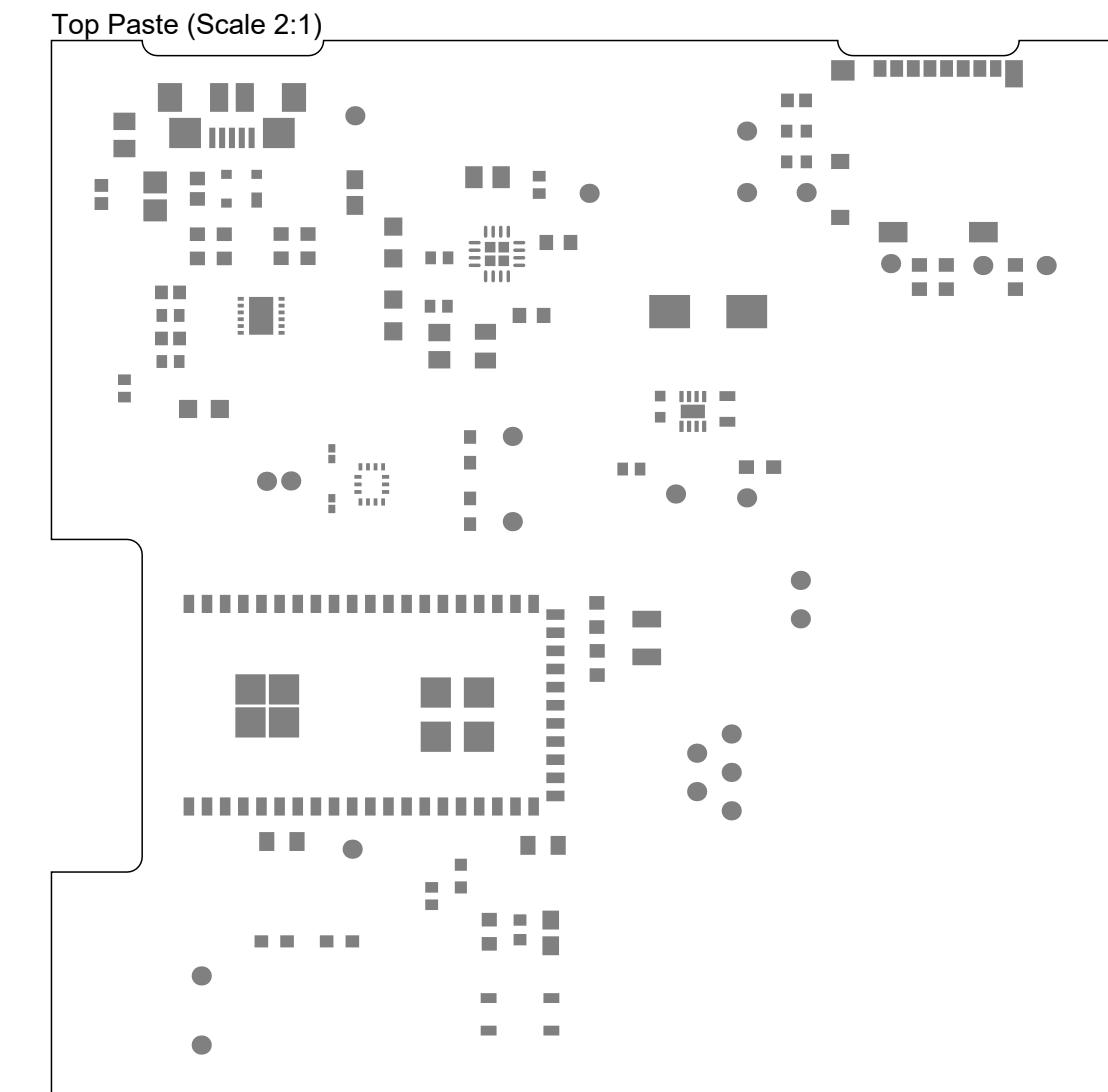
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REV STATUS OF SHEETS		REV						DWG NO: =DOC_NO_ASSY_DWG	REV: .lfe
SHEET									

REVISIONS		DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	<b>Altium</b> TM
ENGINEER: Weihao Huang	2/26/2022	
DESIGNER: Weihao Huang	2/26/2022	
CHECKER: Weihao Huang	2/26/2022	
DESIGN ITEM: PCB_Board	DESIGN ITEM REVISION: 1.0	
TITLE: IoTracking		
SIZE: B	DWG NO:	
REV:		
NEXT ASSY	USED ON	
APPLICATION		
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		Sheet: 5 OF 12



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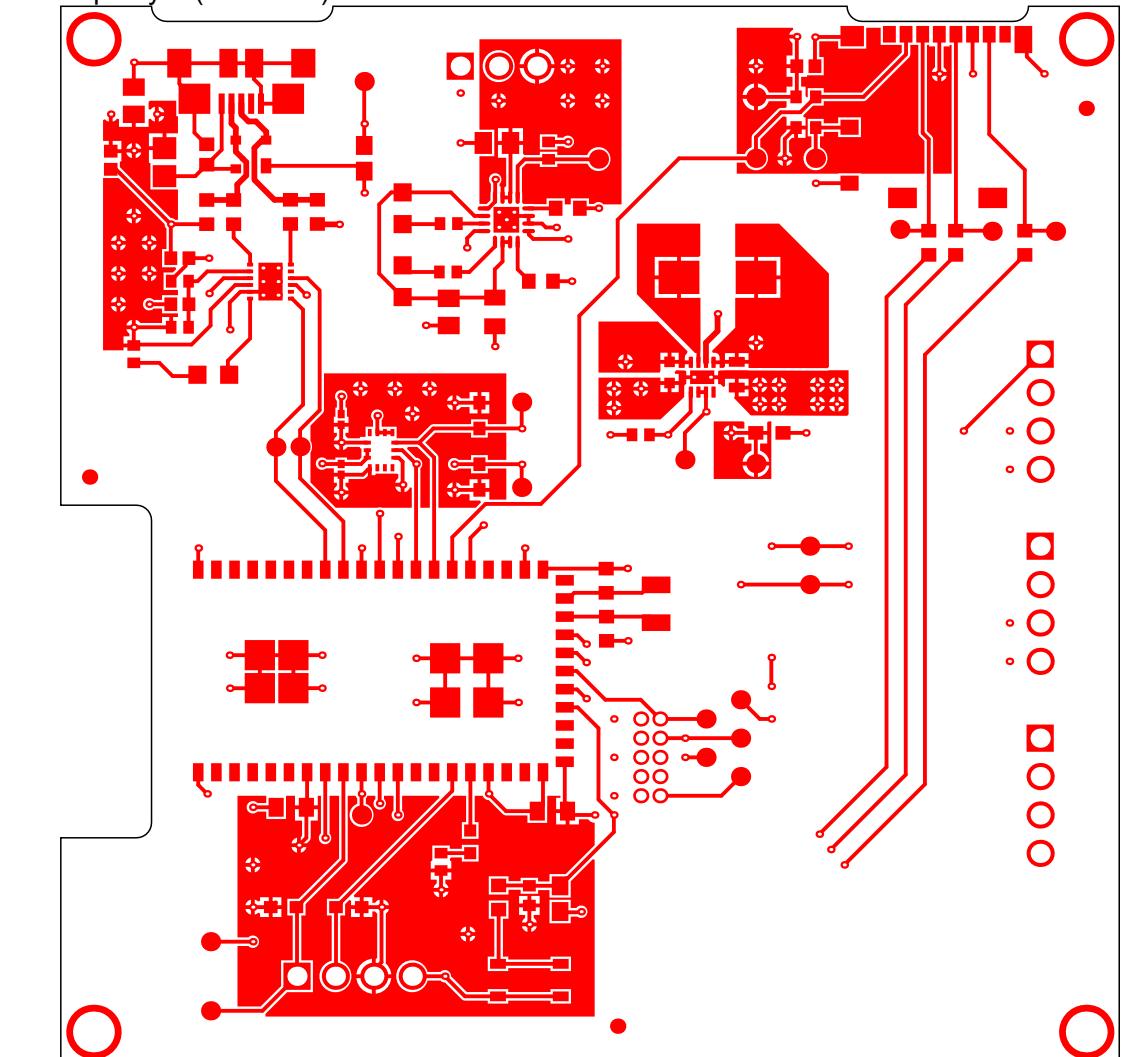
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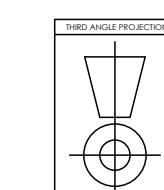
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SHEET								

REVISIONS		DESCRIPTION	DATE	APPROVED

Top Layer (Scale 2:1)



APPROVALS	DATE	<b>Altium</b> TM
ENGINEER: Weihao Huang	2/26/2022	
DESIGNER: Weihao Huang	2/26/2022	
CHECKER: Weihao Huang	2/26/2022	
DESIGN ITEM: PCB_Board	DESIGN ITEM REVISION: 1.0	
TITLE: IoTracking		
SIZE: B	DWG NO:	
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SCALE: 1:1	FILE NAME: IoTracking_Board_Fabrication.PCBDw	
REV: .lfe	FILE NAME: IoTracking_Board_Fabrication.PCBDw	
Sheet: 6 of 12		



THIRD ANGLE PROJECTION

NEXT ASSY

USED ON

APPLICATION

REFERENCE DOCUMENTS

CHECKER

DESIGNER

ENGINEER

TITLE

SIZE

DATE

APPROVALS

REV

DWG NO

FILE NAME

SCALE

ZONE

REV

REVISIONS

DWG NO

FILE NAME

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REV

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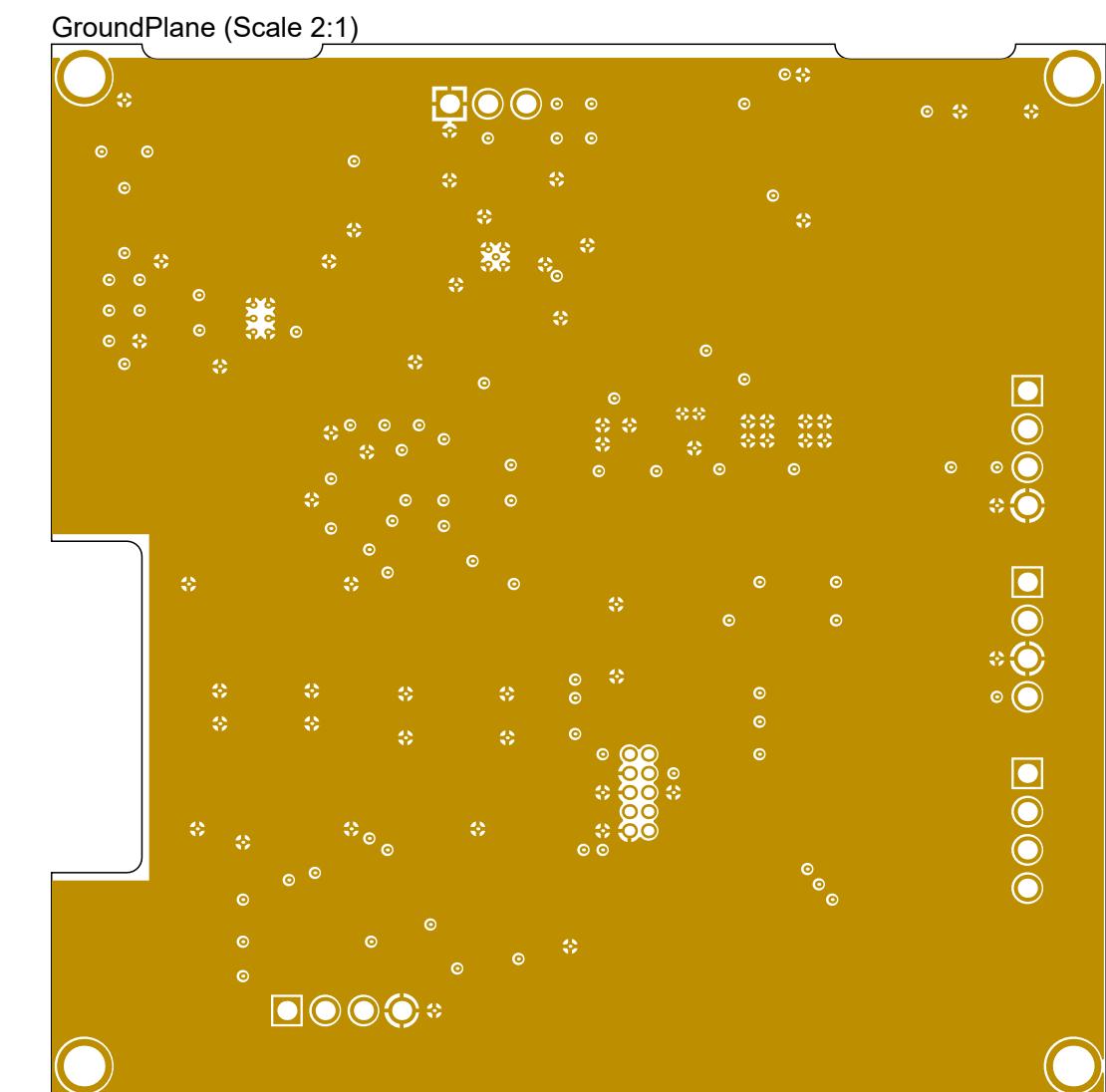
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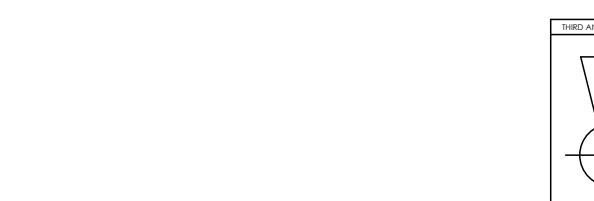
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SHEET									

REVISIONS		DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	<b>Altium</b> PCB Board
ENGINEER:	Weihao Huang	
DESIGNER:	Weihao Huang	
CHECKER:	Weihao Huang	
Reference Documents		DESIGN ITEM: PCB Board
		DESIGN ITEM REVISION: 1.0
		TITLE: IoTTracking
		SIZE: B
		DWG NO: .lfe
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		IoTracking_Board_Fabrication.PCBDw
SCALE:	FILE NAME:	
		Sheet: 7 of 12



NEXT ASSY	USED ON
APPLICATION	

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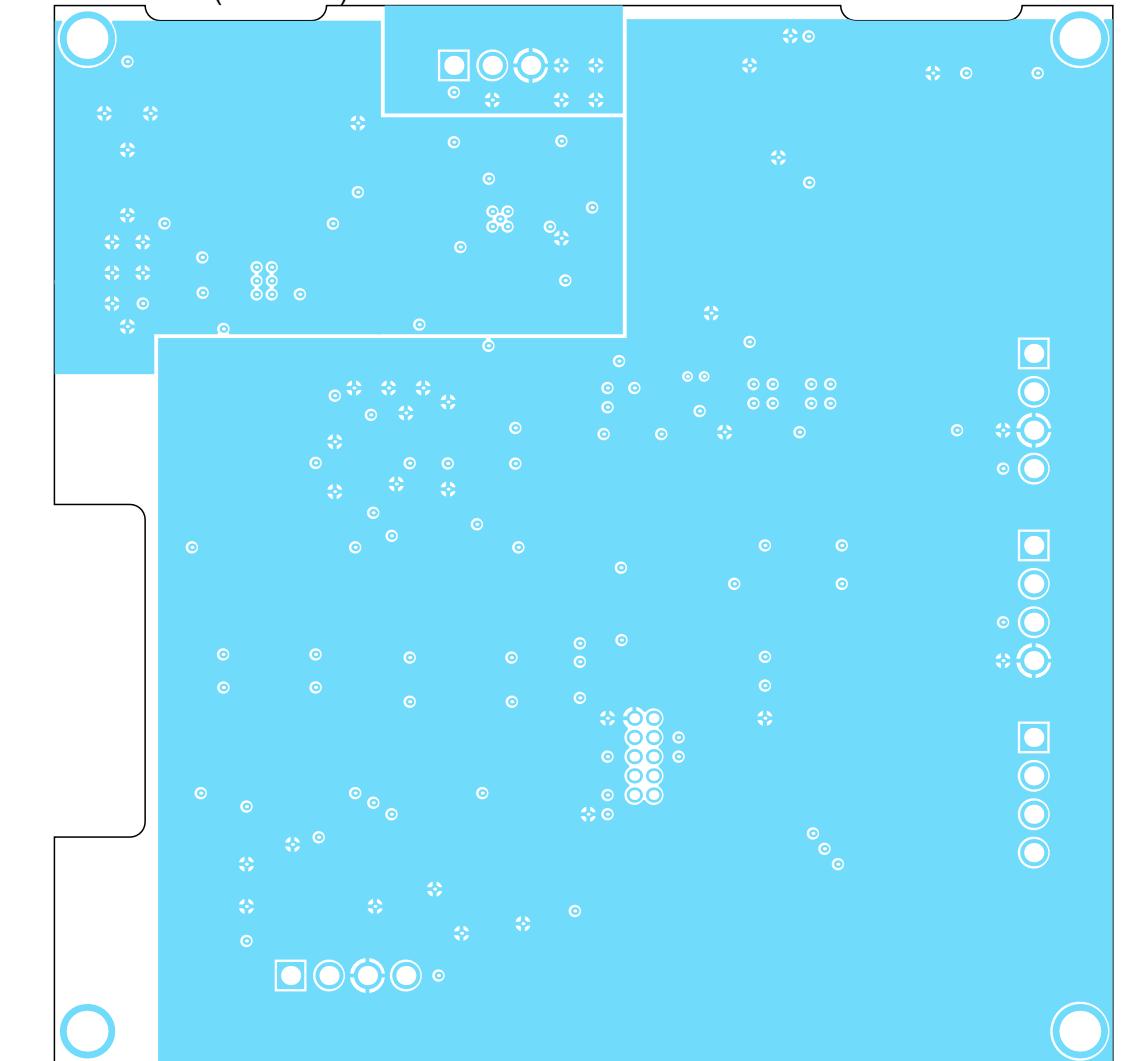
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REV STATUS OF SHEETS		REV						DWG NO: =DOC_NO_ASSY_DWG	REV: .lfe
SHEET									

REVISIONS		DESCRIPTION	DATE	APPROVED

PowerPlane (Scale 2:1)



APPROVALS	DATE	<b>Altium</b> PCB Board IoTracking
ENGINEER:	Weihao Huang	
DESIGNER:	Weihao Huang	
CHECKER:	Weihao Huang	
Reference Documents		
THIRD ANGLE PROJECTION		
NEXT ASSY	USED ON	
APPLICATION		
SIZE: B	DWG NO:	
SCALE: 1.0	FILE NAME: IoTracking_Board_Fabrication.PCBDw	REV: 1.0
Sheet: 8 of 12		

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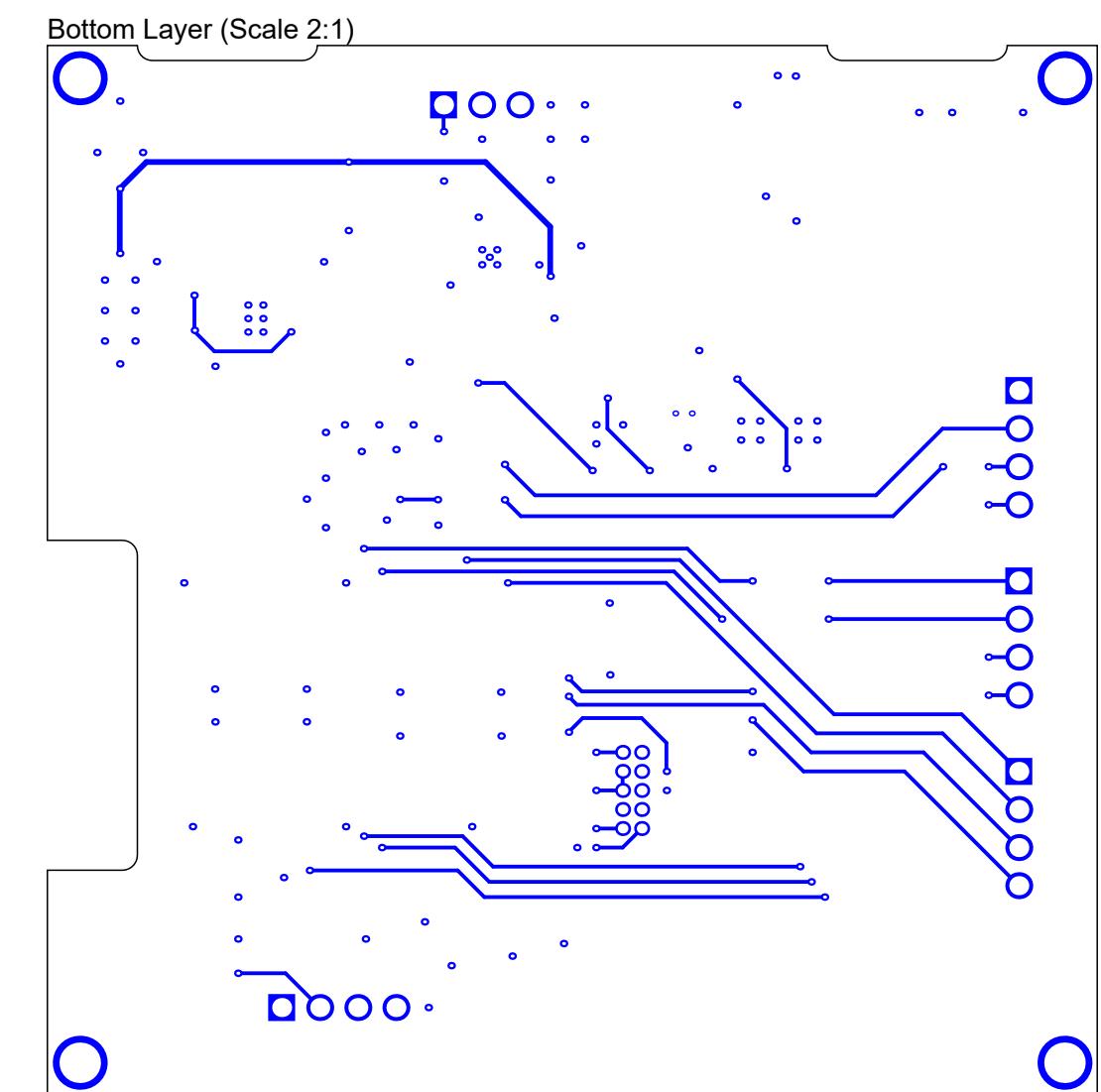
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REV STATUS OF SHEETS		REV					DWG NO. =DOC_NO_ASSY_DWG	REV .lfe
SHEET								

REVISIONS		DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	<b>Altium</b> TM
ENGINEER: Weihao Huang	2/26/2022	
DESIGNER: Weihao Huang	2/26/2022	
CHECKER: Weihao Huang	2/26/2022	
Reference Documents		
DESIGN ITEM: PCB_Board DESIGN ITEM REVISION: 1.0		
TITLE: IoTTracking		
SIZE: B	DWG NO:	
IoTracking_Board_Fabrication.PCBDw		
SCALE:	FILE NAME:	
REV: 9 OF 12		

THIRD ANGLE PROJECTION

NEXT ASSY USED ON APPLICATION

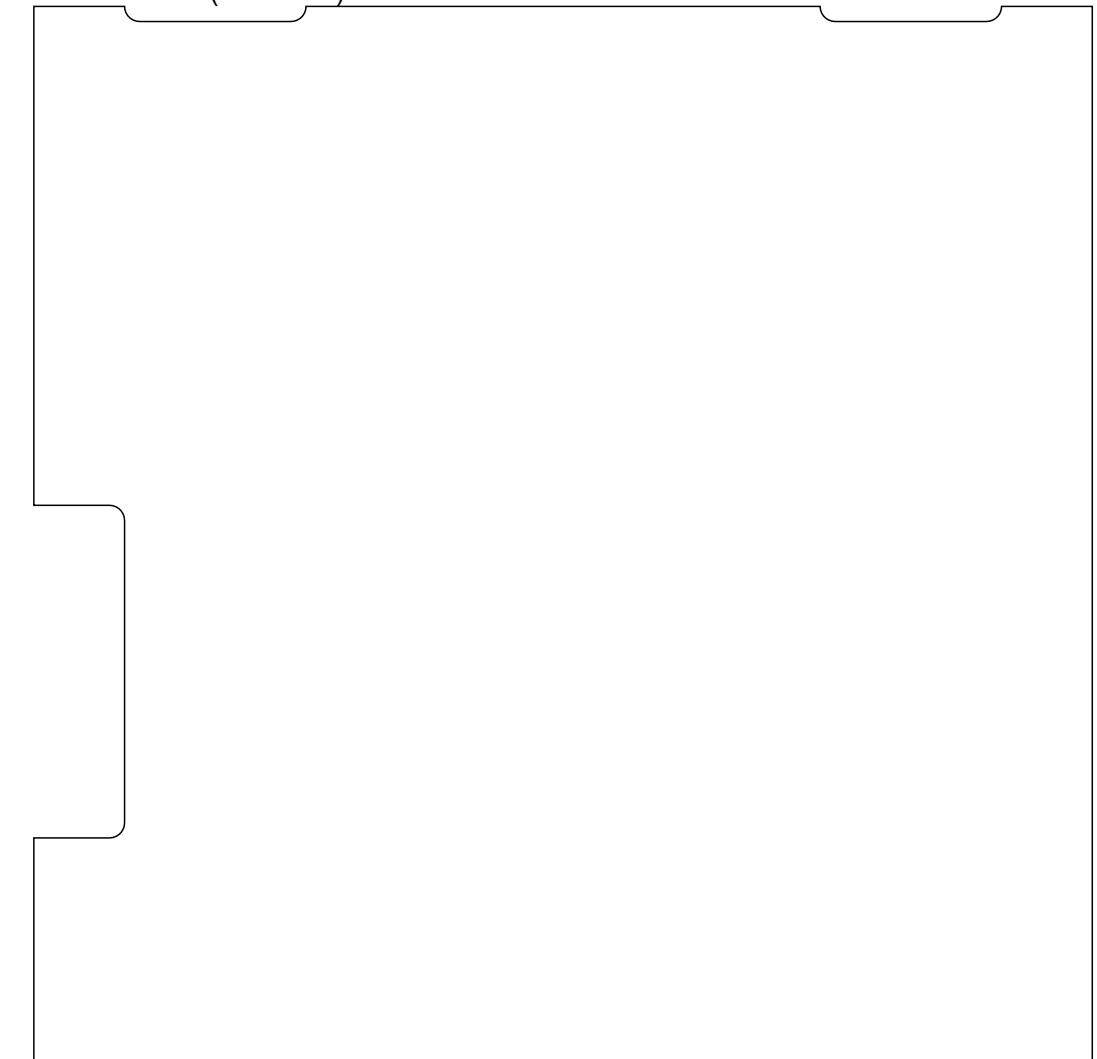
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Bottom Paste (Scale 2:1)



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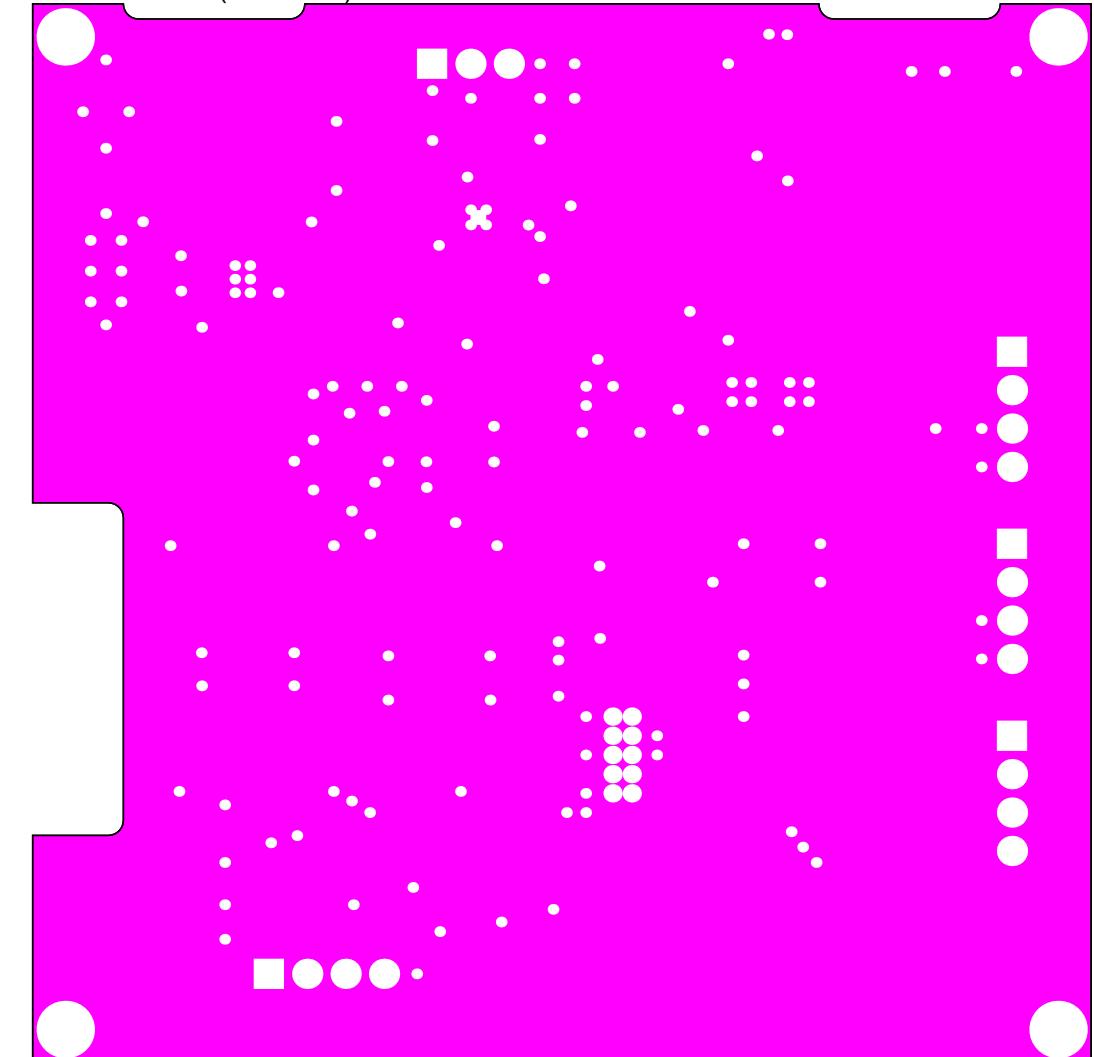
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APPROVALS		DATE			
ENGINEER:	Weihao Huang	2/26/2022			
DESIGNER:	Weihao Huang	2/26/2022			
CHECKER:	Weihao Huang	2/26/2022	DESIGN ITEM:	PCB_Board	DESIGN ITEM REVISION:
			TITLE:	IoTracking	
			SIZE:	DWG NO:	REV:
		B	IoTracking_Board_Fabrication.PCBDw		
N		SCALE:	FILE NAME:	SHEET:	10 OF 12

Bottom Solder (Scale 2:1)



11

THIRD ANGLE PROJECTION:	APPROVALS		DATE
	ENGINEER:	Weihao Huang	2/26/2022
	DESIGNER:	Weihao Huang	2/26/2022
	CHECKER:	Weihao Huang	2/26/2022
		Reference Documents	
NEXT ASSY	USED ON	APPLICATION	
APPLICATION			

**Altium**™

DESIGN ITEM: PCB\_Board DESIGN ITEM REVISION: 1.0

TITLE: IoTTracking

SIZE: B DWG NO:

SCALE: FILE NAME: IoTTracking\_Board\_Fabrication.PCBDw SHEET: 11 OF 12



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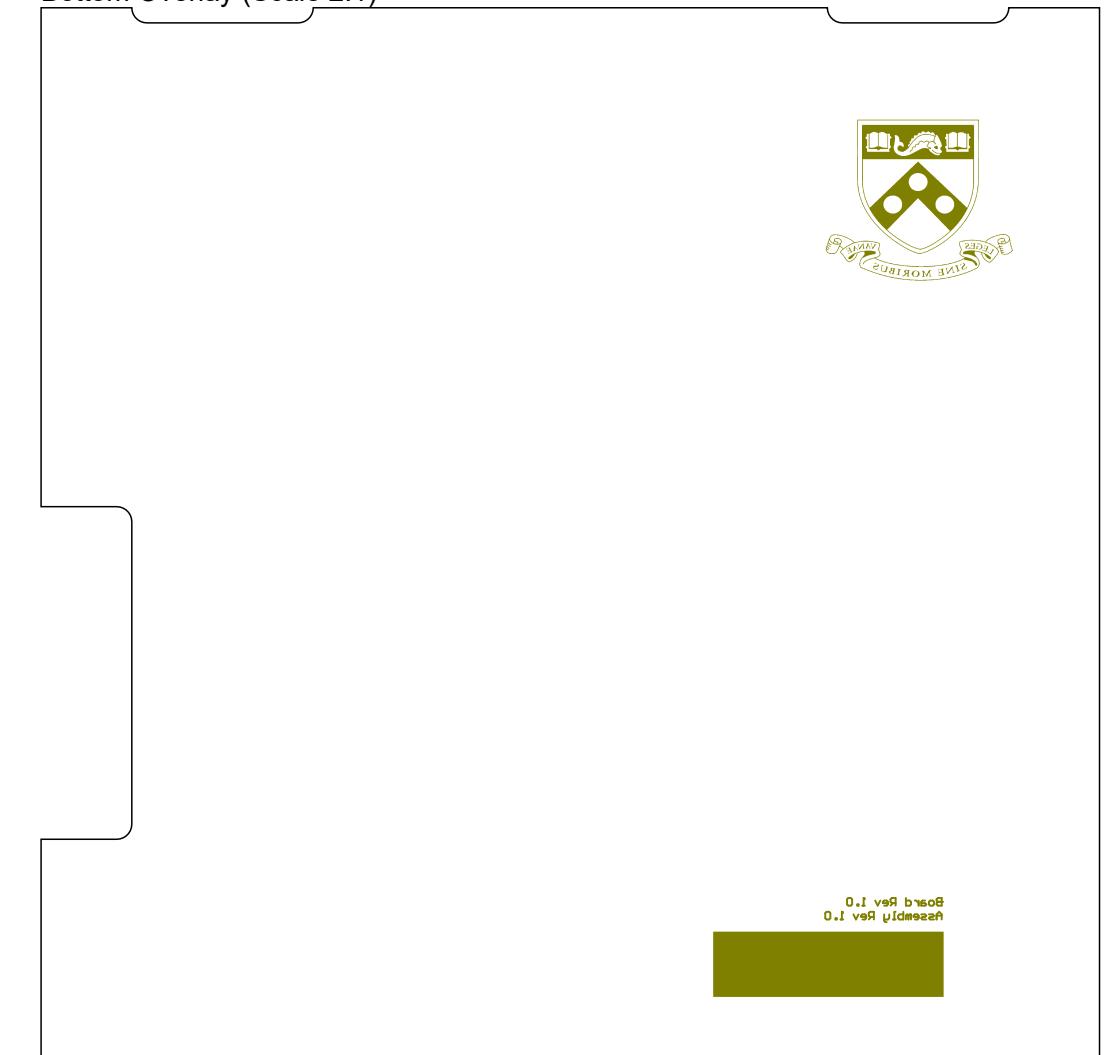
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REV STATUS OF SHEETS		REV							DWG NO: =DOC_NO_ASSY_DWG	REV: .lfe
SHEET										

REVISIONS		DESCRIPTION	DATE	APPROVED

Bottom Overlay (Scale 2:1)



APPROVALS	DATE	<b>Altium</b> TM	
ENGINEER:	Weihao Huang	2/26/2022	
DESIGNER:	Weihao Huang	2/26/2022	
CHECKER:	Weihao Huang	2/26/2022	DESIGN ITEM: PCB_Board DESIGN ITEM REVISION: 1.0
Reference Documents			
THIRD ANGLE PROJECTION			TITLE: IoTracking
			SIZE: B DWG NO:
			REV:
NEXT ASSY	USED ON		
APPLICATION			IoTracking_Board_Fabrication.PCBDw
SCALE:	FILE NAME:		Sheet: 12 of 12

REV STATUS OF SHEETS	REV					DWG NO.: =DOC_NO_ASSY_DWG	REV: .lfe
SHEET							

REVISIONS	
DESCRIPTION	DATE

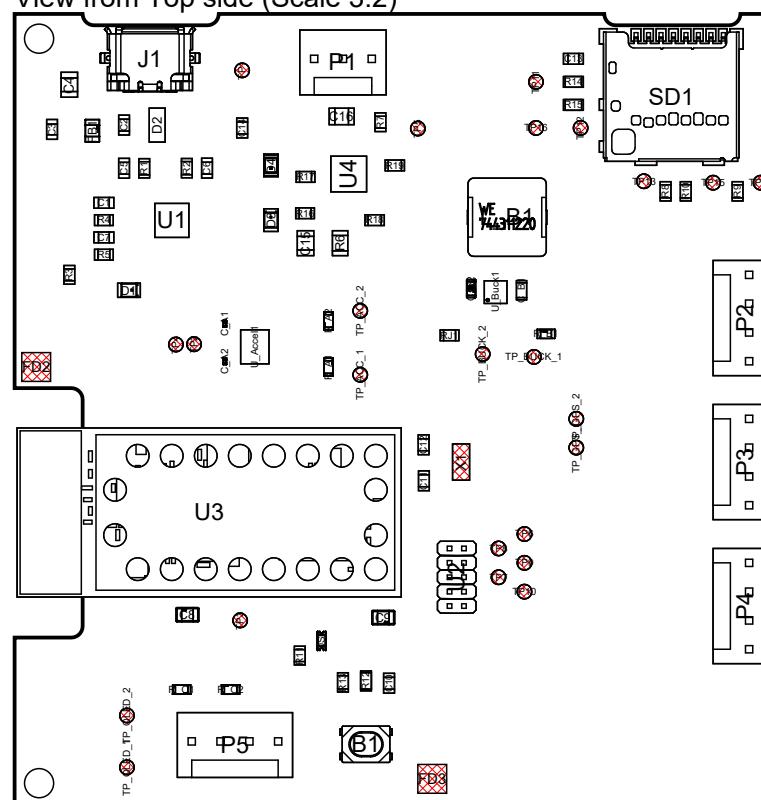
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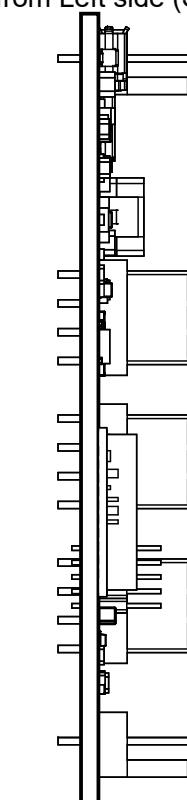
## NOTES (unless otherwise specified):

1. This item is electrostatic sensitive and shall be handled accordingly

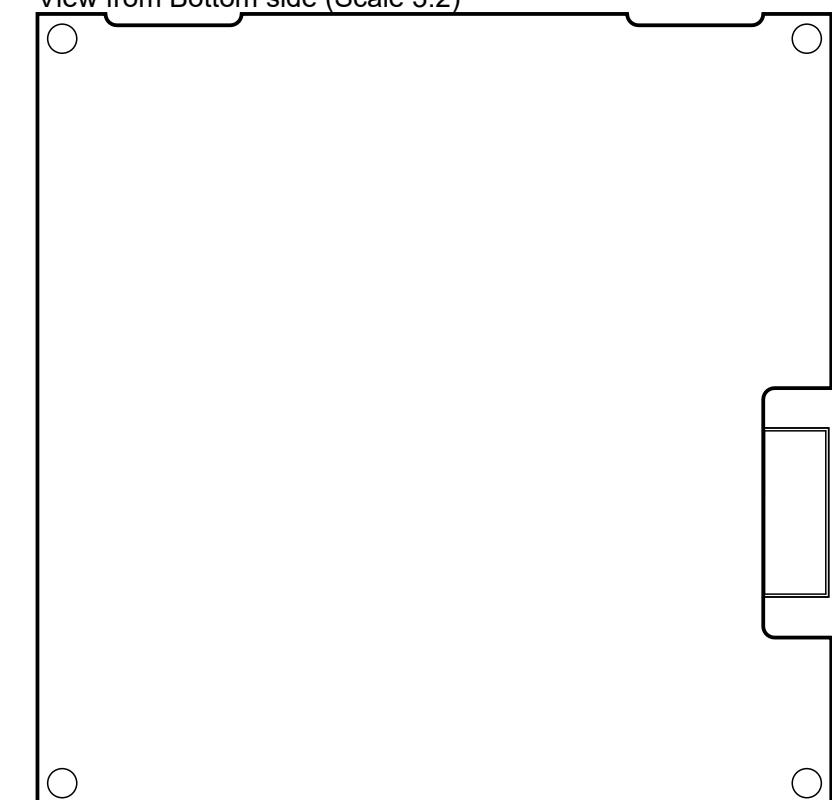
View from Top side (Scale 3:2)



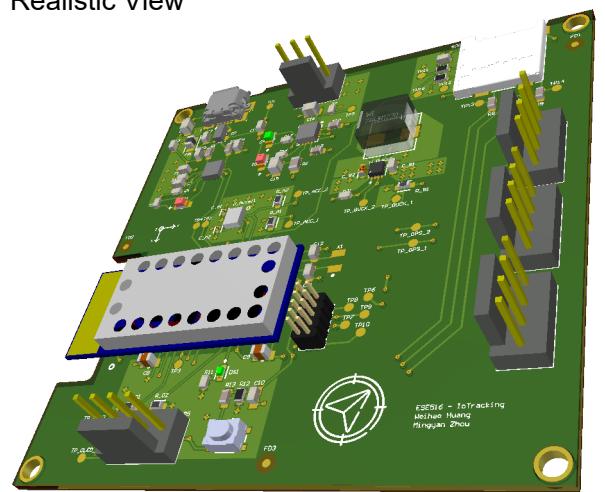
View from Left side (Scale 3:2)



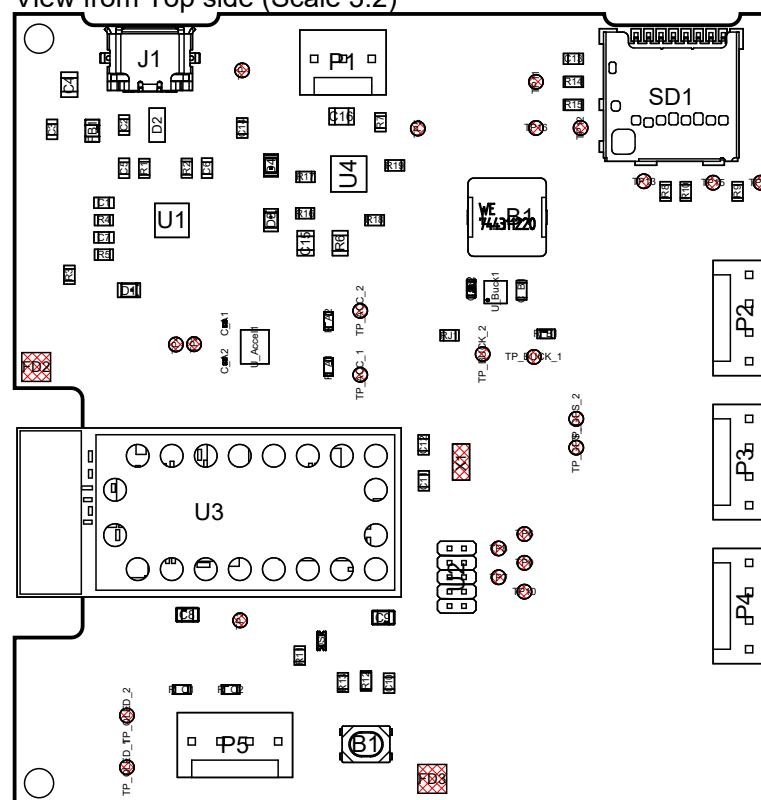
View from Bottom side (Scale 3:2)



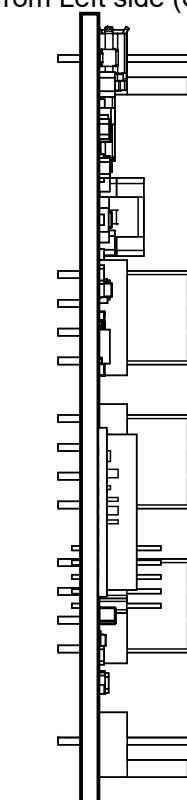
Realistic View



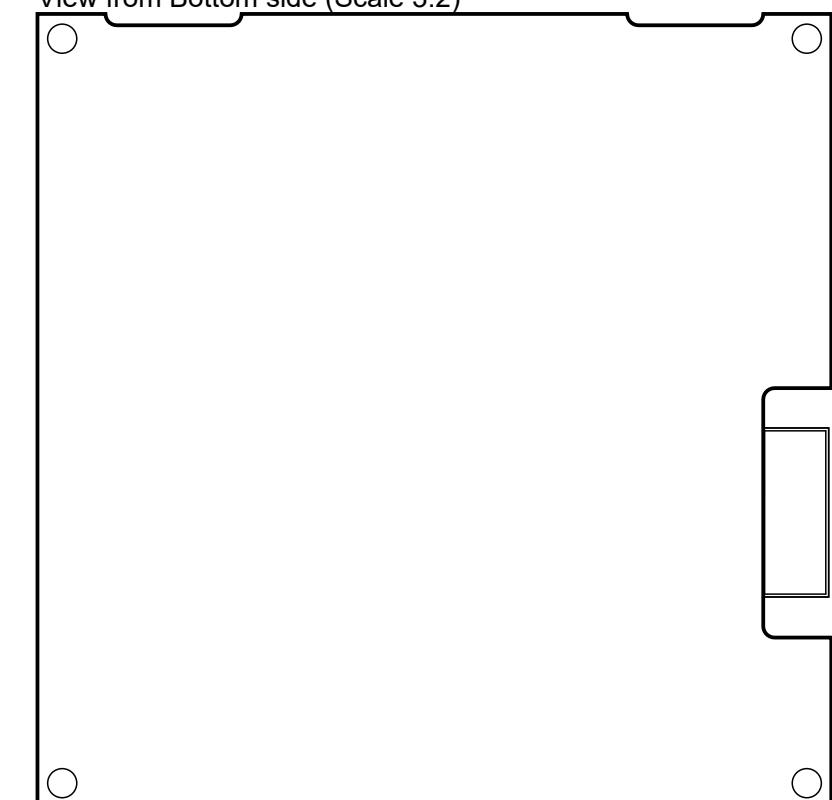
View from Top side (Scale 3:2)



View from Left side (Scale 3:2)

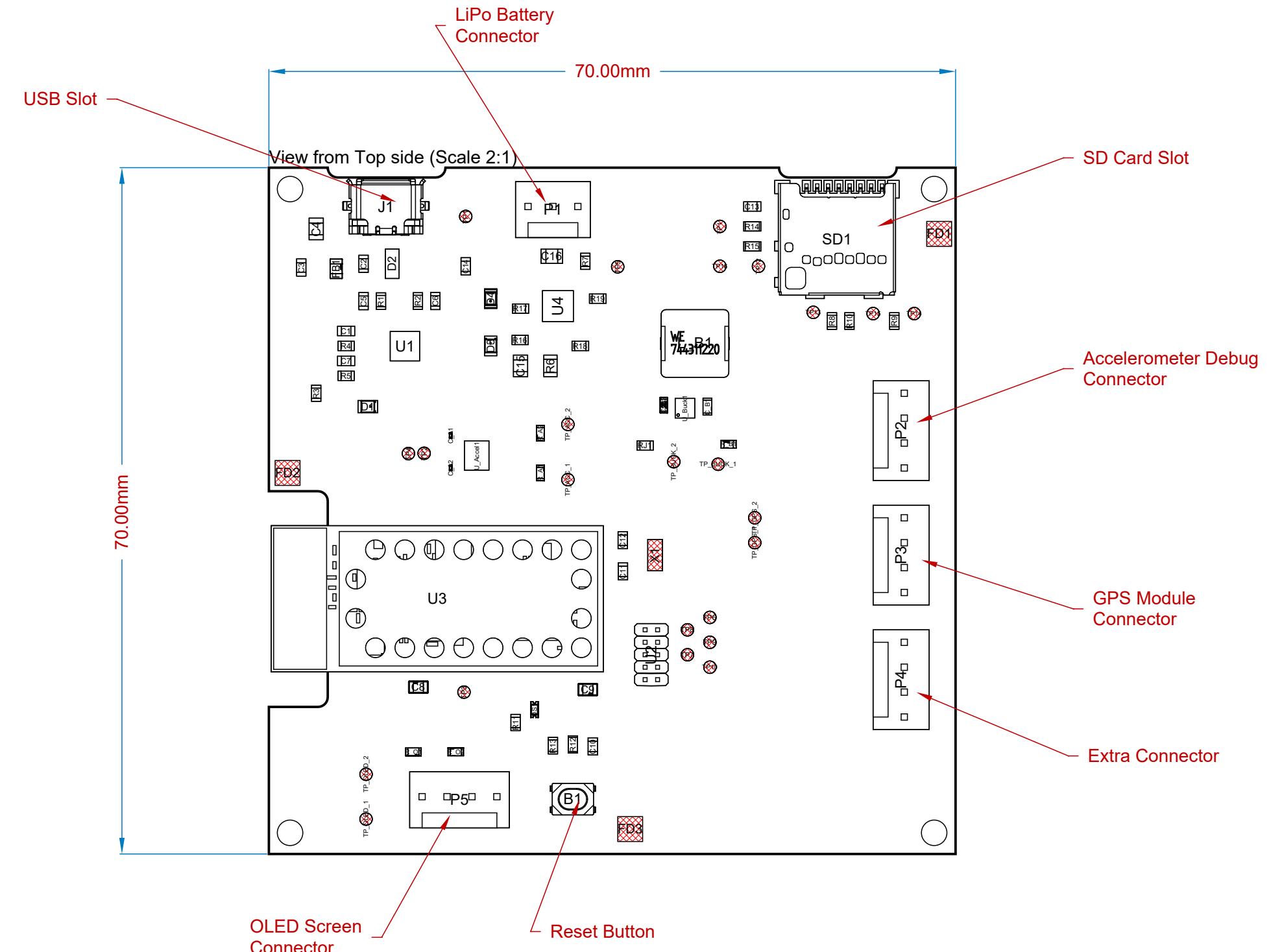


View from Bottom side (Scale 3:2)



APPROVALS	DATE
ENGINEER: Weihao Huang	2/26/2022
DESIGNER: Weihao Huang	2/26/2022
CHECKER: Weihao Huang	2/26/2022
Reference Documents	
DESIGN ITEM:	PCB Board Assembly
DESIGN ITEM REVISION:	1.0
TITLE:	IoTracking
SIZE:	B
DWG NO:	
SCALE:	
FILE NAME:	IoTracking_Board_Assembly.PCBDwf
REV:	
Sheet:	1 of 4

**Altium**



A

B

C

D

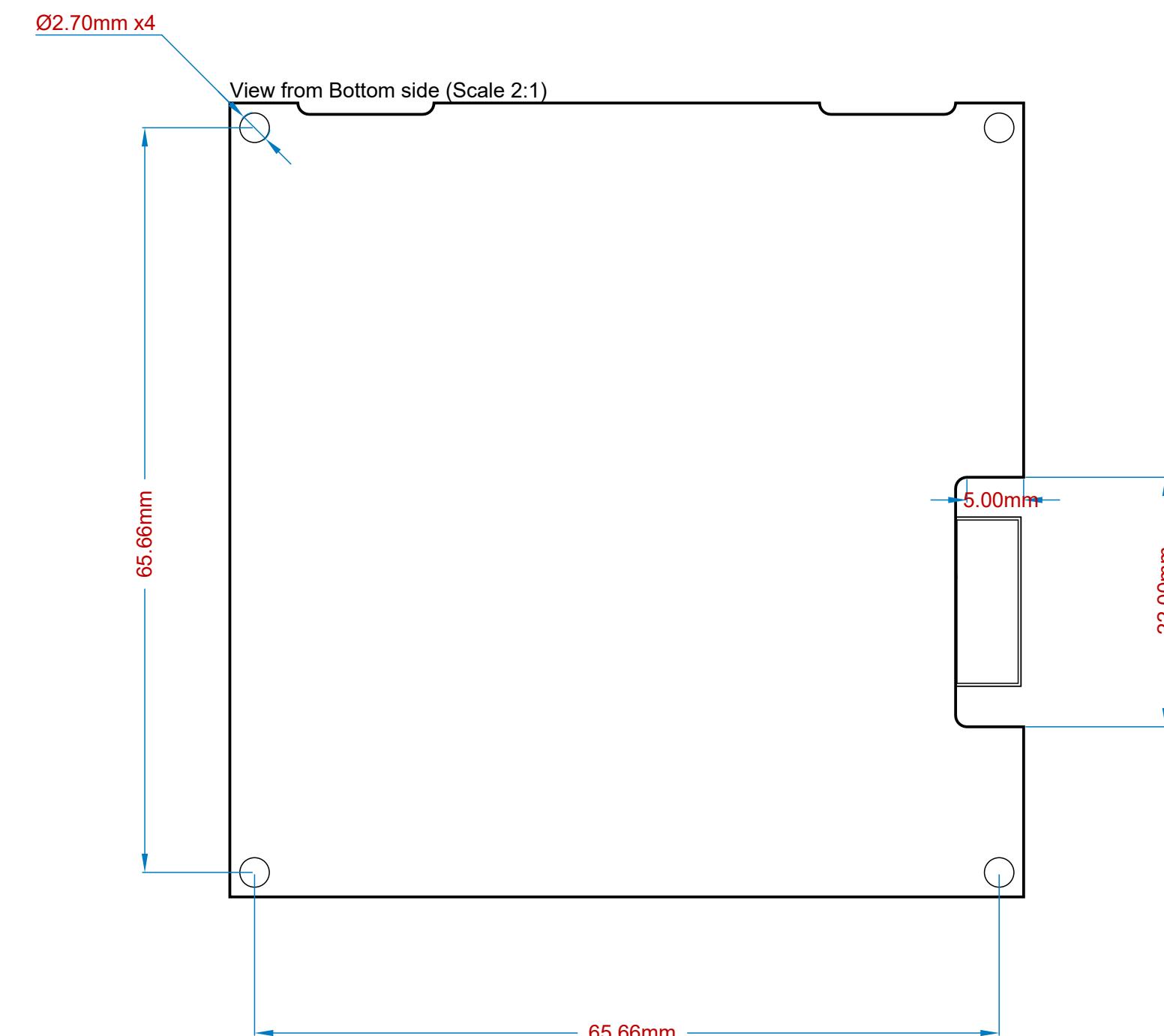
E

F

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REV STATUS OF SHEETS		REV						DWG NO: =DOC_NO_ASSY_DWG	REV: .lfe
SHEET									

REVISIONS		DESCRIPTION	DATE	APPROVED



APPROVALS	DATE	<b>Altium</b> PCB Board Assembly 1.0
ENGINEER: Weihao Huang	2/26/2022	
DESIGNER: Weihao Huang	2/26/2022	
CHECKER: Weihao Huang	2/26/2022	
Reference Documents		
THIRD ANGLE PROJECTION		
NEXT ASSY	USED ON	
APPLICATION		
SIZE: B	DWG NO:	
SCALE:	FILE NAME: IoTracking_Board_Assembly.PCBDwf	REV:

A

B

C

D

E

F

.lt

DWG NO:  
=DOC\_NO\_ASSY\_

.lfe

DWG NO:  
=DOC\_NO\_ASSY\_

.lfe

3 OF 4

Bill Of Materials

Line #	Designator	Name	Quantity
	U_Accel1	6 DoF Accelerometer	1
	GPS_Module1	GPS 13740	1
27	U1	FT234XD-R	1
36	U3	SAMW25H18-MR510PB	1
28	R4, RJ1	CRCW06030000Z0EC	2
	C_A1, C_A2	GRM033C80J104ME15D	2
6	R12, R14, R15	ERJ3EKF1002V	3
3	R3, R5, R7, R11, R16, R17	CRCW06031K00FKEB	6
11	R1, R2, R8, R9, R10	27R 1% 0603(1608)	5
16	C1, C3, C7, C13	VJ0603Y104KXJCW1BC	4
	R_A1, R_A2, R_O1, R_O2	AT0603DRD074K7L	4
	R_B1	CPF0603B180KE	1
2	R18, R19	1K13 1% 0603(1608)	2
7	C2	C0603C103K4RACTU	1
17	R13	100R 1% 0603(1608)	1
24	FB1	BLM21PG221SN1D	1
29	R6	Jumper 0805(2012)	1
20	P1	640456-3	1
	P2, P3, P4, P5	640456-4	4
4	C10, C14	0603YC105JAT2A	2
5	C4, C15, C16	CAP 4.7uF 16V 0805(2012)	3
9	C11, C12	CAP 18pF 16V 0603(1608)	2
14	C5, C6	CAP 47pF 16V 0603(1608)	2
32	DS1	LTST-C191KGKT	1
31	D4	LTST-C170GKT	1
35	B1	PTS810 SJK 250 SMTR LFS	1
30	D1, D3	LTST-C170CKT	2
34	D2	PRTR5V0U2X,215	1
	C_B2	C1608X5R1C106M080AB	1
21	U2	20021111-00010T4LF	1
39	J1	ZX62R-B-5P	1
8	C8, C9	0805YD106KAT2A	2
	U_Buck1	TPS62082DSGR	1
19	SD1	104031-0811	1
25	U4	BQ24075RGTRG4	1
	L_B1	744311100	1
	LCD1	OLED LCD Screen	1
	Battery1	LiPo Battery	1
	C_B1	C1608X5R0J226M080AC	1

REV STATUS OF SHEETS		REV	DWG NO. =DOC_NO_ASSY_DWG		REV	REVISIONS				
SHEET						ZONE	REV	DESCRIPTION	DATE	APPROVED

APPROVALS		DATE	
ENGINEER:	Weihao Huang	2/26/2022	
DESIGNER:	Weihao Huang	2/26/2022	
CHECKER:	Weihao Huang	2/26/2022	
Reference Documents		DESIGN ITEM: PCB_Board Assembly DESIGN ITEM REVISION: 1.0	
NEXT ASSY		USED ON	TITLE: IoTracking
APPLICATION			SIZE: B DWG NO:
SCALE:	FILE NAME: IoTracking_Board_Assembly.PCBDwf	REV:	4 OF 4