SRAM INTERFACE

FLOW_IN —— 4 —— [REG] —— 4 —→ FLOW_OUT

node id —→ [=] —→ nid_eq_sram

[TYPE == read] —→ write_req

[TYPE == write] —→ read_req

TYPE_IN —— 3 ——
NID_IN —— 4 ——
ADDR_IN —— 18 —— [REG]
DATA_IN —— 32 ——
AGE_IN —→ age
VALID_IN —→ valid

nid_eq_sram
valid —→ passthru
age

nid_eq_sram
valid
age —→ sram_read
read_req

nid_eq_sram
valid
age —→ sram_write
write_req

[REG] [REG] [REG] [REG]

type
nid
address
data

read resp —→ type
nid
address
data

pass
read

[REG] [REG] [REG] [REG] [REG]

SRAM_WE   SRAM_CS   SRAM ADDR   SRAM DATA

type —— 3 —→ TYPE_OUT
nid —— 4 —→ NID_OUT
address —— 18 —→ ADDR_OUT
data —— 32 —→ DATA_OUT
—→ AGE_OUT
valid —→ VALID_OUT

[REG]