

FORMAT		OPCODE	31	24	23	16	15	8	7	0
0 0	0 0		OPCODE		DEST			SRC A		SRC B
	0 1		OPCODE		DEST			SRC A		IMM
	1 0		OPCODE		DEST			IMM		SRC B
	1 1		OPCODE		DEST			IMM		IMM

0 0 0 0	ADD
0 0 0 1	SUB
0 1 0 0	AND
0 1 0 1	OR
0 1 1 0	XOR
0 1 1 1	XNOR

0 1	0 0	OPCODE	DEST	SRC A	SRC B
	0 1	OPCODE	DEST	SRC A	IMM
	1 0	OPCODE	DEST	IMM	SRC B
	1 1	OPCODE	DEST	IMM	IMM

0 0 0 0	LOAD BIT
0 0 0 1	TEST BIT
1 1 1 0	SWAP BIT
1 1 1 1	SWAP

10		OPCODE	DEST SRC	IMM
	0 0 0 0 0	LOAD LOW		
	0 0 0 0 1	LOAD HIGH		

11		OPCODE	COND SRC	BRANCH ADDRESS
	000000	== 0		
	000001	!= 0		
	000010	> 0		
	000011	< 0		
	000100	>= 0		
	000101	<= 0		
	100000	ALWAYS		