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Project Explorer

dio

e100

code

eeopro100.c

spec

8255X_OpenSDM_2006.pdf

readme.txt

EntryEditor

eeopro100.c

```
#define BIT(n) (1 << (n))
#define BITS(n, m) (((0xffffffffU << (31 - n)) >> (31 -

/* The SCB accepts the following controls for the Tx and
#define CU_NOP 0x0000 /* No operation. */
#define CU_START 0x0010 /* CU start. */
#define CU_RESUME 0x0020 /* CU resume. */
#define CU_STATSADDR 0x0040 /* Load dump counters ad
#define CU_SHOWSTATS 0x0050 /* Dump statistical coun
#define CU_CMD_BASE 0x0060 /* Load CU base address.
#define CU_DUMPSTATS 0x0070 /* Dump and reset statis
#define CU_SRESUME 0x00a0 /* CU static resume. */

#define RU_NOP 0x0000
#define RX_START 0x0001
#define RX_RESUME 0x0002
```

8255X_OpenSDM_2006.pdf

Host Software Interface

intel

Table 14. SCB Command Word Bits Descriptions

Bit	Symbol	Description
		This field contains the CU Command. Valid values for this field are: 0000 NOP. The no operation command does not affect the current state of the unit. 0001 CU Start. CU Start begins execution of the first command on the CBL. A pointer to the first CB of the CBL should be placed in the SCB General Pointer before issuing this command. NOTE: The CU Start command should only be issued when the CU is in the idle or suspended states (never when the CU is in the active state) and all of the previously issued CBs have been processed and completed by the CU. Sometimes, it is only possible to determine that all CBs are completed by checking the complete bit in all previously issued Command Blocks. 0010 CU Resume. The CU Resume command resumes CU operation by executing the next command. If the CU is Idle, it ignores the CU Resume command. 0100 Load Dump Counters Address. This command directs the device where to

LinksConsole

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All Data

Type	time	Code file	Code text	PDF File	Page	Spec Text	Comments
valid	2012.08.24 15:0						
valid	2012.09.10 16:5	eeopro100.	#define CU_NOP 0x0000 /*	8255X_Open	46	0000 NOP. The no operation com	CU NOP
valid	2012.09.10 16:5	eeopro100.	#define CU_START 0x0010 /	8255X_Open	46	0001 CU Start. CU Start begins e	CU START
valid	2012.09.10 16:5	eeopro100.	#define CU_RESUME 0x0020	8255X_Open	46	0010 CU Resume. The CU Resum	CU RESUME
valid	2012.09.10 16:5	eeopro100.	#define CU_STATSADDR 0x004	8255X_Open	46	0100 Load Dump Counters Addre	Load Dump Counter Address.
valid	2012.09.10 16:5	eeopro100.	#define CU_SHOWSTATS 0x005	8255X_Open	46	0101 Dump Statistical Counters.	Dump statistical counters.
valid	2012.09.10 16:5	eeopro100.	#define CU_CMD_BASE 0x0060	8255X_Open	46	0110 Load CU Base. The internal	Load CU base address.
valid	2012.09.10 16:5	eeopro100.	#define CU_DUMPSTATS 0x007	8255X_Open	46	0111 Dump and Reset Statistical	Dump and reset statistical counters.