coDoc

File coDoc Window Help

79

EntryEditor

eepro100.c

```
                                tx_buffer_size);
        size += tx_buffer_size;
    }
    if (tbd_array == 0xffffffff) {
        /* Simplified mode. Was already handled by code above. */
    } else {
        /* Flexible mode. */
        uint8_t tbd_count = 0;
        if (s->has_extended_tcb_support && !(s->configuration[6] & BIT(4))) {
            /* Extended Flexible TCB. */
            for (; tbd_count < 2; tbd_count++) {
                uint32_t tx_buffer_address = e100_ldl_le_phys(tbd_address);
                uint16_t tx_buffer_size = e100_ldw_le_phys(tbd_address + 4);
                uint16_t tx_buffer_el = e100_ldw_le_phys(tbd_address + 6);
                tbd_address += 8;
                TRACE(RXTX, logout
                    ("TBD (extended flexible mode): buffer address 0x%08x, size 0
                     tx_buffer_address, tx_buffer_size));
                tx_buffer_size = MIN(tx_buffer_size, sizeof(buf) - size);
                cpu_physical_memory_read(tx_buffer_address, &buf[size],
                                         tx_buffer_size);
                size += tx_buffer_size;
                if (tx_buffer_el & 1) {
                    break;
                }
```

8255X_OpenSDM_2006.pdf

— Bit 4 - Extended Transmit CB (TxCB). This bit is reserved on the 82557 and should be set to 1. However, for the 82558 or 82559, it determines the type of TxCB that is to be used by the device.

If this bit is 1, the device reads the standard 4 Dword TxCB. When this bit equals 0, the device reads 8 Dwords for all CBs and processes the TxCBs as Extended TxCBs as described in Section 6.4.2.5, "Transmit (100b)".

0 = Extended TxCB.

1 = Standard TxCB.

Default - 1 (Standard TxCB).

Recommended - 1 for compatibility reasons. If performance is the main criteria, it is recommended that this bit equal 0.

— Bit 3 - CI Interrupt = CU Idle Interrupt. This bit determines whether the device generates an interrupt when the CU leaves the Active state (CNA interrupt) or when the CU enters the Idle state (CI interrupt). If CNA interrupt is enabled, the device will generate an interrupt when the CU goes from the Active to a non-active state (Idle or Suspended). Interrupts are generated whenever the device sees an EL or S bit in a CB that causes it to go into the Idle or Suspended state respectively on completion of the command. The CI interrupt will generate interrupts only on a transition from an Active to the Idle state. If the CI mode is enabled, interrupts can be generated in dynamic chaining (suspend/resume) by setting the I-bit on individual CBs.

0 = CNA Interrupt. An interrupt is generated when the CU goes from active to idle or suspended state.

1 = CI Interrupt. An interrupt is generated when the CU goes from the active to the idle state.

Default - 0 (CNA interrupt).

Recommended - 0, depending on the implementation of the transmit code.

Links | Console | AST View

Search: All Data

Type	time	Code file	Code text	PDF File	Pag	Spec Text	Comments
/[213]	2012.08.24 15:26						
valid	2012.09.11 17:24	eepro100.c	s->mdimem[reg] = 0x0001;	8255X_OpenSI	131	0	Support auto-negotiation.
valid	2012.09.13 16:38	eepro100.c	!(s->configuration[6] & BIT(4))	8255X_OpenSI	79	0 = Extended TxCB.	Extended TxCB.
valid	2012.09.11 17:10	eepro100.c	data &= ~0x0200;	8255X_OpenSI	127	0 = Normal operation	Normal operation.
valid	2012.09.13 17:37	eepro100.c	e100_stl_le_phys(s->statsaddr + 0, s	8255X_OpenSI	48	0 Transmit good frames. This counter	Transmit good frames.
valid	2012.09.12 11:03	eepro100.c	cu_idle = 0,	8255X_OpenSI	44	00 Idle	CU Idle.
valid	2012.09.11 15:04	eepro100.c	CmdNOp = 0	8255X_OpenSI	65	000	Command NOP.
valid	2012.09.10 16:58	eepro100.c	#define RU_NOP 0x0000	8255X_OpenSI	47	000 NOP. NOP is a no operation comn	RU NOP.
valid	2012.09.12 21:45	eepro100.c	case RU_NOP: /* No operation	8255X_OpenSI	47	000 NOP. NOP is a no operation comn	RU NOP.
valid	2012.09.12 10:58	eepro100.c	ru_idle = 0,	8255X_OpenSI	44	0000 Idle	RU Idle.
valid	2012.09.10 16:53	eepro100.c	#define CU_NOP 0x0000 /* No	8255X_OpenSI	46	0000 NOP. The no operation comman	CU NOP