

MPU Peripheral Access  
Layer

MPU Register Masks



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graph LR; A[MPU Register Masks] --> B[MPU Peripheral Access Layer]
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The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'MPU Peripheral Access Layer' on two lines. The box on the right is light gray with a black border and contains the text 'MPU Register Masks' on one line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a relationship or flow from the register masks to the access layer.