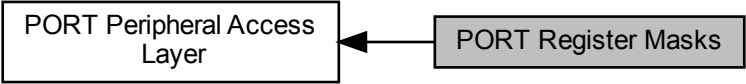


PORT Peripheral Access
Layer

PORT Register Masks



```
graph LR; A[PORT Register Masks] --> B[PORT Peripheral Access Layer]
```

The diagram consists of two rectangular boxes. The box on the left is white with a black border and contains the text 'PORT Peripheral Access Layer' in two lines. The box on the right is gray with a black border and contains the text 'PORT Register Masks' in one line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a relationship or flow from the masks to the access layer.