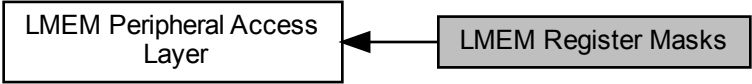


LMEM Peripheral Access  
Layer

LMEM Register Masks



```
graph RL; A[LMEM Register Masks] --> B[LMEM Peripheral Access Layer]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is white with a black border and contains the text 'LMEM Peripheral Access Layer' in two lines. The box on the right is gray with a black border and contains the text 'LMEM Register Masks' in one line. A black arrow points from the right box to the left box.