

GPIO Peripheral Access  
Layer

GPIO Register Masks



```
graph LR; A[GPIO Register Masks] --> B[GPIO Peripheral Access Layer]
```

The diagram consists of two rectangular boxes connected by a horizontal arrow. The box on the left is white with a black border and contains the text 'GPIO Peripheral Access Layer' in two lines. The box on the right is light gray with a black border and contains the text 'GPIO Register Masks' in a single line. A black arrow points from the right side of the gray box to the left side of the white box, indicating a dependency or data flow from the masks to the access layer.