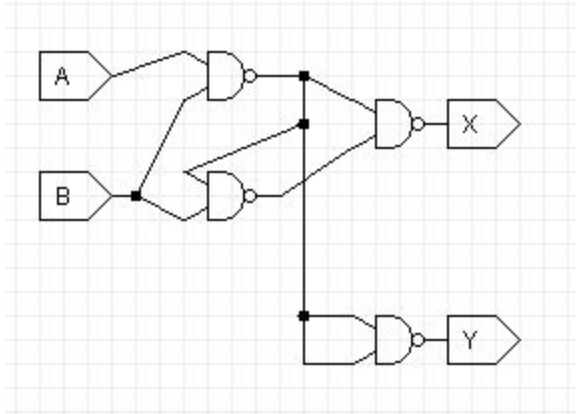


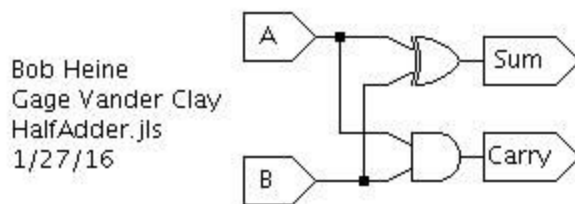
Gage Vander Clay
Bob Heine
CIS 451
1/20/16

Lab 2

Sample Circuit:



Half Adder:



Full Adder:

