

5 - combinational circuits - decoders, encoders, multiplexers

decoders

- used to implement arbitrary functions
- abstraction and modularity, as building blocks
- you can implement arbitrary functions with decoders

$n \rightarrow$ in every output, there is only one 1 among 0s $\rightarrow 2^n$
minterm generator

S_1	S_0	Q_0	Q_1	Q_2	Q_3
0	0	1	0	0	0
0	1	0	1	0	0
1	0	0	0	1	0
1	1	0	0	0	1

decodes a binary number into a one of four code

$Q_0 = \bar{S}_1 \cdot \bar{S}_0$
 $Q_1 = \bar{S}_1 \cdot S_0$
 $Q_2 = S_1 \cdot \bar{S}_0$
 $Q_3 = S_1 \cdot S_0$

enable input used to activate or deactivate the device

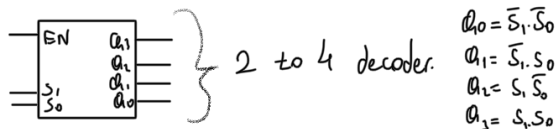
$EN=1 \Rightarrow$ decoder is active, behaves as specified

$EN=0 \Rightarrow$ decoder is inactive, all outputs are 0.

EN	S_1	S_0	Q_0	Q_1	Q_2	Q_3
0	0	0	0	0	0	0
0	0	1	0	0	0	0
0	1	0	0	0	0	0
0	1	1	0	0	0	0
1	0	0	1	0	0	0
1	0	1	0	1	0	0
1	1	0	0	0	1	0
1	1	1	0	0	0	1

EN 0 $\begin{matrix} S_1 \\ S_0 \end{matrix}$ $\begin{matrix} \times \\ \times \end{matrix}$ don't care

blocks and abstraction encapsulated decoders



- decoder blocks provide abstraction
- simpler diagrams and hardware reuse
- like functions in programming

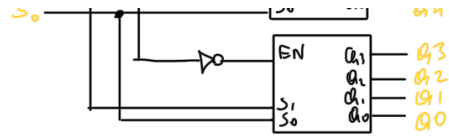
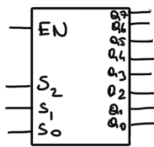
3 to 8 decoder

S_2	S_1	S_0	Q_0	Q_1	Q_2	Q_3	Q_4	Q_5	Q_6	Q_7
0	0	0	1	0	0	0	0	0	0	0
0	0	1	0	1	0	0	0	0	0	0
0	1	0	0	0	1	0	0	0	0	0
0	1	1	0	0	0	1	0	0	0	0
1	0	0	0	0	0	0	1	0	0	0
1	0	1	0	0	0	0	0	1	0	0
1	1	0	0	0	0	0	0	0	1	0
1	1	1	0	0	0	0	0	0	0	1

or two 2 to 4 decoders

(S_2 like enable input)
 when $S_2 = 0$ outputs: $Q_0 - Q_3$
 when $S_2 = 1$ outputs: $Q_4 - Q_7$



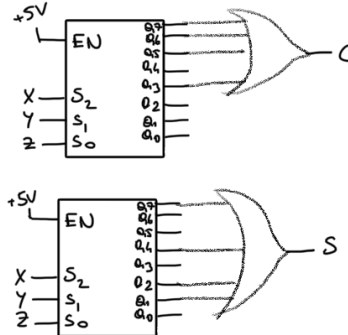


decoder-based adder

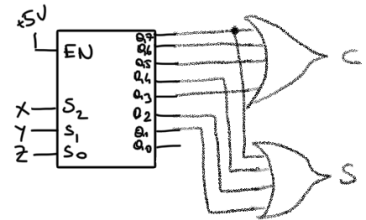
X	Y	Z	C	S
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	1	0
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	1	1

$$C(X, Y, Z) = \sum m(3, 5, 6, 7)$$

$$S(X, Y, Z) = \sum m(1, 2, 4, 7)$$



or using one decoder



Variation of the standard decoder

active-high decoder (normal one)

EN	S ₁	S ₀	a ₀	a ₁	a ₂	a ₃
0	x	x	0	0	0	0
1	0	0	1	0	0	0
1	0	1	0	1	0	0
1	1	0	0	0	1	0
1	1	1	0	0	0	1

$$a_3 = S_1 S_0 \quad a_2 = S_1 S_0' \quad a_1 = S_1' S_0 \quad a_0 = S_1' S_0'$$

→ generate minterms

active-low decoder

EN'	S ₁ '	S ₀ '	a ₀ '	a ₁ '	a ₂ '	a ₃ '
0	0	0	0	1	1	1
0	0	1	1	0	1	1
0	1	0	1	1	0	1
0	1	1	1	1	1	0
1	x	x	1	1	1	1

$$a_3' = S_1' + S_0' \quad a_2' = S_1' + S_0 \quad a_1' = S_1 + S_0' \quad a_0' = S_1 + S_0$$

→ generate maxterms

$$\text{converting} = \sum m(1, 5, 7) = \prod M(1, 2, 3, 6)$$

multiplexers / demultiplexers / muxes

- used to choose between devices
- also used to implement arbitrary functions

inputs =

- 2ⁿ data input lines
- S select line (to choose one of the 2ⁿ data input lines)

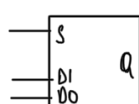
$$2^n \rightarrow$$



$$\rightarrow 1$$

Output =

one of the 2ⁿ data inputs



$$Q = S' D_0 + S D_1$$

2 to 1 mux

S	D ₁	D ₀	Q
0	0	0	0
0	0	1	1
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

$$\Rightarrow$$

S	Q
0	D ₀
1	D ₁

∴ if S = 0 → the output is D₀

if $S=1 \rightarrow$ the output is D_1

1 0 1 1

4 to 1 multiplex

EN	S1	S0	Q
0	0	0	D0
0	0	1	D1
0	1	0	D2
0	1	1	D3
1	x	x	1

$$Q = S_1' S_0' D_0 + S_1' S_0 D_1 + S_1 S_0' D_2 + S_1 S_0 D_3 + \text{EN}$$

(active-low one)

functions with multiplexers

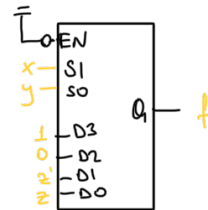
ex: func	selectors		
	x	y	z
0	0	0	0
1	0	0	1
1	0	1	0
0	0	1	1
0	1	0	0
0	1	0	1
1	1	1	0
1	1	1	1



A 2^n to 1 multiplexer routes one of 2^n input lines to a single output line

more efficient way

when $xy = 00 \quad f = z$ (function/output)
 $xy = 01 \quad f = z'$
 $xy = 10 \quad f = 0$
 $xy = 11 \quad f = 1$



encoders

\hookrightarrow the inverse operation of a decoder

$2^n \rightarrow \boxed{} \rightarrow n$
 (or less)

inputs								outputs		
D0	D1	D2	D3	D4	D5	D6	D7	x	y	z
1	0	0	0	0	0	0	0	0	0	0
0	1	0	0	0	0	0	0	0	0	1
0	0	1	0	0	0	0	0	0	1	0
0	0	0	1	0	0	0	0	0	1	1
0	0	0	0	1	0	0	0	1	0	0
0	0	0	0	0	1	0	0	1	0	1
0	0	0	0	0	0	1	0	1	1	0
0	0	0	0	0	0	0	1	1	1	1

ex: octal to binary

examples of all = 7-segment display



0 1 2 3 4 5 6 7 8 9

ex inputs

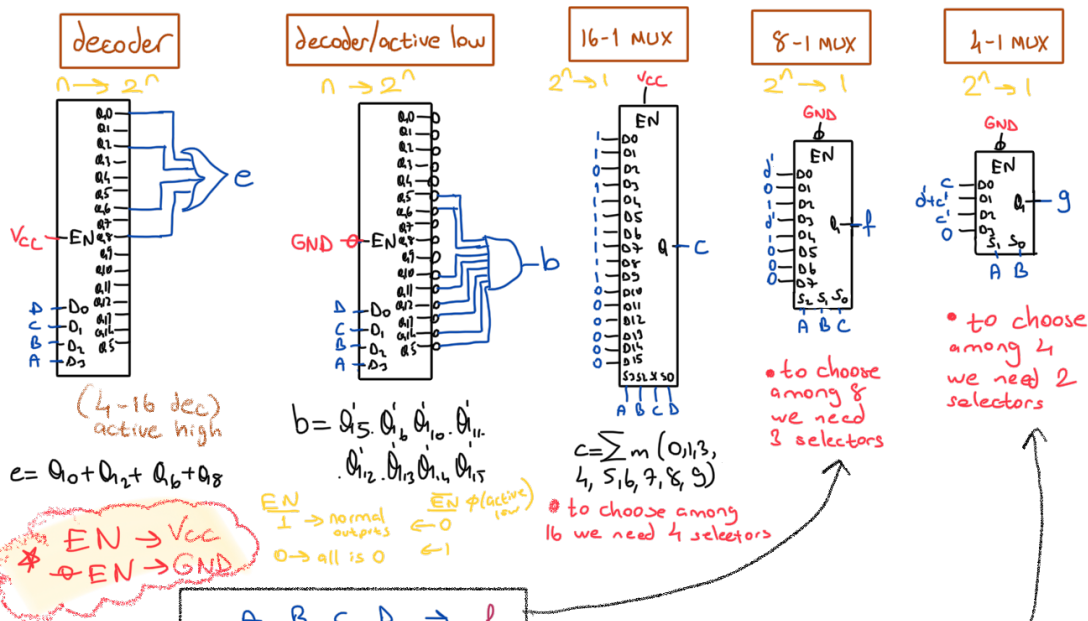
outputs

A B C D \rightarrow a b c d e f g

$0 \rightarrow a+b+c+d+e+f$
 $1 \rightarrow b+c$
 $9 \rightarrow a+b+c+d+f+g$
 $[0,9] \rightarrow \{a,b,c,d,e,f,g\}$
 (4 bits)
 ABCD

0	0	0	0	0	1	1	1	1	1	1	0
1	0	0	0	1	0	1	1	0	0	0	0
2	0	0	1	1	1	0	1	1	0	1	1
3	0	0	1	1	1	1	0	0	0	1	1
4	0	1	0	0	1	1	1	0	0	1	1
5	0	1	0	0	1	1	1	0	1	1	1
6	0	1	1	1	1	1	0	1	1	1	1
7	0	1	1	1	1	1	1	0	0	1	1
8	1	0	0	0	0	0	1	1	1	1	1
9	1	0	0	0	1	1	1	1	1	1	1
10	1	0	1	1	1	1	0	0	0	0	0
11	1	0	1	1	1	1	0	0	0	0	0
12	1	0	1	1	1	0	0	0	0	0	0
13	1	0	1	1	0	0	0	0	0	0	0
14	1	0	1	0	0	0	0	0	0	0	0
15	1	0	1	0	0	0	0	0	0	0	0

$a = \sum m(0,2,6,7,8,9)$
 $b = \sum m(0,1,2,3,4,7,8,9) \rightarrow \text{active low } \sum M(5,6,10,11,12,13,14,15)$
 $c = \sum m(0,1,3,4,5,6,7,8,9)$
 $d = \sum m(0,2,3,5,6,8,9)$
 $e = \sum m(0,2,6,8)$
 $f = \sum m(0,4,5,6,8,9)$
 $g = \sum m(2,3,4,5,6,8,9) \rightarrow \text{active low } \sum M(0,1,7,10,11,12,13,14,15)$



A	B	C	D	f
0	0	0	0	1
1	0	0	0	0
2	0	0	1	0
3	0	0	1	0
4	0	1	0	1
5	0	1	0	1
6	0	1	1	1
7	0	1	1	1
8	1	0	0	1
9	1	0	0	1
10	1	0	1	0
11	1	0	1	0
12	1	1	0	0
13	1	1	0	0
14	1	1	1	0
15	1	1	1	0

to eliminate D

A	B	C	D	g
0	0	0	0	0
1	0	0	0	0
2	0	0	1	0
3	0	0	1	0
4	0	1	0	0
5	0	1	0	0
6	0	1	1	0
7	0	1	1	0
8	1	0	0	0
9	1	0	0	0
10	1	0	1	0
11	1	0	1	0
12	1	1	0	0
13	1	1	0	0
14	1	1	1	0
15	1	1	1	0

to eliminate C and D

C	D	D+C
0	0	0
0	1	1
1	0	1
1	1	1

D+C

