

**Transient Voltage  
Surge Suppressors By:**

**ST-CDIN Series**

DIN Rail Mounted Series Wired AC Unit with Sine Wave Tracking and Discrete All-Mode Protection



*Energy Control Systems*



*"Power Quality is our Only Business"*

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The SineTamer® ST-CDIN devices provide the best ring wave transient protection available for a device of its type. These devices are intended for a single 120 or 240 VAC circuit applications at locations feeding sensitive/critical equipment. It is extremely effective in limiting transients generated inside the facility and is an absolute must on circuits feeding critical microprocessor based equipment. It boasts a robust 60kA per phase peak surge current rating.

This economical 35MM DIN rail mounted device is unique in that it is designed as a stand-alone surge suppression device and requires no special external fusing. It is equipped with our board level thermal fusing in combination with our patent-pending component level current fusing as well. Its compact size makes installation a breeze and the warranty is the best in the industry. Add to all that, dedicated "all mode" Enhanced Sinewave Tracking™ and encapsulated Optimal Response Network™, and you get a device that defines effective and reliable surge suppression.

We believe that we offer the most versatile TVSS devices on the market with performance specs that are superior to our competitors and a warranty that is second to none, the ST-CDIN units are simply another example of meeting the market demand.

**GENERAL**

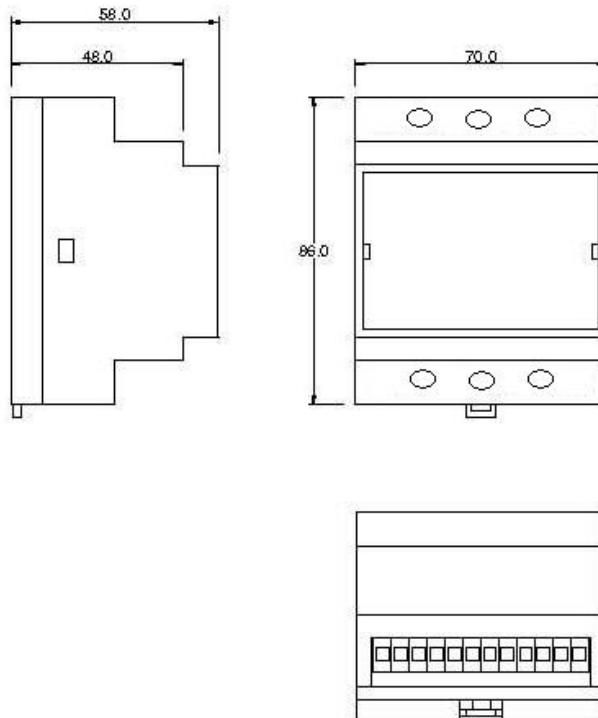
<b>Description:</b>	Series wired parallel-connected transient voltage surge suppressor with encapsulated Optimal Response Network™ and Enhanced Sinewave Tracking circuitry (60kA per phase peak surge current.)
<b>Application:</b>	Designed for use at ANSI/IEEE Category A with susceptibility up to medium exposure levels to protect sensitive/critical loads fed by a single 120 or 240VAC circuit.
<b>Warranty:</b>	<b>25 Years Unlimited Free Replacement</b>
<b>Unit Listings:</b>	Tested to UL 1449 Second Edition and CUL

**MECHANICAL**

<b>Enclosure:</b>	Housing = Lexan 940 (UL 94V-0); Base = NORYL V01550 (UL 94V-0).
<b>Mounting:</b>	35MM DIN rails
<b>Connection Method:</b>	Terminal strip at both the input and output sides of the device. 12 AWG – 30 AWB
<b>Shipping Weight:</b>	≈ 2 lbs

**ELECTRICAL**

<b>Circuit Design:</b>	Series wired, parallel connected hybrid design incorporating discrete all mode protection and utilizing our encapsulated Optimal Response Network™ and Enhanced Sinewave Tracking circuitry design to provide lowest possible let-through-voltages. All suppression circuits are completely encapsulated in our exclusive compound to assure long component life and complete protection from the environment and/or vibration.
<b>Protection Modes:</b>	Dedicated protection components and circuitry for each mode. Discrete L-N (Normal Mode), and Discrete L-G, N-G (Common Mode)
<b>Input Power</b>	50-60Hz
<b>Frequency:</b>	
<b>Maximum Continuous</b>	
<b>Operating Current:</b>	20 Amps AC
<b>Response Time:</b>	<1 nanosecond
<b>Circuit Diagnostics:</b>	Super Bright LED, normally on.
<b>Circuit Interrupt:</b>	None Required – Board level current fusing standard.



MEASURED LIMITING VOLTAGE PERFORMANCE AND ELECTRICAL SPECIFICATIONS					
Model	MCOV	Mode	ANSI/IEEE C62.41 & C62.45 Let-Through Voltage Test Results		
			A1 2kV, 67A 100KHz Ring Wave 180° Phase Angle	A3 6kV, 200A 100KHz Ring Wave 90° Phase Angle	B3/C1 6kV, 3kA Impulse Wave 90° Phase Angle
ST-CDIN120-20	150 L-N	L-N	30 V (D)	94V (D)	289 (D)
	150 L-G	L-G	58 V (D)	190V (D)	380 (D)
	150 N-G	N-G	33 V (S)	94V (S)	550 (S)
ST-CDIN240-20	300 L-N	L-N	66 V (D)	121V (D)	548 (S)
	300 L-G	L-G	100 V (D)	220V (D)	558 (S)
	300 N-G	N-G	33 V (S)	121V (S)	655 (S)

\***Measured Limiting Voltage (Let-Through) Test Environment:** Dynamic (D) or Static (S), positive polarity. All voltages are peak ( $\pm 10\%$ ). Time Base is 1ms. 180° phase angle voltages are measured from the zero crossing, 90° phase angle voltages are measured from the positive peak of the sine wave to the positive peak of the surge indicating actual excess voltage let through. All tests were performed with the device connected in series simulating actual installation.

\*\***Suppressed Voltage Test Environment** using test parameters as defined by Underwriters Laboratory: Dynamic (D) or Static (S), Positive Polarity. Time base=10 $\mu$ s. All voltages are peak ( $\pm 10\%$ ), 90° phase angle voltages are measured from the zero crossing to the peak of the surge.