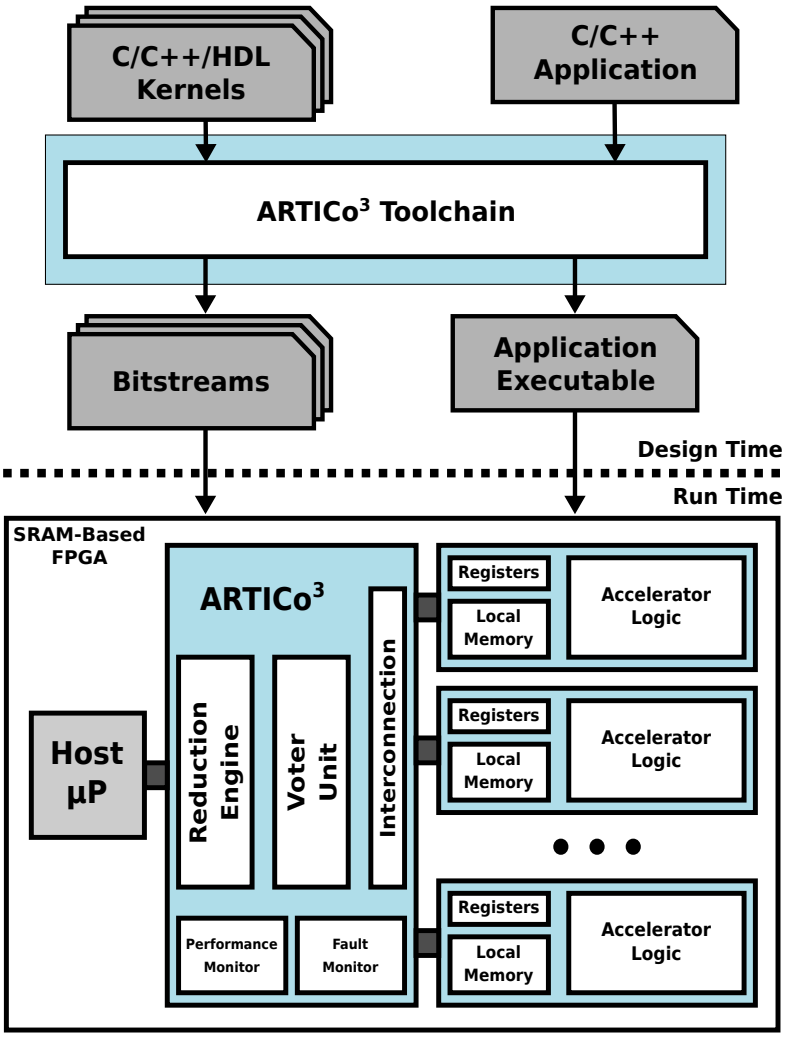
**ARTICo³** is an **open-source runtime reconfigurable processing architecture** to enable **hardware-accelerated** high-performance embedded computing. It is highly flexible, offering **adaptive** computing **performance**, **energy efficiency**, and **fault tolerance** on demand.

The architecture is complemented at **design time** with an **automated toolchain** to build reconfigurable multi-accelerator systems, and at **run time** with a **software library** to transparently manage both reconfiguration and parallel execution processes.




## Main Features

### Hardware Design made easy

Write your accelerators in C/C++ and exploit High-Level Synthesis.

### FPGA Reconfiguration made easy

Forget about complex design flows and low-level technology limitations.

### Parallel Processing made easy

Offload computations to the FPGA fabric transparently using a lightweight API.

## Open Source Framework

ARTICo³ is an **open-source** framework available on GitHub. **Documentation** and **tutorials** are also available online for you to use it in your own projects.

ARTICo³ is supported by an increasing **community** of developers in the field of **reconfigurable embedded systems**.

https://des-cei.github.io/tools/artico3

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CEI UPM | Centro de Electrónica Industrial

UNIVERSIDAD POLITÉCNICA DE MADRID