

DP513757**Deviation - Prototype** • CRBU: NG2-1RU Churchill P3B Deviation**Release**

Churchill P3B-HBM 68-7325-02 rev01 *41pcs
800-106819-01: approve to use up rev02 (stock 14pcs, BOM rev03)
Fan Module 800-106831-01 rev02

BB 73-20364-01 rev09
01-1057-02: use recovered ASIC
28-14425-04: approve to use rev01 (BOM rev02)
11-100337-01: substitute with 11-3233-01
17-16082-01: use raw part 15-106622-01
17-16083-01: use raw part 15-106622-01
17-16084-01: use raw part 15-106622-01
29-101733-02: approve to use cancelled MPN: 2324734-1
Stuff 11-101250-01 at C21_N0 (change C21_N0 from 0.1uF to 10uF)

CPU 73-19882-04 rev06
28-14428-04: approve to use rev01 (BOM rev02)
11-100337-01: substitute with 11-3233-01
29-101734-02: approve to use cancelled MPN:2324787-1
Stuff 12-2775-02 at R536
Stuff 12-2537-02 at R1274
No stuff / Remove R1282

PDB 73-19884-05 rev05
28-14431-05: approve to use rev01(BOM rev02)

IO 73-19953-03 rev03
28-14484-03: approve to use rev01 (BOM rev02)

FOL to follow the P3B CPU MANUAL PROGRAMMING INSTRUCTION DOC
on box, or use doc attached in the deviation.
Further bring-ups and programming instructions are available on box link:
<https://cisco.app.box.com/folder/119105116392>

MB Programming Instructions:

1. Enable 12V and 12V_Standby on MB board.
2. Program Power CPLD using Lattice Cable (program at ICT).
 - a. Add header at P10.
 - b. Use JTAG header at J32.
3. Program IOFPGA using Xilinx Cable.
 - a. Using JTAG header at J31
4. Program IR SugarLoaf power controller using IR cable.
 - a. Using IR header at J5
5. Program the 4 TI JMACs power controllers using TI cable (program at ICT).
 - a. Using IR header at J5
6. Program IO_CPLDs using Lattice Cable
 - a. Remove header at P10
 - b. Using JTAG header at J32.

PA GB 73-20364-01 rev09 *1pcs
01-1057-02: use 2.5D mechanical sample ASIC 01-1057-01 (contact Kevin Cheng)
28-14425-04: use solder sample
11-100337-01: substitute with 11-3233-01
17-15787-01: use raw part 16-100991-01
17-16082-01: use raw part 15-106622-01
17-16083-01: use raw part 15-106622-01
17-16084-01: use raw part 15-106622-01
29-101733-02: approve to use cancelled MPN: 2324734-1

FOL to follow the instruction in BB PA Board Test Plan 20200703.ppt in
attachments tab of deviation.

- Dye & Pry BGA PN & Location: 01-1057-01 U1_N0, 16-100991-01 U1_C1
- Cross section PN & location:29-103805-01 P3, 16-101403-01 U36, 29-2327-02 HTSENSE, and 29-103702-01, J10

Strain Gauge GB 68-7325-01 rev03 *1pcs: PCBA only test 5DX, no ICT
Fan Module 800-106831-01 rev02

BB 73-20364-01 rev09
01-1057-02: use mechanical sample ASIC, edgebonded
28-14425-04: use solder sample
11-100337-01: substitute with 11-3233-01
17-15787-01: use raw part 16-100991-01
17-16082-01: use raw part 15-106622-01
17-16083-01: use raw part 15-106622-01
17-16084-01: use raw part 15-106622-01
29-101733-02: approve to use cancelled MPN: 2324734-1
Install hightpipe 700-117164-03 and 800-49219-01 after edgebonded

CPU 73-19882-04: Build down version 73-19882-03 rev08
Build with 28-14428-03
17-15891-02: use raw part 16-101515-01
29-101734-02: approve to use cancelled MPN:2324787-1

PDB 73-19884-05 rev05
28-14431-05: use rev.01 (BOM rev.02)

IO 73-19953-03 rev03
28-14484-03: use rev.01 (BOM rev.02)

Cover Page**Number:** DP513757**Status:** Release

Deviation Type:	Deviation - Prototype	Reason For Change:	Deviation - Prototype
Description of Change:	<p>CRBU: NG2-IRU Churchill P3B Deviation</p> <p>Churchill P3B-HBM 68-7325-02 rev01 *41pcs 800-106819-01: approve to use up rev02 (stock 14pcs, BOM rev03) Fan Module 800-106831-01 rev02</p> <p>BB 73-20364-01 rev09 01-1057-02: use recovered ASIC 28-14425-04: approve to use rev01 (BOM rev02) 11-100337-01: substitute with 11-3233-01 17-16082-01: use raw part 15-106622-01 17-16083-01: use raw part 15-106622-01 17-16084-01: use raw part 15-106622-01 29-101733-02: approve to use cancelled MPN: 2324734-1 Stuff 11-101250-01 at C21_N0 (change C21_N0 from 0.1uF to 10uF)</p> <p>CPU 73-19882-04 rev06 28-14428-04: approve to use rev01 (BOM rev02) 11-100337-01: substitute with 11-3233-01 29-101734-02: approve to use cancelled MPN:2324787-1 Stuff 12-2775-02 at R536 Stuff 12-2537-02 at R1274 No stuff / Remove R1282</p> <p>PDB 73-19884-05 rev05 28-14431-05: approve to use rev01(BOM rev02)</p> <p>IO 73-19953-03 rev03 28-14484-03: approve to use rev01 (BOM rev02)</p> <p>FOL to follow the P3B CPU MANUAL PROGRAMMING INSTRUCTION DOC on box, or use doc attached in the deviation. Further bring-ups and programming instructions are available on box link: https://cisco.app.box.com/folder/119105116392</p> <p>MB Programming Instructions:</p> <ol style="list-style-type: none">1. Enable 12V and 12V_Standby on MB board.2. Program Power CPLD using Lattice Cable (program at ICT).<ol style="list-style-type: none">a. Add header at P10.b. Use JTAG header at J32.3. Program IOFPGA using Xilinx Cable.<ol style="list-style-type: none">a. Using JTAG header at J314. Program IR SugarLoaf power controller using IR cable.<ol style="list-style-type: none">a. Using IR header at J55. Program the 4 TI JMACs power controllers using TI cable (program at ICT).<ol style="list-style-type: none">a. Using IR header at J56. Program IO_CPLDs using Lattice Cable<ol style="list-style-type: none">a. Remove header at P10b. Using JTAG header at J32. <p>-----</p> <p>PA GB 73-20364-01 rev09 *1pcs 01-1057-02: use 2.5D mechanical sample ASIC 01-1057-01 (contact Kevin Cheng) 28-14425-04: use solder sample 11-100337-01: substitute with 11-3233-01 17-15787-01: use raw part 16-100991-01 17-16082-01: use raw part 15-106622-01 17-16083-01: use raw part 15-106622-01 17-16084-01: use raw part 15-106622-01 29-101733-02: approve to use cancelled MPN: 2324734-1</p> <p>FOL to follow the instruction in BB PA Board Test Plan 20200703.ppt in attachments tab of deviation.</p> <ul style="list-style-type: none">• Dye & Pry BGA PN & Location: 01-1057-01 U1_N0, 16-100991-01 U1_C1• Cross section PN & location:29-103805-01 P3, 16-101403-01 U36, 29-2327-02 HTSENSE, and 29-103702-01, J10 <p>-----</p> <p>Strain Gauge GB 68-7325-01 rev03 *1pcs: PCBA only test 5DX, no ICT Fan Module 800-106831-01 rev02</p> <p>BB 73-20364-01 rev09 01-1057-02: use mechanical sample ASIC, edgebonded 28-14425-04: use solder sample 11-100337-01: substitute with 11-3233-01 17-15787-01: use raw part 16-100991-01 17-16082-01: use raw part 15-106622-01 17-16083-01: use raw part 15-106622-01 17-16084-01: use raw part 15-106622-01 29-101733-02: approve to use cancelled MPN: 2324734-1 Install lightpipe 700-117164-03 and 800-49219-01 after edgebonded</p> <p>CPU 73-19882-04: Build down version 73-19882-03 rev08 Build with 28-14428-03 17-15891-02: use raw part 16-101515-01 29-101734-02: approve to use cancelled MPN:2324787-1</p> <p>PDB 73-19884-05 rev05 28-14431-05: use rev.01 (BOM rev.02)</p> <p>IO 73-19953-03 rev03 28-14484-03: use rev.01 (BOM rev.02)</p>	<p>Workflow: Deviation - Prototype</p> <p>Originator: Hoang, Daniel (danhoang)</p> <p>Date Released: 08/12/2020 10:59:22 PM PDT</p> <p>Effective To: 10/11/2020 12:00:00 AM PDT</p>	
Reason Code:	For temporary Manufacturing process change		
Change Analyst:	Change Analyst-Routing Group		

Date Originated: 08/06/2020 03:43:07 PM PDT

Final Complete
Date:

Effective From: 08/12/2020 12:00:00 AM PDT

Page Two

Business Unit: CRBU

Reject Reason:

Marking Place deviation label on top side of PCBAs and top chassis cover

Instructions:

Reject Reason
History:

Affected Items

Item Number	Item Description	Revision	LifeCycle Phase	Mass	Reliability Grade	L2 Use Condition	ERP Scheduling	Control Code	SuCM	Risk Rating	Risk Reason Code
68-7325-02	FINAL ASSEMBLY,HBM,TI JMAC,6 FANS,1RU,NG2	-01	Prototype	No Privilege	A	2	No Privilege	CN: Cisco Negotiated			
73-19882-04	PCA, DBRD, NG2 1RU, CPU BOARD, Broadwell DE	-06	Prototype	No Privilege	A	3	No Privilege				
73-19884-05	PCA, DBRD, NG2 1RU, PDB	-05	Prototype	No Privilege	A	3	No Privilege	CN: Cisco Negotiated			
73-19953-03	PCA, DBRD, NG2 1RU, I/O	-03	Prototype	No Privilege	A	3	No Privilege				
73-20364-01	PCA, MBRD, NG2 1RU, 2.5D GB, BASE BOARD , TI JMAC	-09	Prototype	No Privilege	A	3	No Privilege	CN: Cisco Negotiated			

Workflow

Status Code	Workflow Status	Reviewer Job Function	Reviewer	Action	Workflow	Req'd	Signoff User	Status Changed By	Local Client Time	Signoff Comments	Signoff Durat
Future Process	Expired	No Privilege			Deviation - Prototype						No Privilege
Current Process	Release	No Privilege			Deviation - Prototype						No Privilege
Process is moved forward	CCB Review	No Privilege			Deviation - Prototype			Hoang, Daniel (danhoang)	08/12/2020 10:59:22 PM PDT		No Privilege
Process is moved forward	CCB Review	No Privilege	Hoang, Daniel (danhoang)	Approved	Deviation - Prototype	Yes	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		No Privilege
Process is moved forward	Pending	No Privilege			Deviation - Prototype			Hoang, Daniel (danhoang)	08/12/2020 10:58:28 PM PDT		No Privilege

Relationships

Criteria Met	Name	Description	Current Status	Rule	Type
--------------	------	-------------	----------------	------	------

Attachments

Filename	File Description	File Size	File Type	Folder Number	Folder Description	Folder Version	Checkout User	Checkout Date	Modified Date	Last View Date	Label
NG2 BB PA Board Test Plan 20200703.pptx		4,442,249	pptx	FOLDER1335012		1			08/06/2020 04:13:59 PM PDT	08/06/2020 04:13:58 PM PDT	
P3B MB TI JMAC Programming Instruction.docx		14,293	docx	FOLDER1336557		1			08/12/2020 12:52:50 PM PDT	08/12/2020 12:52:49 PM PDT	
Updated P3B Churchill CPU manual programming instructions.docx		30,959,870	docx	FOLDER1336675		1			08/12/2020 10:55:38 PM PDT	08/12/2020 10:55:38 PM PDT	

History

Status	Next Status	Action	User	Group(s)	Local Client Time	Affected Object	Details	Comments	User(s) Notified
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:27 PM PDT		Notify CM_MPA: [SUCCESS];Notification sent to Cms/MPAs Successfully		
Release		Scheduled Notification	Hoang, Daniel (danhoang)		08/12/2020 10:59:27 PM PDT		Notification Notify_CM_Deviations sent to Wang, Fangjuan (fang-juan.wang@foxconn.com);Wong, Tze hang (tzewong);Wu, Tao (jerrytwu1);Xu, Julie (julieux168);Sim, Vikki (vikki.sim@foxconn.com);Ou, Gerry (gerryou01);Fong, Tat ho (tat-ho.fong@foxconn.com);Liu, Celia (lin.liu@mail.foxconn.com);Yu, Su-fen (su-fen.yu@foxconn.com);Nie, Merry (merrynie168);Zhang, Zemin (zezhang);Cui, Jerry		Chang, Christine (christine.chang@foxconn.c Hu, Marco (marcofhu); W Angel (juchi.wu@foxconn.c Lu, Kelvin (kelvinluc); No Privilege; Xu, Tina (tinaxul Privilege; No Privilege; Lee (peter-2345); Tsai, Claire (claire.tsai@foxconn.com); Fangjuan (fang-

Status	Next Status	Action	User	Group(s)	Local Client Time	Affected Object	Details	Comments	User(s) Notified
							(jerry.hs.cui1);Tan, Kok peng (kokpengtan);Yang, De (kenny.dc.yang@foxconn.com);Lu, Vicky (vicky.cy.lu@foxconn.com);Chen, Xingguo (xing-guochen);Chen, Kenson (kensonchen);Yang, Lily (lily.zl.yang@foxconn.com);Pham, Linh (linhpham9);Wang, Alice (alicewang168);Jiang, Amy (amy.jiang);Chou, Cindy (cindywzchou);Chen, Lawrence (lawrencechen2017);Zhang, Crystal (crystalzha);Lam, Terry (terry.tm.lam@foxconn.com);Lee, Tim (timleemkd);Lee, Lucky (luckylee168);Chen, Houyu (alex.hy.chen@foxconn.com);He, Andy (andyzwhel123);Kwong, Alex (alkwong);Zeng, Fei (fei.f.zeng@foxconn.com);He, Macro (macro.jl.he@foxconn.com);Zheng, Sara (sara.fq.zheng@mail.foxconn.com);Zhang, Nina (zhangnina);Gui, Weifeng (wegui);Guo, Jasper (jasperguo);Zeng, Jim (jim.j.zeng@foxconn.com);Zhou, Susan (susanzhou168);Sun, Nolen (nolensun168);Peng, Yuna (yuna.peng);Liao, Demon (demon.zl.liao@foxconn.com);Yeung, Ka lok (ka-lok.yeung@foxconn.com);Mai, Jinmei (canemail168);Zhang, Jamy (jamy.jm.zhang@foxconn.com);Wang, Feirong (feirwang);Liu, Evan (evanliu245183);Zhong, Zack (zack.zq.zhong@foxconn.com);Huang, Kiwi (kiwihuang5107);Gu, Denise (denise.xh.gu@foxconn.com);Zhu, Saga (saga.bp.zhu@foxconn.com);Li, Qiang (boblee);Li, Evelyn (evelynli34360);Chen, Annie (annie.ys.chen@foxconn.com);Fan, Tracy (cui-xia.fan@mail.foxconn.com);Cai, Pauline (pauline.bz.cai@foxconn.com);Wu, Leon (leon.l.wu@foxconn.com);Ren, Ping (ping.p.ren@foxconn.com);Lin, Sara (sara.lin);Kwok, Mandy (mandykwok1);Fu, Danielle (daniellefu);Feng, Ze-song (ze-song.feng@foxconn.com);Huang, Anna (anna.wh.huang@foxconn.com);Wu, Candice (candice.he.wu@foxconn.com);Zhou, Alin (alin.l.zhou1);Chang, Christine (christine.chang@foxconn.com);Hu, Marco (marcofhu);Wu, Angel (juchi.wu@foxconn.com);Lu, Kelvin (kelvinluc);Chang, Jui chuan (ray.jc.chang@foxconn.com);Xu, Tina (tinaxu168);Li, Eagle (eagle.hh.li@foxconn.com);Liang, Kim (kim.q.liang@foxconn.com);Lee, Peter (peter-2345);Tsai, Claire (claire.tsai@foxconn.com) with priority Regular		juan.wang@foxconn.com); Tze hang (tzewong); No Pri No Privilege; Sim, Vikki (vikki.sim@foxconn.com); Gerry (gerryou01); Fong, Ti (tat-ho.fong@foxconn.com) Celia (lin.liu@mail.foxconn Yu, Su-fen (su-fen.yu@foxconn.com); Nie, (merryniel168); Zhang, Zem (zezhang); Cui, Jerry (jerry.hs.cui1); Tan, Kok pe (kokpengtan); Yang, De (kenny.dc.yang@foxconn.co Lu, Vicky (vicky.cy.lu@foxconn.com) Xingguo (xing-guochen); N Privilege; Yang, Lily (lily.zl.yang@foxconn.com) Linh (linhpham9); Wang, A (alicewang168); Jiang, Amy (amy.jiang); Chou, Cindy (cindywzchou); Chen, Lawr (lawrencechen2017); Zhang Crystal (crystalzha); Lam, T (terry.tm.lam@foxconn.com Tim (timleemkd); Lee, Luck (luckylee168); Chen, Houyu (alex.hy.chen@foxconn.con Andy (andyzwhel123); Kwo Alex (alkwong); No Privileg Privilege; Zheng, Sara (sara.fq.zheng@mail.foxcor Zhang, Nina (zhangnina); G Weifeng (wegui); Guo, Jasp (jasperguo); No Privilege; Z Susan (susanzhou168); Sun, (nolensun168); Peng, Yuna (yuna.peng); No Privilege; ' Ka lok (ka-lok.yeung@foxconn.com); ! Jinmei (canemail168); No Pr Wang, Feirong (feirwang); ! Privilege; No Privilege; Hua Kiwi (kiwihuang5107); No Privilege; No Privilege; Li, (boblee); No Privilege; Chei Annie (annie.ys.chen@foxconn.co Fan, Tracy (cui-xia.fan@mail.foxconn.com) Pauline (pauline.bz.cai@foxconn.co Privilege; No Privilege; Lin (sara.lin); Kwok, Mandy (mandykwok1); Fu, Daniell (daniellefu); No Privilege; N Privilege; No Privilege; Zho (alin.l.zhou1)
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:27 PM PDT		Notify CM, MPA: [SUCCESS];Notification sent to Cms/MPAs Successfully		
Release		Scheduled Notification	Hoang, Daniel (danhoang)		08/12/2020 10:59:26 PM PDT		Notification Notify_CM_Deviations sent to Wang, Fangjuan (fang-juan.wang@foxconn.com);Wong, Tze hang (tzewong);Wu, Tao (jerrytwu1);Xu, Julie (julieux168);Sim, Vikki (vikki.sim@foxconn.com);Ou, Gerry (gerryou01);Fong, Tat ho (tat-ho.fong@foxconn.com);Liu, Celia (lin.liu@mail.foxconn.com);Yu, Su-fen (su-fen.yu@foxconn.com);Nie, Merry (merryniel168);Zhang, Zemin (zezhang);Cui, Jerry (jerry.hs.cui1);Tan, Kok peng (kokpengtan);Yang, De (kenny.dc.yang@foxconn.com);Lu, Vicky (vicky.cy.lu@foxconn.com);Chen, Kenson (kensonchen);Yang, Lily (lily.zl.yang@foxconn.com);Pham, Linh (linhpham9);Wang, Alice (alicewang168);Jiang, Amy (amy.jiang);Chou, Cindy (cindywzchou);Chen, Lawrence (lawrencechen2017);Zhang, Crystal (crystalzha);Lam, Terry (terry.tm.lam@foxconn.com);Lee, Tim (timleemkd);Lee, Lucky (luckylee168);Chen, Houyu (alex.hy.chen@foxconn.com);He, Andy (andyzwhel123);Kwong, Alex (alkwong);Zeng, Fei (fei.f.zeng@foxconn.com);He, Macro (macro.jl.he@foxconn.com);Zheng, Sara (sara.fq.zheng@mail.foxconn.com);Zhang, Nina (zhangnina);Gui, Weifeng (wegui);Guo, Jasper (jasperguo);Zeng, Jim (jim.j.zeng@foxconn.com);Zhou, Susan (susanzhou168);Sun, Nolen (nolensun168);Peng, Yuna (yuna.peng);Liao, Demon (demon.zl.liao@foxconn.com);Yeung, Ka lok (ka-lok.yeung@foxconn.com);Mai, Jinmei (canemail168);Zhang, Jamy (jamy.jm.zhang@foxconn.com);Wang, Feirong (feirwang);Liu, Evan (evanliu245183);Zhong, Zack (zack.zq.zhong@foxconn.com);Huang, Kiwi (kiwihuang5107);Gu, Denise (denise.xh.gu@foxconn.com);Zhu, Saga (saga.bp.zhu@foxconn.com);Li, Qiang (boblee);Li, Evelyn (evelynli34360);Chen, Annie (annie.ys.chen@foxconn.com);Fan, Tracy (cui-xia.fan@mail.foxconn.com);Cai, Pauline		Wang, Fangjuan (fang-juan.wang@foxconn.com); Tze hang (tzewong); No Pri No Privilege; Sim, Vikki (vikki.sim@foxconn.com); Gerry (gerryou01); Fong, Ti (tat-ho.fong@foxconn.com) Celia (lin.liu@mail.foxconn Yu, Su-fen (su-fen.yu@foxconn.com); Nie, Merry (merryniel168); Zhang, Zem (zezhang); Cui, Jerry (jerry.hs.cui1); Tan, Kok pe (kokpengtan); Yang, De (kenny.dc.yang@foxconn.co Lu, Vicky (vicky.cy.lu@foxconn.com) Xingguo (xing-guochen); N Privilege; Yang, Lily (lily.zl.yang@foxconn.com) Linh (linhpham9); Wang, A (alicewang168); Jiang, Amy (amy.jiang); Chou, Cindy (cindywzchou); Chen, Lawr (lawrencechen2017); Zhang Crystal (crystalzha); Lam, T (terry.tm.lam@foxconn.com Tim (timleemkd); Lee, Luck (luckylee168); Chen, Houyu (alex.hy.chen@foxconn.con Andy (andyzwhel123); Kwo Alex (alkwong); No Privileg Privilege; Zheng, Sara (sara.fq.zheng@mail.foxcor Zhang, Nina (zhangnina); G Weifeng (wegui); Guo, Jasp (jasperguo); No Privilege; Z Susan (susanzhou168); Sun, (nolensun168); Peng, Yuna (yuna.peng); No Privilege; ' Ka lok (ka-lok.yeung@foxconn.com); ! Jinmei (canemail168); No Pr

Status	Next Status	Action	User	Group(s)	Local Client Time	Affected Object	Details	Comments	User(s) Notified
							(pauline.bz.cai@foxconn.com);Wu, Leon (leon.l.wu@foxconn.com);Ren, Ping (ping.p.ren@foxconn.com);Lin, Sara (sara.lin);Kwok, Mandy (mandykwok1);Fu, Danielle (daniellefu);Feng, Ze-song (ze-song.feng@foxconn.com);Huang, Anna (anna.wh.huang@foxconn.com);Wu, Candice (candice.he.wu@foxconn.com);Zhou, Alin (alin.l.zhou1);Chang, Christine (christine.chang@foxconn.com);Hu, Marco (marcofhu);Wu, Angel (juchi.wu@foxconn.com);Lu, Kelvin (kelvinluc);Chang, Jui chuan (ray.jc.chang@foxconn.com);Xu, Tina (tinaxu168);Li, Eagle (eagle.hh.li@foxconn.com);Liang, Kim (kim.q.liang@foxconn.com);Lee, Peter (peter-2345);Tsai, Claire (claire.tsai@foxconn.com) with priority Regular		Wang, Feirong (feirwang);Lin, Privilege; No Privilege; Hu, Kiwi (kiwihuang5107); No Privilege; No Privilege; Li, (boblee); No Privilege; Chei Annie (annie.ys.chen@foxconn.com); Fan, Tracy (cui-xia.fan@mail.foxconn.com) Pauline (pauline.bz.cai@foxconn.com);Lin, Privilege; No Privilege; Lin (sara.lin); Kwok, Mandy (mandykwok1); Fu, Danielle (daniellefu); No Privilege; No Privilege; Zhe (alin.l.zhou1); Chang, Chris (christine.chang@foxconn.com); Hu, Marco (marcofhu); Wu, Angel (juchi.wu@foxconn.com); Lu, Kelvin (kelvinluc); No Privilege; Xu, Tina (tinaxu168); No Privilege; Lee (peter-2345); Tsai, Claire (claire.tsai@foxconn.com)
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		SetDeviationVisibility: Deviation Visibility updated successfully		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Release		Modify	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		<Page Two.Visibility>was<>is<Foxconn-China-BV (FOC),FOXCONN-LONGHUA-INC (FOL)>		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		SetDeviationEffectiveDates: Deviation Effective Dates automated successfully		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:23 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Release		Modify	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		<Cover Page.Effective To>was<>is<2020-10-11 07:00:00 GMT>		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Release		Modify	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		<Cover Page.Effective From>was<>is<2020-08-12 07:00:00 GMT>		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Release		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
CCB Review	Release	Change Status	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		Deviation - Prototype.CCB Review=>Deviation - Prototype.Release		Hoang, Daniel (danhoang)
CCB Review		Approve	Hoang, Daniel (danhoang)		08/12/2020 10:59:22 PM PDT		Hoang, Daniel Approved (without password) for Hoang, Daniel		
CCB Review		Add to approvers list	Hoang, Daniel (danhoang)		08/12/2020 10:58:28 PM PDT		(Added to status - CCB Review) Hoang, Daniel (danhoang)		
Pending	CCB Review	Change Status	Hoang, Daniel (danhoang)		08/12/2020 10:58:28 PM PDT		Deviation - Prototype.Pending=>Deviation - Prototype.CCB Review		Hoang, Daniel (danhoang)
Pending		Add file attachment	Hoang, Daniel (danhoang)		08/12/2020 10:55:38 PM PDT		FOLDER1336675 V 1 Updated P3B Churchill CPU manual programming instructions.docx		
Pending		Remove file attachment	Hoang, Daniel (danhoang)		08/12/2020 10:54:33 PM PDT		FOLDER1336558 V 1 P3B Churchill CPU manual programming instructions.docx		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:45:52 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Add file attachment	Hoang, Daniel (danhoang)		08/12/2020 12:55:20 PM PDT		FOLDER1336558 V 1 P3B Churchill CPU manual programming instructions.docx		
Pending		Remove file attachment	Hoang, Daniel (danhoang)		08/12/2020 12:53:11 PM PDT		FOLDER1335008 V 1 P4 Churchill FOC manual programming instructions.docx		
Pending		Add file attachment	Hoang, Daniel (danhoang)		08/12/2020 12:52:50 PM PDT		FOLDER1336557 V 1 P3B MB TI JMAC Programming Instruction.docx		
Pending		Print Tab	Hoang, Daniel (danhoang)		08/12/2020 10:22:28 AM PDT		Page Two		

Status	Next Status	Action	User	Group(s)	Local Client Time	Affected Object	Details	Comments	User(s) Notified
Pending		Print Tab	Hoang, Daniel (danhoang)		08/12/2020 10:22:28 AM PDT		Cover Page		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:18:18 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/12/2020 10:16:13 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/10/2020 04:17:24 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/10/2020 04:07:23 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/10/2020 10:12:26 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Print Tab	Hoang, Daniel (danhoang)		08/09/2020 11:22:54 AM PDT		Page Two		
Pending		Print Tab	Hoang, Daniel (danhoang)		08/09/2020 11:22:54 AM PDT		Cover Page		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/09/2020 11:22:30 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/09/2020 11:19:29 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Print Tab	Hoang, Daniel (danhoang)		08/07/2020 07:20:55 PM PDT		Page Two		
Pending		Print Tab	Hoang, Daniel (danhoang)		08/07/2020 07:20:55 PM PDT		Cover Page		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/07/2020 07:20:00 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Print Tab	Fong, Bill (bfong)		08/07/2020 12:22:24 PM PDT		Cover Page;Page Two;Affected Items;Workflow;Relationships;Attachments;History;		
Pending		Export	Fong, Bill (bfong)		08/07/2020 12:18:21 PM PDT				
Pending		Export	Fong, Bill (bfong)		08/07/2020 12:16:27 PM PDT				
Pending		Print Tab	Fong, Bill (bfong)		08/07/2020 12:14:07 PM PDT		Cover Page;Page Two;Affected Items;Workflow;Relationships;Attachments;History;		
Pending		Print Tab	Fong, Bill (bfong)		08/07/2020 12:13:58 PM PDT		Cover Page;Page Two;Affected Items;Workflow;Relationships;Attachments;History;		
Pending		Print Tab	Fong, Bill (bfong)		08/07/2020 12:12:58 PM PDT		Cover Page;Page Two;Affected Items;Workflow;Relationships;Attachments;History;		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/07/2020 09:40:10 AM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 07:30:19 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 04:20:23 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 04:19:13 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Add file attachment	Hoang, Daniel (danhoang)		08/06/2020 04:13:59 PM PDT		FOLDER1335012 V 1 NG2 BB PA Board Test Plan 20200703.pptx		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 04:08:29 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		Add file attachment	Hoang, Daniel (danhoang)		08/06/2020 04:02:46 PM PDT		FOLDER1335008 V 1 P4 Churchill FOC manual programming instructions.docx		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 04:00:10 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 03:58:51 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 03:54:44 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 03:53:43 PM PDT		ChangeOrderUpdateTitleBlock: DP513757: No Action required		

Status	Next Status	Action	User	Group(s)	Local Client Time	Affected Object	Details	Comments	User(s) Notified
Pending		User Defined Action	Hoang, Daniel (danhoang)		08/06/2020 03:52:36 PM PDT		ChangeOrderUpdateTitleBlock:: DP513757: No Action required		
Unassigned	Pending	Change Status	Hoang, Daniel (danhoang)		08/06/2020 03:52:36 PM PDT		Unassigned ==> Deviation - Prototype.Pending		
Unassigned		Create	Hoang, Daniel (danhoang)		08/06/2020 03:43:07 PM PDT				

Printed by: Wang, Alice (alicewang168) on 08/13/2020 02:27:15 AM PDT