```
LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x03);
//GIP
LCD_Gen_write_1A_1P(0x01,0x00);
LCD_Gen_write_1A_1P(0x02,0x00);
LCD_Gen_write_1A_1P(0x03,0x73);
LCD_Gen_write_1A_1P(0x04,0xD3);
LCD_Gen_write_1A_1P(0x05,0x00);
LCD_Gen_write_1A_1P(0x06,0x0A);
LCD_Gen_write_1A_1P(0x07,0x0F);
LCD_Gen_write_1A_1P(0x08,0x00);

LCD_Gen_write_1A_1P(0x09,0x01);
LCD_Gen_write_1A_1P(0x0A,0x01);
LCD_Gen_write_1A_1P(0x0B,0x01);
LCD_Gen_write_1A_1P(0x0C,0x01);
LCD_Gen_write_1A_1P(0x0D,0x01);
LCD_Gen_write_1A_1P(0x0E,0x01);

LCD_Gen_write_1A_1P(0x0F,0x19);

LCD_Gen_write_1A_1P(0x10,0x19);

LCD_Gen_write_1A_1P(0x11,0x00);
LCD_Gen_write_1A_1P(0x12,0x00);
LCD_Gen_write_1A_1P(0x13,0x00);
LCD_Gen_write_1A_1P(0x14,0x00);
LCD_Gen_write_1A_1P(0x15,0x00);

LCD_Gen_write_1A_1P(0x16,0x00);
LCD_Gen_write_1A_1P(0x17,0x00);
LCD_Gen_write_1A_1P(0x18,0x00);
LCD_Gen_write_1A_1P(0x19,0x00);
LCD_Gen_write_1A_1P(0x1A,0x00);
LCD_Gen_write_1A_1P(0x1B,0x00);
LCD_Gen_write_1A_1P(0x1C,0x00);
LCD_Gen_write_1A_1P(0x1D,0x00);
LCD_Gen_write_1A_1P(0x1E,0x40);
LCD_Gen_write_1A_1P(0x1F,0x80);
LCD_Gen_write_1A_1P(0x20,0x06);
LCD_Gen_write_1A_1P(0x21,0x01);
LCD_Gen_write_1A_1P(0x22,0x00);

LCD_Gen_write_1A_1P(0x23,0x00);
LCD_Gen_write_1A_1P(0x24,0x00);
LCD_Gen_write_1A_1P(0x25,0x00);
```

```
LCD_Gen_write_1A_1P(0x26,0x00);
LCD_Gen_write_1A_1P(0x27,0x00);
LCD_Gen_write_1A_1P(0x28,0x33);
LCD_Gen_write_1A_1P(0x29,0x03);
LCD_Gen_write_1A_1P(0x2A,0x00);
LCD_Gen_write_1A_1P(0x2B,0x00);
LCD_Gen_write_1A_1P(0x2C,0x00);
LCD_Gen_write_1A_1P(0x2D,0x00);
LCD_Gen_write_1A_1P(0x2E,0x00);
LCD_Gen_write_1A_1P(0x2F,0x00);

LCD_Gen_write_1A_1P(0x30,0x00);
LCD_Gen_write_1A_1P(0x31,0x00);
LCD_Gen_write_1A_1P(0x32,0x00);
LCD_Gen_write_1A_1P(0x33,0x00);
LCD_Gen_write_1A_1P(0x34,0x03);
LCD_Gen_write_1A_1P(0x35,0x00);
LCD_Gen_write_1A_1P(0x36,0x03);
LCD_Gen_write_1A_1P(0x37,0x00);
LCD_Gen_write_1A_1P(0x38,0x00);
LCD_Gen_write_1A_1P(0x39,0x00);
LCD_Gen_write_1A_1P(0x3A,0x00);
LCD_Gen_write_1A_1P(0x3B,0x00);
LCD_Gen_write_1A_1P(0x3C,0x00);

LCD_Gen_write_1A_1P(0x3D,0x00);
LCD_Gen_write_1A_1P(0x3E,0x00);
LCD_Gen_write_1A_1P(0x3F,0x00);
LCD_Gen_write_1A_1P(0x40,0x00);
LCD_Gen_write_1A_1P(0x41,0x00);
LCD_Gen_write_1A_1P(0x42,0x00);
LCD_Gen_write_1A_1P(0x43,0x00);
LCD_Gen_write_1A_1P(0x44,0x00);

//-----------GIP_2--------------
LCD_Gen_write_1A_1P(0x50,0x01);
LCD_Gen_write_1A_1P(0x51,0x23);
LCD_Gen_write_1A_1P(0x52,0x45);
LCD_Gen_write_1A_1P(0x53,0x67);
LCD_Gen_write_1A_1P(0x54,0x89);
LCD_Gen_write_1A_1P(0x55,0xAB);
LCD_Gen_write_1A_1P(0x56,0x01);
LCD_Gen_write_1A_1P(0x57,0x23);
LCD_Gen_write_1A_1P(0x58,0x45);
LCD_Gen_write_1A_1P(0x59,0x67);
```

```
LCD_Gen_write_1A_1P(0x5A,0x89);
LCD_Gen_write_1A_1P(0x5B,0xAB);
LCD_Gen_write_1A_1P(0x5C,0xCD);
LCD_Gen_write_1A_1P(0x5D,0xEF);

LCD_Gen_write_1A_1P(0x5E,0x11);          //01
LCD_Gen_write_1A_1P(0x5F,0x08);
LCD_Gen_write_1A_1P(0x60,0x02);
LCD_Gen_write_1A_1P(0x61,0x00);
LCD_Gen_write_1A_1P(0x62,0x01);
LCD_Gen_write_1A_1P(0x63,0x0D);
LCD_Gen_write_1A_1P(0x64,0x0C);
LCD_Gen_write_1A_1P(0x65,0x02);
LCD_Gen_write_1A_1P(0x66,0x02);
LCD_Gen_write_1A_1P(0x67,0x02);
LCD_Gen_write_1A_1P(0x68,0x02);
LCD_Gen_write_1A_1P(0x69,0x02);
LCD_Gen_write_1A_1P(0x6A,0x02);
LCD_Gen_write_1A_1P(0x6B,0x0F);
LCD_Gen_write_1A_1P(0x6C,0x02);
LCD_Gen_write_1A_1P(0x6D,0x02);
LCD_Gen_write_1A_1P(0x6E,0x02);
LCD_Gen_write_1A_1P(0x6F,0x02);

LCD_Gen_write_1A_1P(0x70,0x02);
LCD_Gen_write_1A_1P(0x71,0x02);
LCD_Gen_write_1A_1P(0x72,0x0E);
LCD_Gen_write_1A_1P(0x73,0x06);
LCD_Gen_write_1A_1P(0x74,0x07);
LCD_Gen_write_1A_1P(0x75,0x08);
LCD_Gen_write_1A_1P(0x76,0x02);
LCD_Gen_write_1A_1P(0x77,0x00);
LCD_Gen_write_1A_1P(0x78,0x01);
LCD_Gen_write_1A_1P(0x79,0x0D);
LCD_Gen_write_1A_1P(0x7A,0x0C);
LCD_Gen_write_1A_1P(0x7B,0x02);
LCD_Gen_write_1A_1P(0x7C,0x02);
LCD_Gen_write_1A_1P(0x7D,0x02);
LCD_Gen_write_1A_1P(0x7E,0x02);
LCD_Gen_write_1A_1P(0x7F,0x02);
LCD_Gen_write_1A_1P(0x80,0x02);
LCD_Gen_write_1A_1P(0x81,0x0F);
LCD_Gen_write_1A_1P(0x82,0x02);
LCD_Gen_write_1A_1P(0x83,0x02);
LCD_Gen_write_1A_1P(0x84,0x02);
```

```
LCD_Gen_write_1A_1P(0x85,0x02);
LCD_Gen_write_1A_1P(0x86,0x02);
LCD_Gen_write_1A_1P(0x87,0x02);
LCD_Gen_write_1A_1P(0x88,0x0E);
LCD_Gen_write_1A_1P(0x89,0x06);
LCD_Gen_write_1A_1P(0x8A,0x07);

LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x01);
LCD_Gen_write_1A_1P(0x2E,0x50);


LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x04);
LCD_Gen_write_1A_1P(0x6C,0x15);

LCD_Gen_write_1A_1P(0x6E,0x2A);

LCD_Gen_write_1A_1P(0x6F,0x33);

LCD_Gen_write_1A_1P(0x8D,0x15);

LCD_Gen_write_1A_1P(0x3A,0xA4);

LCD_Gen_write_1A_1P(0x87,0xBA);




LCD_Gen_write_1A_1P(0x26,0x76);

LCD_Gen_write_1A_1P(0xB2,0xD1);

//          LCD_Gen_write_1A_1P(0x00,0x00);


LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x01);
LCD_Gen_write_1A_1P(0x22,0x0A);

LCD_Gen_write_1A_1P(0x31,0x00);

LCD_Gen_write_1A_1P(0x53,0x57);

LCD_Gen_write_1A_1P(0x55,0x61);

LCD_Gen_write_1A_1P(0x50,0x9F);
```

```
LCD_Gen_write_1A_1P(0x51,0x9A);

LCD_Gen_write_1A_1P(0x60,0x0C);



LCD_Gen_write_1A_1P(0xA0,0x00);

LCD_Gen_write_1A_1P(0xA1,0x22);

LCD_Gen_write_1A_1P(0xA2,0x33);

LCD_Gen_write_1A_1P(0xA3,0x13);

LCD_Gen_write_1A_1P(0xA4,0x17);

LCD_Gen_write_1A_1P(0xA5,0x2B);

LCD_Gen_write_1A_1P(0xA6,0x1F);

LCD_Gen_write_1A_1P(0xA7,0x1F);

LCD_Gen_write_1A_1P(0xA8,0xAD);

LCD_Gen_write_1A_1P(0xA9,0x1E);

LCD_Gen_write_1A_1P(0xAA,0x29);

LCD_Gen_write_1A_1P(0xAB,0x9D);

LCD_Gen_write_1A_1P(0xAC,0x1D);

LCD_Gen_write_1A_1P(0xAD,0x1B);

LCD_Gen_write_1A_1P(0xAE,0x4F);

LCD_Gen_write_1A_1P(0xAF,0x22);

LCD_Gen_write_1A_1P(0xB0,0x2A);

LCD_Gen_write_1A_1P(0xB1,0x5A);
```

```
LCD_Gen_write_1A_1P(0xB2,0x67);

LCD_Gen_write_1A_1P(0xB3,0x3F);

LCD_Gen_write_1A_1P(0xC0,0x00);
LCD_Gen_write_1A_1P(0xC1,0x22);
LCD_Gen_write_1A_1P(0xC2,0x33);
LCD_Gen_write_1A_1P(0xC3,0x13);
LCD_Gen_write_1A_1P(0xC4,0x17);
LCD_Gen_write_1A_1P(0xC5,0x2B);
LCD_Gen_write_1A_1P(0xC6,0x1F);
LCD_Gen_write_1A_1P(0xC7,0x1F);
LCD_Gen_write_1A_1P(0xC8,0xAD);
LCD_Gen_write_1A_1P(0xC9,0x1E);
LCD_Gen_write_1A_1P(0xCA,0x29);
LCD_Gen_write_1A_1P(0xCB,0x9D);
LCD_Gen_write_1A_1P(0xCC,0x1D);
LCD_Gen_write_1A_1P(0xCD,0x1B);
LCD_Gen_write_1A_1P(0xCE,0x4F);
LCD_Gen_write_1A_1P(0xCF,0x22);
LCD_Gen_write_1A_1P(0xD0,0x2A);
LCD_Gen_write_1A_1P(0xD1,0x5A);
LCD_Gen_write_1A_1P(0xD2,0x67);
LCD_Gen_write_1A_1P(0xD3,0x3F);

LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x00);
//LCD_Gen_write_1A_1P(0x51,0xFF);
//LCD_Gen_write_1A_1P(0x53,0x2C);
LCD_Gen_write_1A_0P(0x35);




LCD_Gen_write_1A_3P(0xFF,0x98,0x81,0x01);
LCD_Gen_write_1A_1P(0xb7,0x03);




LCD_DCS_write_1A_0P(0x11);
Delay_ms(200);
LCD_DCS_write_1A_0P(0x29);
```

```
Delay_ms(10);
```