

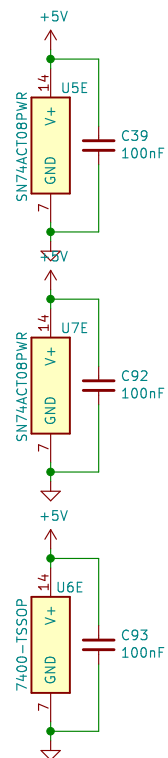
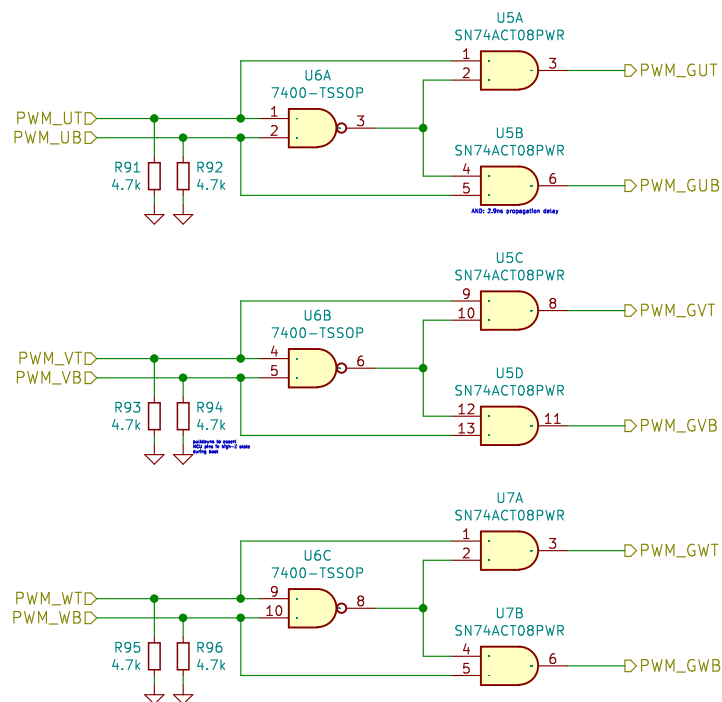
Sheet: /io/
File: Hesc io.sch

Title: HESC V1

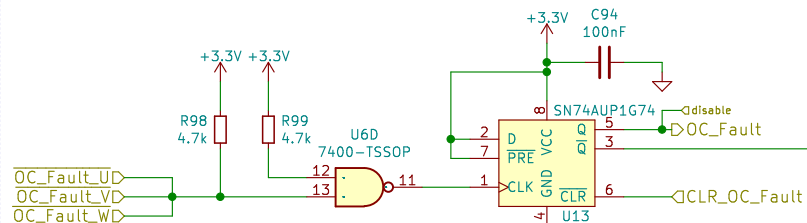
Size: A4 | Date: 14.7.2019
KiCad E.D.A. eeschema (5.1.2)-2

Rev: Init
Id: 3/6

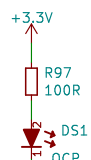
PWM overlap elimination



Overcurrent latch



INPUTS				OUTPUTS	
PRE	CLR	CLK	D	Q	Q̄
L	H	X	X	H	L
X	L	X	X	L	H
H	H	1	H	H	L
H	H	1	L	L	H
H	H	L	X	Q ₀	Q̄ ₀



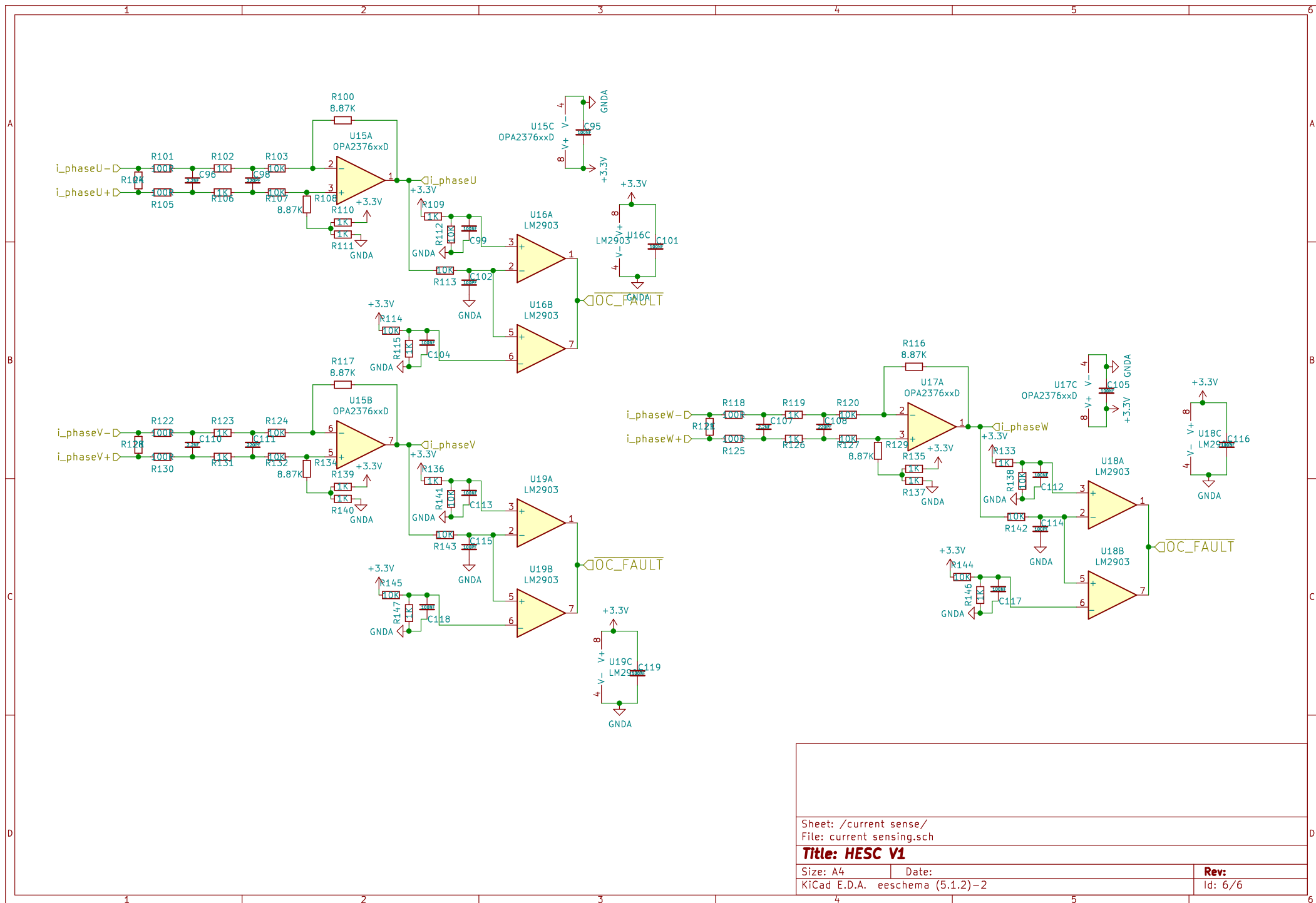
Sheet: /supervisor/
File: Hesc-supervisor.sch

Title: HESC V1

Size: A4 Date: 2017-02-22

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Rev:
Id: 5/6



Sheet: /current sense/
File: current sensing.sch

Title: HESC V1

Size: A4

Date:

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Rev:

Id: 6/6