

Asaf Ahmad Shayaan

Enthusiastic software developer eager to contribute to team success through hard work, attention to detail and excellent organizational skills. Clear understanding of Java and Algorithms. Motivated to learn, grow and excel in Information Technology industry.



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India



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SKILLS

Java



Spring Boot



C programming language



SQL



Object Oriented Programming



Android Development



Spring MVC



Algorithms



LANGUAGES

English

Native or Bilingual Proficiency

Hindi

Full Professional Proficiency

INTERESTS

Chess

Art

Computer Gaming

Football

EDUCATION

(B.E) Birla Institute of Technology and Science, Pilani

08/2016 - 07/2020

GPA - 6.54

Courses

- Object Oriented Programming
- Data Structure and Algorithm
- Digital Image Processing
- Operating Systems

Intermediate

D.P.S Gaya

04/2013 - 03/2015

Percentage - 93.4

WORK EXPERIENCE

Project Build - Trainee Nucleus Software Exports Ltd.

07/2019 - 12/2019

Noida, IN

Tasks

- Developed a plugin in Eclipse using JDT and IntelliJ using PSI along with recursion algorithms which enabled the developer/tester to traverse through a Spring Boot application layer by layer(Controller, Service and Dao).
- Developed a java application to parse sql script file using ANTLR tool and make an excel file using the script file. The application handled all types of sql queries and made modifications in the corresponding excel file accordingly.
- Made UI modifications in an existing Spring Boot application using jqgrid plugin of JQuery.

Web Developer Intern Aditya Birla Insulators

05/2018 - 07/2018

Howrah, IN

Tasks

- Developed the back-end of a business portal using JSP,Servlets and Java for automating the order placing process.

PROJECTS

Analysis of films in the light of Child Abuse (02/2020 - 05/2020)

- Submitted a thirty-three page analysis of two movies named Capernaum and Ugly.

Matlab GUI

- Developed Matlab GUI APP with specification and technical details of Wireless Cellular Generations 1G to 5G and WLAN 802.11 Wifi standards

Logic function realization using transmission gates logic style with a load capacitance of 100fF (09/2018 - 10/2018)

- Used Cadence software to implement boolean function $F = AB + AD + BD + CD$.
- Did time analysis and calculated parameters such as high to low propagation delay and total propagation delay.

ACHIEVEMENTS

Joint Entrance Exam Advanced Rank - 5240

International Mathematics Olympiad (2009) Rank - 608