

Title: *		Waterloo
Size: A4	Author: *	200 University Ave W
Date: 2023-03-31 Revision: *	Sheet 1 of 5	Engineering 5, 1104
File: Monitoring IC.SchDoc		Waterloo, ON N2L 3G1

A

A

B

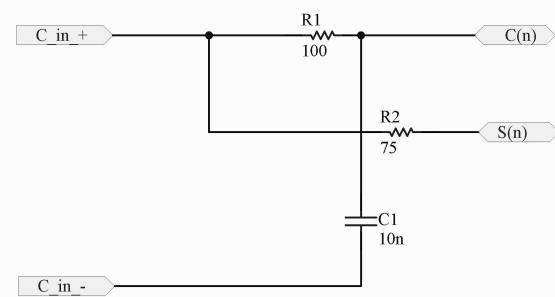
B

C

C

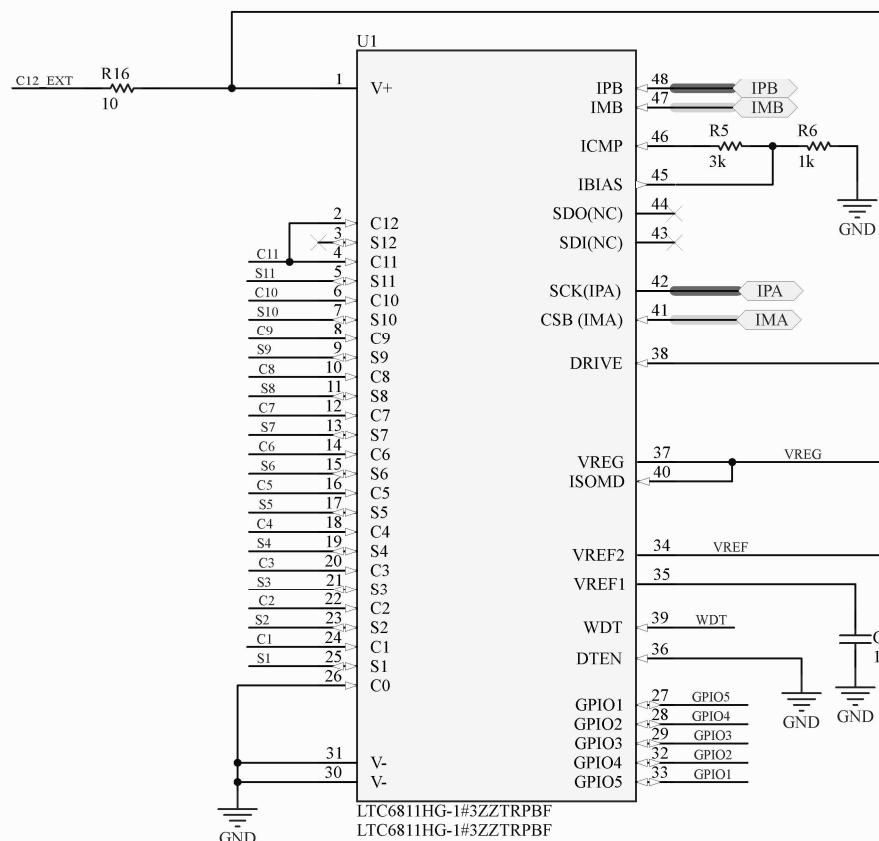
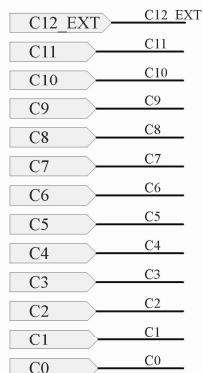
D

D

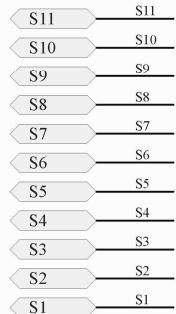


Title: *		Waterloo 200 University Ave W Engineering 5, 1104 Waterloo, ON N2L 3G1
Size: A4	Author: *	
Date: 2023-03-31	Revision: *	Sheet 2 of 5
File: Cell Balancing.SchDoc		

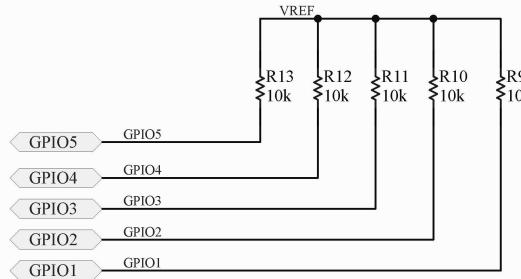
## Cell Inputs



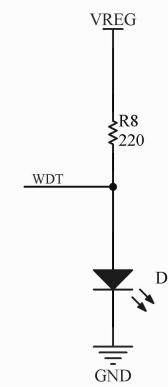
## S Pin



## GPIO INPUTS

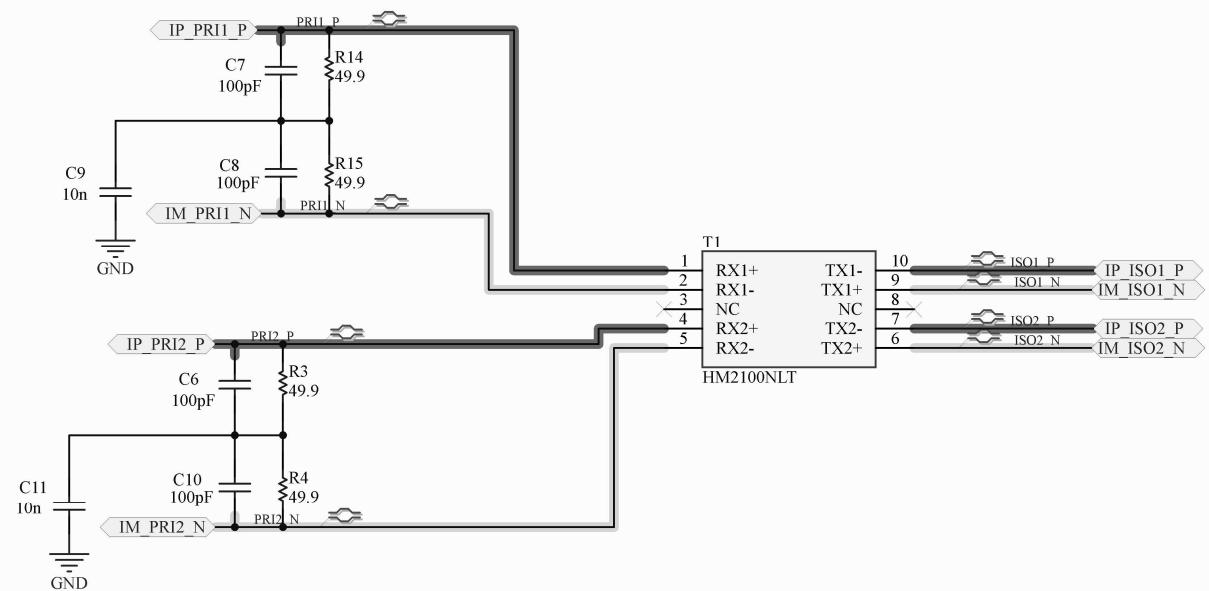


## WDT LED



Title: *	Waterloo
Size: A4	Author: *
Date: 2023-03-31	Revision: *
File: LTC6811_Sheet.SchDoc	Sheet 3 of 5 N2L 3G1

A



B

A

B

C

C

D

D

Title: *		Waterloop
200 University Ave W		Engineering 5, 1104
Date: 2023-03-31 Revision: *		Sheet 4 of 5 N2L 3G1
Size: A4	Author: *	<b>WATERLOOP</b>
File: Transformer Stage.SchDoc		

A

A

B

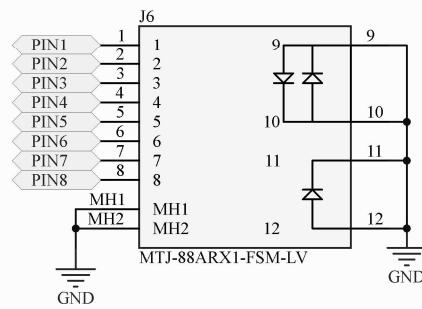
B

C

C

D

D



Title: *	Waterloo
Size: A4	Engineering 5, 1104 Waterloo, ON
Date: 2023-03-31	Revision: * Sheet 5 of 5
File: RJ45 Connector.SchDoc	N2L 3G1

# Waterloop - BMS DISCHARGING BOARD

Adesh P Singh

