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Roll No.

Paper Code: DCS-216

ADCA-11 2nd Year Examination, Academic Batch 2016-17 Computer Organization

Time: 3 Hours [Max. Marks: 100

Note. Attempt any *Five* questions. All questions carry equal marks.

- Q1. a) Explain in detail all the types of Adders and Subtractors? How a full adder is built using half adder
- b) Explain all type of counters with their block diagram.
- Q2. a) Explain the role of fetch, decode and execution in instruction cycle. Draw the flow chart.
- b) Explain Micro programmed and hard wired control unit. Draw the block diagram of control unit.
- Q3. Explain in detail about
- a) Auxiliary memory
- b) Associative memory
- Q4. Explain the functional diagram of 16- bit Micro processor (8086).
- Q.5.Draw and explain the working of a circulating registers or ringing counter with the help of timing diagram?
- Q.6. Write a short note on the following:
- a. Programmable counter
- b. Shift Register
- Q.7. What are the different types of instructions? Is there any impact of addressing modes in instruction set design of a machine?
- Q.8. Write short note on the data manipulation instructions?