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BCA-7

1st Year Examination, Calendar Batch 2017 Computer Organization

Time: 3 Hours] [Max. Marks: 100

Note. Attempt any *Five* questions. All questions carry equal marks.

- Q.1 Explain the role of fetch, decode and execution in instruction cycle. Draw the flow chart?
- Q.2 Explain in detail all the types of Adders and Subtractors? How a full adder is built using half adder.
- Q.3 What is the basic advantage of priority interrupt over a non priority interrupt?
- Q.4 Explain how S- R flip-flop is realized by NOR Gate and by NAND gate.
- Q.5 Explain the functional diagram of 16- bit Micro processor (8086).
- Q.6 Explain all type of counters with their block diagram.

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- Q.7 Explain instruction format. What are the various types of instruction format? Discuss them with suitable example?
- Q.8 What are Logic gates? Explain in detail all type of logic Gates and design AND, OR, NOT gates using universal gates.

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