

GENERAL INFORMATION:

SIMPLWINDOWS NAME:	Qsys Core
CATEGORY:	Qsys
VERSION:	4.1.1
SUMMARY:	<p>Multiple core modules can be used in the same program slot.</p> <p>This module has a built in reconnect feature.</p>
GENERAL NOTES:	N/A
CRESTRON HARDWARE REQUIRED:	3 Series Processor or 4 Series Processor
SETUP OF CRESTRON HARDWARE:	TCP managed inside the module.
VENDOR FIRMWARE:	N/A
VENDOR SETUP:	N/A

CONTROL:

Initialize	D	Pulse to Initialize connection to the Q-SYS Core. Pulse this after DeploymentHost, TestingHost, DeploymentMode, and UseExternalConnection have been set to their desired states.
DeploymentHost	S	IP Address or Hostname of the Q-SYS Core to connect to for Deployment. This value will be used when DeploymentMode is 1.
[TestingHost]	S	IP Address or Hostname of the Q-SYS Core to connect to for Testing. This value will be used when DeploymentMode is 0.
DeploymentMode	A	<p>Specifies whether to use DeploymentHost or Testing Host.</p> <p>1: DeploymentHost 0: TestingHost</p>
[Debug]	A	<p>Specifies the Debug level for the program</p> <p>0: Disabled 1: Logging Enabled 2: Debug Enabled 3: All Enabled</p> <p>Debug prints to Console Output.</p>
[UseExternalConnection]	D	Raise to handle TCP connection outside of this module.
[ExternalConnectionRx]	S	Rx from the external TCP socket.

FEEDBACK:

[IsInitialized]	D	Signal used to show if the Q-SYS Core has been initialized.
[IsConnected]	D	Signal used to show if the Q-SYS Core is communicating.
[IsLoggedIn]	D	Signal used to show if this module is logged in with the Q-SYS Core.
[IsRedundant]	D	Signal used to show if the Q-SYS Core is redundant.
[IsEmulator]	D	Signal used to show if the Q-SYS Core is connected to an emulated file.
[DesignName]	S	Signal used to show the current running design on the Q-SYS Core.
[ExternalConnectionTx]	S	Tx to the external TCP socket.

PARAMETERS:

CoreID	S	Used to Address Core Module to Control Modules
Username	S	Username for the Core (if required)
Password	S	Password for the Core (if required)
Port	Dec	Default: 1710 Can be changed if doing port forwarding to get to a Q-SYS Core

TESTING:

SAMPLE PROGRAM:	3-Series: 3 Series Test.smw 4-Series: 4 Series Test.smw
REVISION HISTORY:	