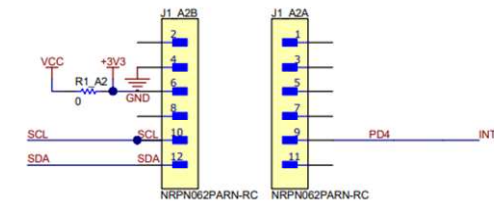
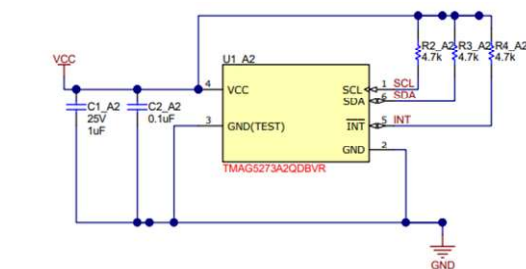


PCB
LOGO
Texas Instruments



PCB
LOGO
WEEE logo

Figure 6-1. TMAG5273A1 Part of EVM Schematic



PCB
LOGO
Texas Instruments



PCB
LOGO
WEEE logo

Figure 6-2. TMAG5273A2 Part of EVM Schematic

Table 16. Pin assignment of the ST morpho connectors

CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin nbr	Pin name	Pin nbr	Pin name ⁽¹⁾	Pin nbr	Pin name ⁽¹⁾	Pin nbr	Pin name
1	PC10	2	PC11	1	PC9	2	PC8
3	PC12	4	PD2	3	PB8	4	PC6
5	VDD	6	E5V	5	PB9	6	PC5
7	BOOT0 ⁽²⁾	8	GND	7	VREFP ⁽³⁾	8	5V_USB_CHGR ⁽⁴⁾
9	NC	10	NC	9	GND	10	NC
11	NC	12	IOREF	11	PA5	12	PA12
13	PA13 ⁽⁵⁾	14	NRST	13	PA6	14	PA11
15	PA14 ⁽⁵⁾	16	PA15	15	PA7	16	PB12
17	PA15	18	PA15	17	PB6	18	PB11

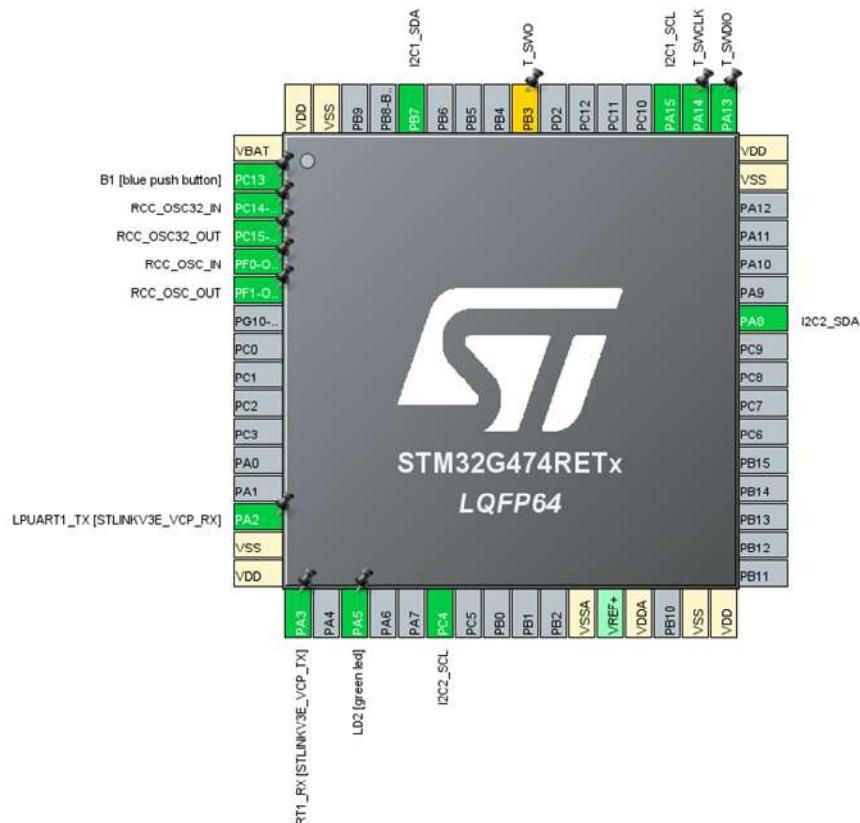
CN7 odd pins		CN7 even pins		CN10 odd pins		CN10 even pins	
Pin nbr	Pin name	Pin nbr	Pin name ⁽¹⁾	Pin nbr	Pin name ⁽¹⁾	Pin nbr	Pin name
19	GND	20	PC7	19	PC7	20	GND
21	PB7	22	PA9	21	PA9	22	PB8
23	PC13	24	VIN	23	PA8	24	PB9
25	PC14	26	NC	25	PB10	26	PB15
27	PC15	28	PA0	27	PB4	28	PB14
29	PF0	30	PA1	29	PB5	30	PB13
31	PF1	32	PA4	31	PB3	32	AGND
33	VBAT	34	PB0	33	PA10	34	PC4
35	PC2	36	PC1/PB9	35	PA2 / PC4	36	NC
37	PC3	38	PC0/PA15	37	PA3 / PC5	38	NC

TMAG5273A
 1 SCL
 1 INT

TMAG5273A
 1 SDA

TMAG5273A
 2 SDA

TMAG5273A
 2 SCL



6.2 PCB Layout

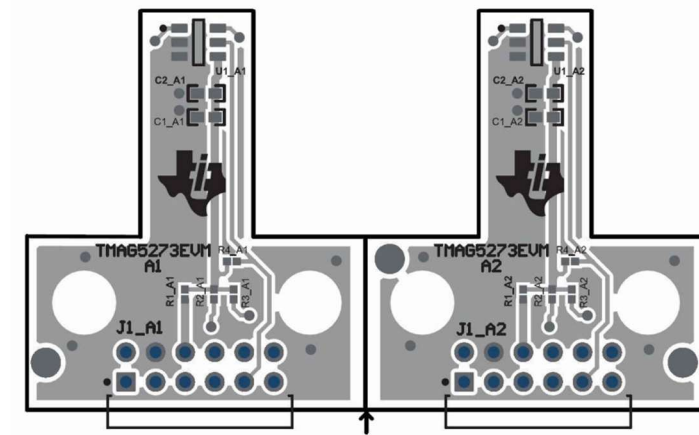


Figure 6-7. TMAG5273EVM Top View

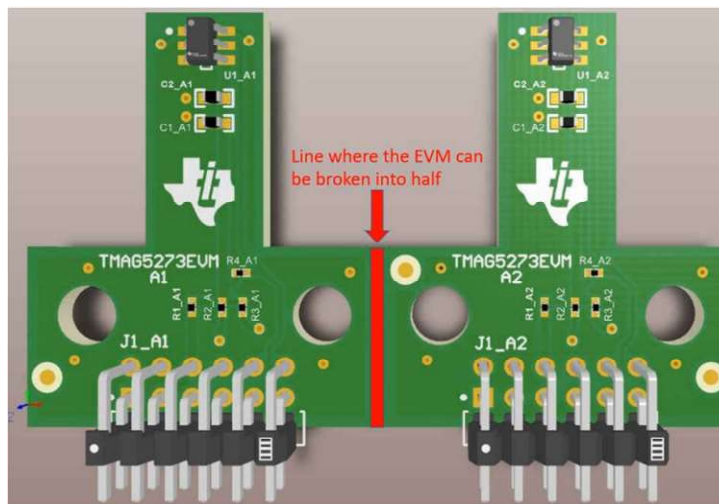


Figure 3-1. TMAG5x73 EVM With A1 and A2 Parts of Board Still Joined Together