Gaurav Khadasane

Hardware Engineer – PCB Design

- Requirement Analysis, Specification Document Creation, Proposal Document creation.
- Experience as a Hardware board Design, High Speed PCB Design up to 12 layers and Power Devices layouts.
- Cadence Allegro, Proteus, Eagle, Altium Designer, Expedition, Hyperlynx.
- CAM350 for Gerber viewing and editing, View Mate Gerber viewing.
- Team Handling for PCB Design and Layout review for quality delivery.
- Layout timeline estimation and defining WBS for project management.

WORK EXPERIENCE

Motherson Technology and Services| Sr. PCB Design Engineer

April. 2022 – Till Date

- Layout Design for automotive breaking systems of Truck and triales.
- Gerber, Assembly and Fabraction package generation.
- Maintain data on Comapany server and sharepoint.

Technosoft Engineering | Hardware Engineer - NPD

July. 2021 - March. 2022

- Capture Requirement specification to prepare Project Report on requirement as per stakeholders
- Project Scheduling and Monitoring for requirement Phase and Hardware design Phases
- RFQ to fabrication and assembly house for quotation and issues resolve alternative parts and assembly instruction.

Pegasus Infocorp, Client: Johnson Control | Sr Engineer PCB Design

August. 2020 - July. 2021

- PCB Design for new products on fire detection systems.
- Library creation for Altium design, symbol and Footprint w.r.t checklist and IPC Std
- Work Independently and liaise with H/W and Mech Team for overall Product DFM, DFT, DFA

Capgemini | Associate Consultant – PCB Design

Jan. 2019 - Feb. 2020

- High Speed PCB design 6 layers.
- Creating and modifying schematic for Layout changes as per ECO documents.
- Managing product documents for Assembly Fabrication drawing and gerber generation.

Interfab Electronics| Sr Design Engineer – Product Development

Nov. 2015 - Dec. 2018

- Reviewing and finalizing Hardware, Layouts, Footprints.
- New Product development for Industrial clients and their onsite test with certification.
- Component selection and Panalization and Gerber generation of final design.

EDUCATION

PG DIPLOMA Embedded systems, CDAC ,2015
Thakur College.

B.E. Electronics and Telecommunication, Mumbai University, 2014
Yadavrao Tasgaonkar Institute of Engineering and Technology

CONTACT

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- Pune
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SOFTWARE

- Altium Designer 22
- Cadence OrCad 17.2
- Mentor Expedition
- Cadstar
- CAM350

Certification

- ✓ IOT NodeMCU, MicroBytes, Mumbai
- ✓ Hardware Board Design, SchemaZen Technologies, Banglore.

Design Exposure

Processors/Controllers:
ATMEL SAM9 Series processor, Atmega328, i.Max RT series Processor.

IO Peripherals:
10/100Mbps Ethernet Controllers and PHYs, USB Controllers, Transceivers and UART.

Memory Section:
DDR2, NAND Flash, QSPI NOR Flash.

Wireless Module:
Quectel MC60 GSM/GPRS/GPS,Wifi 8255, Bluetooth HC06.

Power Management:
 DC-DC, LDO , Multiple O/P SMPS in reference design from Power Integration Tiny series and STMicroelectronics Viper series.