



CMR COLLEGE OF ENGINEERING & TECHNOLOGY

(AUTONOMOUS)

*Permanently Affiliated to JNTUH *NAAC Accredited with 'A' Grade*
Kandlakoya, Medchal Road, Hyderabad - 501 401, Telangana, INDIA.

CONSOLIDATED MEMORANDUM OF MARKS CREDIT SHEET



CMM. No. : U140491

Serial No. : 214H51040183

Name : DEVANSHU GUPTA

Hall Ticket No. : 14H51A0412



Month & Year of Final Exam May 2018

Class Awarded : FIRST CLASS WITH DISTINCTION

Year of Admission : 2014 - 2015

B.Tech, ELECTRONICS & COMMUNICATION ENGINEERING

S. No.	SUBJECT TITLE	INT	EXT	TOT	CR	S. No.	SUBJECT TITLE	INT	EXT	TOT	CR
I SEMESTER						II SEMESTER					
1	LINEAR ALGEBRA & CALCULUS	29	58	87	4	1	ADVANCED CALCULUS	28	55	83	4
2	ENGINEERING PHYSICS-I	24	41	65	4	2	MATHEMATICAL METHODS	27	54	81	4
3	ENGINEERING CHEMISTRY	27	46	73	4	3	DATA STRUCTURES THROUGH 'C'	29	39	68	4
4	ENGINEERING DRAWING	21	28	49	4	4	ENGINEERING PHYSICS-II	24	52	76	3
5	COMPUTER PROGRAMMING THROUGH 'C'	29	43	72	4	5	ADVANCED ENGINEERING CHEMISTRY	24	35	59	3
6	ENGLISH-I	23	48	71	2	6	ENGLISH LANG. COMM. SKILLS LAB	25	50	75	2
7	ENGINEERING PHYSICS LAB	24	39	63	2	7	ENGLISH-II	30	37	67	2
8	COMPUTER PROGRAMMING LAB	24	50	74	2	8	ENGINEERING CHEMISTRY LAB	24	42	66	2
9	IT WORKSHOP	22	44	66	2	9	ENGINEERING WORKSHOP	18	45	63	2
III SEMESTER						10	DATA STRUCTURES LAB	24	48	72	2
1	SPECIAL FUNCTNS. & COMPLEX ANAL.	29	57	86	4	IV SEMESTER					
2	NETWORK THEORY	26	30	56	4	1	ENVIRONMENTAL STUDIES	27	30	57	4
3	ELECTRONIC DEVICES & CIRCUITS	26	32	58	4	2	CONTROL SYSTEMS	23	43	66	4
4	SIGNALS & SYSTEMS	25	49	74	4	3	ELECTRICAL TECHNOLOGY	25	49	74	4
5	PROB. THEORY & RANDOM PROCESS	25	44	69	4	4	ELECTRONIC CIRCUITS ANALYSIS	29	32	61	4
6	SWITCHING THEORY & LOGIC DESIGN	19	29	48*	4	5	ELECTROMNTC. THEO. & TRANS.LINES	30	30	60	4
7	ELECTRONIC DEVICES & CIR. LAB	25	49	74	2	6	PULSE & DIGITAL CIRCUITS	21	24	45*	4
8	BASIC SIMULATION LAB	25	50	75	2	7	GENDER SENSITIZATION	21	41	62	2
V SEMESTER						8	ELECTNC. CIR. & PULSE CIR. LAB	25	50	75	2
1	LINEAR & DIGITAL IC APPLICATIONS	20	41	61	4	9	ELECTRICAL TECHNOLOGY LAB	25	45	70	2
2	MICROPROCESSORS & MICROCONTROLLERS	24	47	71	4	VI SEMESTER					
3	COMP. ORG. & OPERATING SYSTEMS	25	39	64	4	1	MANAGL. ECONS. & FINAN. ANALYSIS	23	35	58	4
4	ANTENNA & WAVE PROPAGATION	19	53	72	4	2	DIGITAL SIGNAL PROCESSING	25	49	74	4
5	ANALOG COMMUNICATIONS	15	34	49	4	3	OBJECT ORIENTED PROG. THRO. JAVA	24	27	51	4
6	DIGITAL DESIGN USING VERILOG HDL	18	37	55	4	4	DIGITAL COMMUNICATIONS	21	44	65	4
7	IC APPLICTN. & HDL SIMULATN. LAB	25	47	72	2	5	MICROWAVE ENGINEERING	24	33	57	4
8	MICROPRORS. & MICROCONTRS. LAB	25	50	75	2	6	EMBEDDED SYSTEMS DESIGN	17	42	59	4
VII SEMESTER						7	COMMUNICATION LAB	24	46	70	2
1	MANAGEMENT SCIENCE	25	41	66	4	8	DIGITAL SIGNAL PROCESSING LAB	24	47	71	2
2	COMPUTER NETWORKS	29	39	68	4	VIII SEMESTER					
3	VLSI DESIGN	24	30	54	4	1	CELLULAR & MOBILE COMMUNICATIONS	16	39	55	4
4	ELECT. MEASURMNT. & INSTRUMTN.	20	30	50	4	2	RADAR SYSTEMS	24	48	72	4
5	DIGITAL DATA COMMUNICATIONS	23	32	55	4	3	WIRELESS COMMUNC. & NETWORKS	24	41	65	4
6	SATELLITE COMMUNICATIONS	18	31	49	4	4	INDUSTRY ORIENTED MINI PROJECT	-	48	48	2
7	ADV. ENGLISH COMMUNC. SKILLS LAB	24	47	71	2	5	SEMINAR	-	49	49	2
8	VLSI LAB	24	44	68	2	6	COMPREHENSIVE VIVA-VOCE	-	89	89	2
						7	MAIN PROJECT	59	130	189	10

No. of Credits Registered for: 226, Aggregate Marks Secured for best: 218

[Theory: Internal=30, External=70] [Lab: Internal=25, External=50] [Project: Internal=60, External=140]



Aggregate Marks Secured :

4419 OUT OF 6050 (73.04%)

Date of Issue :

June 29, 2018

(*Courses registered but not counted for calculation of aggregate)

CONTROLLER OF EXAMINATIONS

CMR COLLEGE OF ENGG & TECH. (A)