



DUAL CIRCUITS

[illegible]

- a. Demonstrates understanding of the previous exercises including short field take-offs and landings, and low-level circuits.
- b. Speeds in all phases of the circuit must be maintained as accurately as possible.

Total	Dual	Solo	IF	X/C	X/C Solo	Ldgs
19:45	0:45					4
	0:45					4/1 GA

Student Signature

Instructor Signature