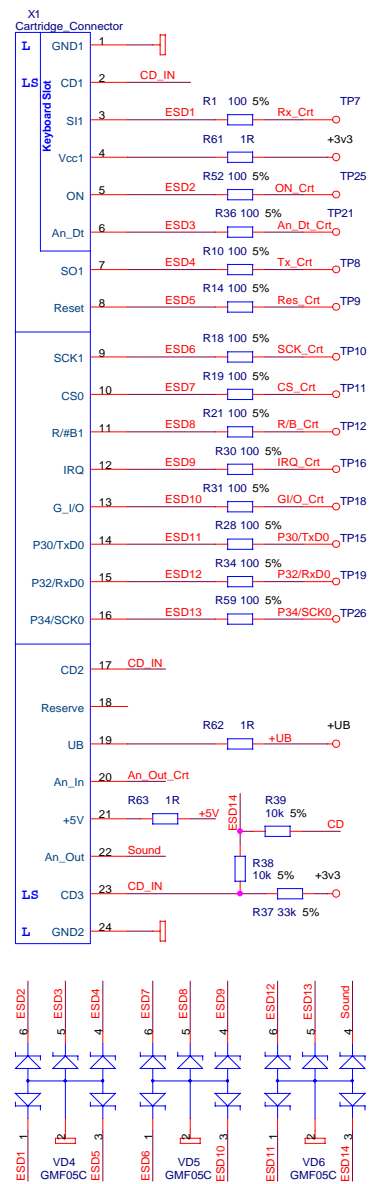


Paths GND, +UB, +3V3 should be 0.8mm wide.
One Via-hole should be for each 0.4 mm of path's wide.
R46 for flash MCU only.



Long pins: GND1, GND2.
Long-Short pin: CD1, CD3.

