

Primeasia University
Department of EEE
EEE-4135/CSE-403 VLSI

Experiment no. 5

Experiment Name: Designing 2 input CMOS OR gate using MICROWIND.

Objective: To design CMOS OR gate and see the performance and output wave shape.

Theory:

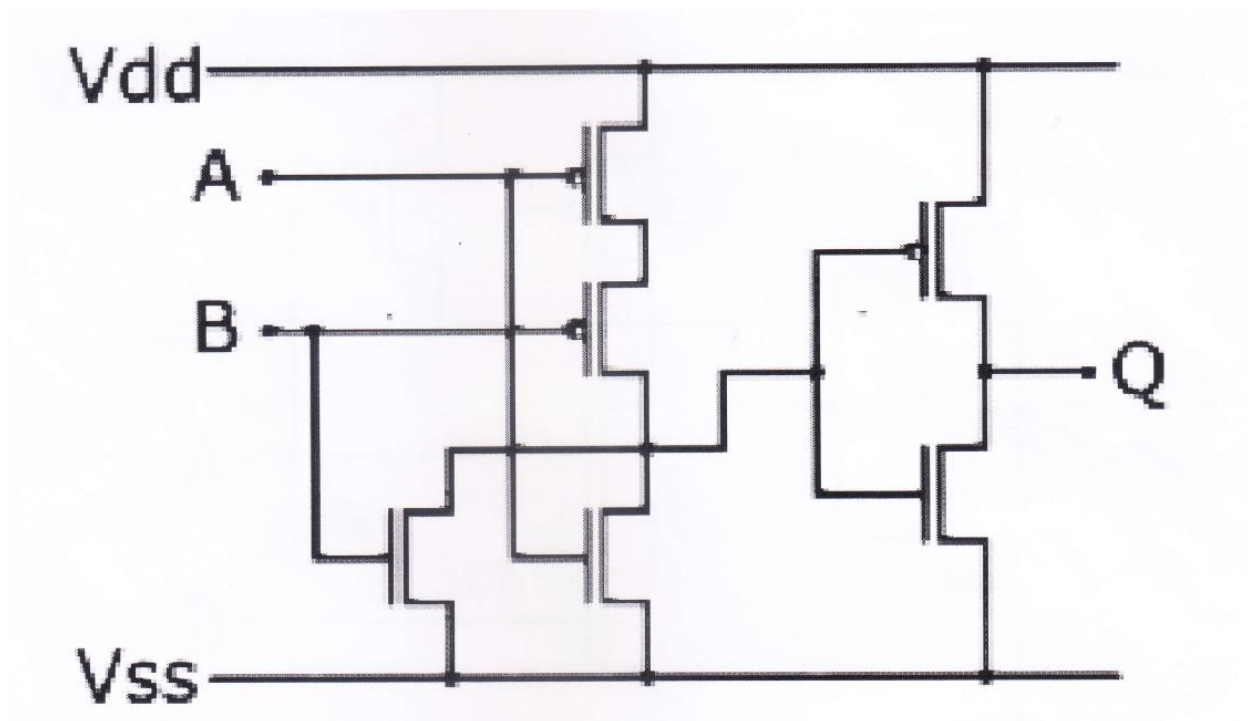


Figure 1: CMOS OR Gate.

Procedure: As mentioned in Lab Manual of Experiment 1.

Question:

1. Design a 3 input CMOS OR Gate.