

Primeasia University
Department of EEE
EEE-4135/CSE-403 VLSI

Experiment no. 4

Experiment Name: Designing 2 input CMOS AND gate using MICROWIND.

Objective: To design CMOS AND gate and see the performance and output wave shape.

Theory:

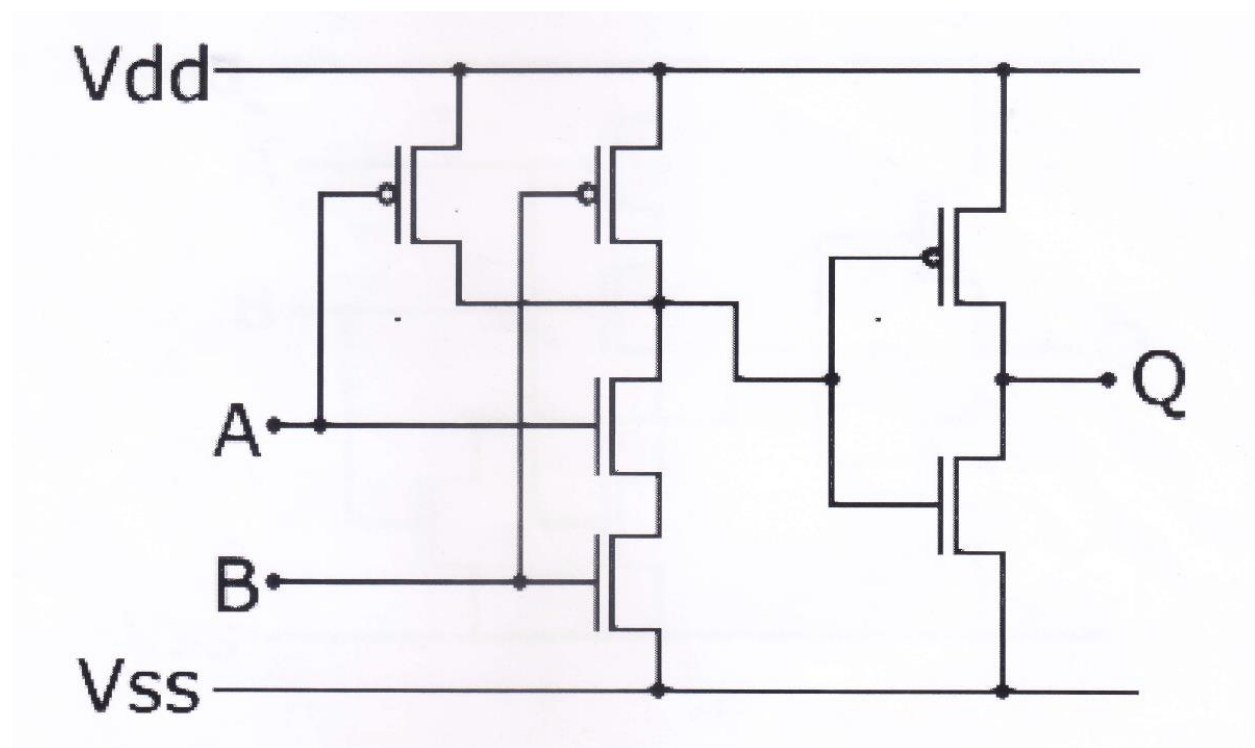


Figure 1: CMOS AND Gate.

Procedure: As mentioned in Lab Manual of Experiment 1.

Question:

1. Design a 3 input CMOS AND Gate.