

Making secure and reliable cars using Separation and Virtualization Technologies

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Agenda



- Motivations
- Separation
- □ Virtualization
- Advanced Applications
- Device Security Architectures

Motivations



□ Automotive trends

- Bringing different electronic domains into single platform
- Increasing Vehicles Intelligence
- Vehicle internal networks are more and more connected to external devices



Potentially increase the attack surface

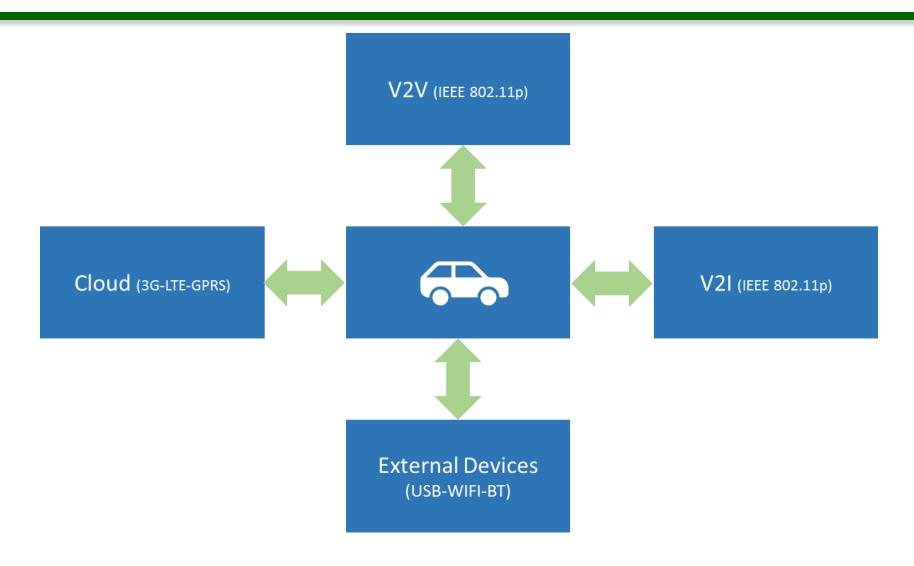
How Vulnerabilities Happen



- □ Architecture
 - Security not built-in from the ground up
 - Companies combine security-critical/non-security-critical applications and code
- Poor Coding
 - Programming errors (i.e. buffer overflows)
- □ Trade-offs
 - Cost (i.e. MMU vs. non-MMU processors)
 - Time-to-market
 - Features
 - Convenience

Automotive Connectivity





Separation is Fundamental



- Security and Safety Architecture
 - Identify critical components in the system
 - Separate those components from untrusted code
 - Enforce strict access control

Enables complex systems with high security and safety



Secure Separation



- Separation architecture for instrument clusters
 - Consolidation
 - Safely run HMI and safety-critical tasks on the same processor
 - Achieve real-time goals
 - Guarantee resources



Secure, Safe and Scalable Instrument Clusters

Separation Benefits



- No recompile needed for components not changed
 - Only re-link the changed parts to existing components
 - No API changes that effects application code
- Separation architecture yields independence between components
 - No need to rerun unit tests on unchanged components
 - Simply test changed/added functionality on relevant components prior to integration tests
 - Only need to rerun your integration tests, not the full test suite

INTEGRITY: a Secure and Safe RTOS



- Securely separate applications to allow mixed safety and security levels on the same processor
 - Including guest OS if needed -> Virtualization
- □ Isolate and protect sensitive data
- □ Secure the vehicle bus from interference from 'outside'
- Be Ultra Reliable and deterministic
- Be designed from day 1 to be Safe, Secure and

Deterministic

INTEGRITY Real-Time Operating System



- Unique real-time operating system architecture
 - Separation kernel architecture
 - Partition scheduling / resource guarantees
 - Advanced multicore/multiprocessor support
 - Single Core, AMP, SMP
 - Safely consolidate software on same processor

The most highly certified RTOS in the

- embedded market segments
 - EAL 6+, highest software security certification in world
 - IEC 61508 SIL3 for industrial
 - CENELEC EN 50128 SW/SIL4 for railway
 - ISO 26262 ASIL for automotive
 - DO-178B Level A for avionics
 - FDA Class II, III approvals for medical
- Extensive middleware and ecosystem
 - Networking, routing, graphics and much more
- Open platform support
 - POSIX, ARINC 653, OpenGLES, upgradeable and flexible





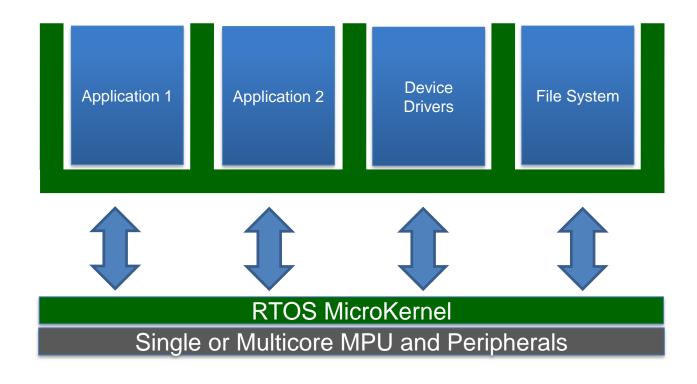






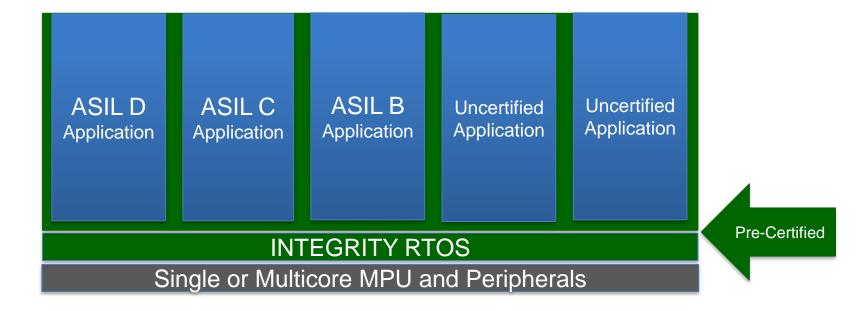
Separation Architecture





ECU Consolidation - Separation Architecture





Each Application Certified to its individual ASIL level
Reduced Development Costs
Reduced Certification Costs

Independent Expert Validation





Department of Transportation

Federal Assistant Assistantian Type Certificate

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Certifying Authority	Level 2 Achived	Applicability	Industry
FAA/EASAIINTEGRITY-178B)	DO-178B1Level	Reliability, ® afety	Avionics
NSA	EAL6+1High TRobustness 17 Type 11	Security	Defense
NIST	FIPS@140-2,@evel@1	Security	All
DIA	TSABI@L-4	Security	Enterprise/IT
FDA	ClassIII,III	Reliability, : \$afety	Medical
TUV I Nord, I exida	IEC361508:2010@35IL34	Safety	Industrial Automation
TUV I Nord, I exida	EN550128:220112351L24	Safety	Rail, Transportation
TUV I Nord, I exida	ISO226262:201033ASIL3D	Safety	Automotive
Transdyne Corp.	SEI/CMMI©Certified	Quality	All
IEEE@and@the@Open@Group	1003.11EEEPOSIXICertified	Open, Interoperable	All















Virtualization

Commodity Operating Systems



- Embedded Linux OS (e.g. Yocto distribution)
 - provides an attractive set of ready-made software
 - consisting of millions of lines of code
 - will continue to contain security vulnerabilities and software bugs
- □ A powerful method for improving the security of a system having Linux as an Operating System
 - is to use an Hypervisor
 - to guarantee separation between the system software components

Architectural Comparison





Type-2 Hypervisor

GPOS (Linux/Windows)

SoC

- VMware Fusion/MVP
- Parallels
- Linux KVM
- ✓ GPOS limits security, performance, determinism



Console/dom0
Type-1 Hypervisor

SoC

- Xen
- VLX-MH
- ✓ GPOS limits security, performance, determinism
- ✓ Large footprint



Traditional
Type-1 Hypervisor

SoC

- VMware ESX
- OKL4
- ✓ Reinventing the wheel
- ✓ Lacks Real-time

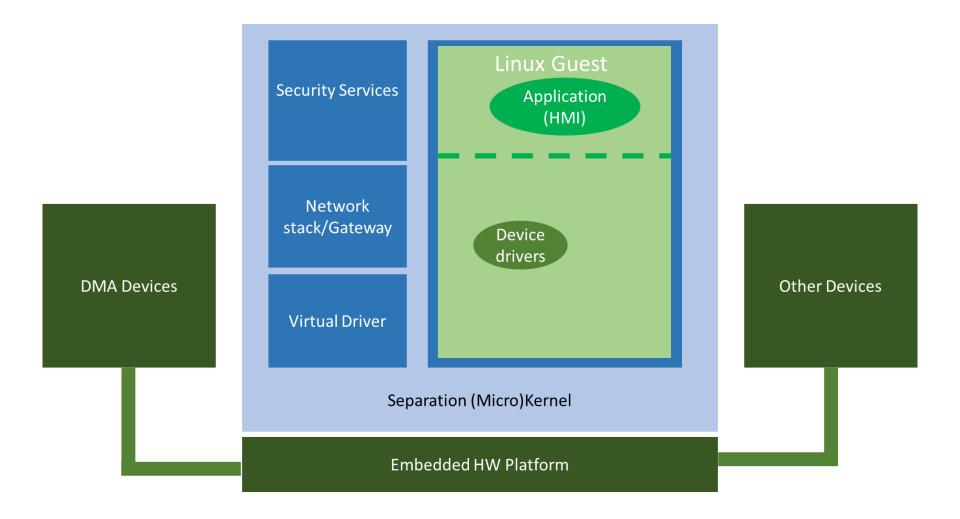
INTEGRITY Multivisor Microkernel Hosted Hypervisors



- What do hypervisors need to do?
 - Partition and protect memory resources
 - Secure access control for I/O and other system objects
 - Interprocess communication (IPC)
 - Schedule workloads securely and efficiently across cores
 - Power management
 - Device drivers
 - Handle disparate workloads real-time and general purpose
 - Health monitoring / high availability
- This a subset of what a real-time microkernel already does extremely well
- Add System Virtualization as a microkernel service

Microkernel Hosted Hypervisors - Architecture





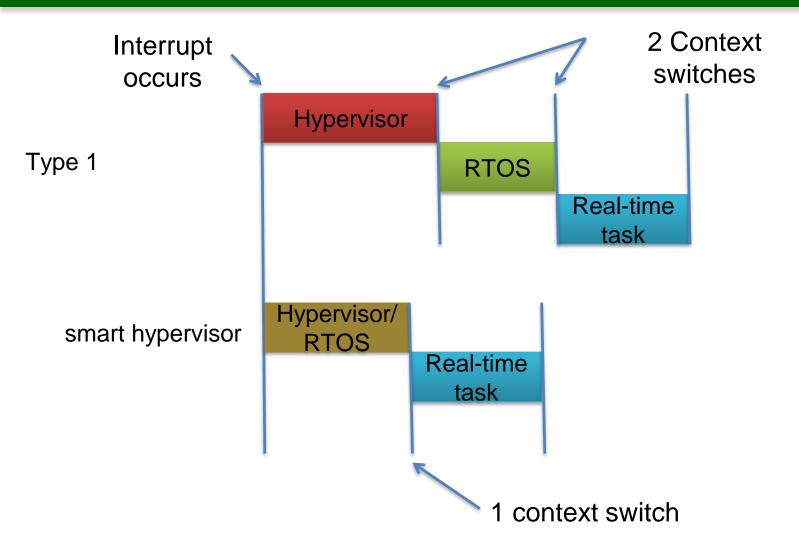
I/O Device Security



- Linux Guest connected device security
 - Especially for devices exploiting DMA
- □ I/O MMU (Second Stage MMU)
 - provides a programmatic interface to define which ranges of addresses the device can access
 - This allows device drivers to run purely in a Separation Kernel partition, or a Guest OS
 - taken as a compromise for the sake of either maintainability or timeto-market
- Virtual Drivers
 - devices managed by the Separation Kernel
 - flaw in a Guest OS device driver cannot
 - wrongfully program the DMA hardware
 - cause potentially fatal memory corruption

Hypervisor Type 1 vs smart hypervisor



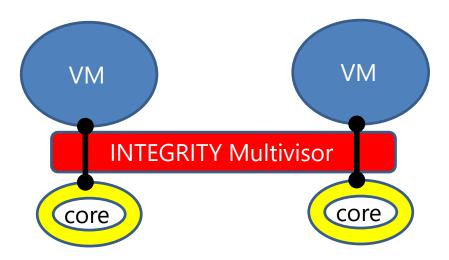


Multicore Approaches



Static Partitioning

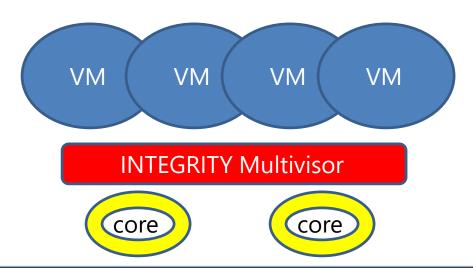
- One VM per core
- VMs are fixed to cores (no migration)
- Simplest no scheduling
- Least flexible use of cores
- Basically this is Asymmetric Multi Processing with memory protection



Multicore Approaches



- Dynamic Partitioning
 - Fully scheduled VMs (one or more VMs per core)
 - Power efficiency
 - Example: dual core, 2 VMs, each 50% loaded
 - Optimal: run both VMs on one core and turn the other core off
 - Well-suited to microkernel architecture
 - VMs and/or native processes are schedulable and migratable

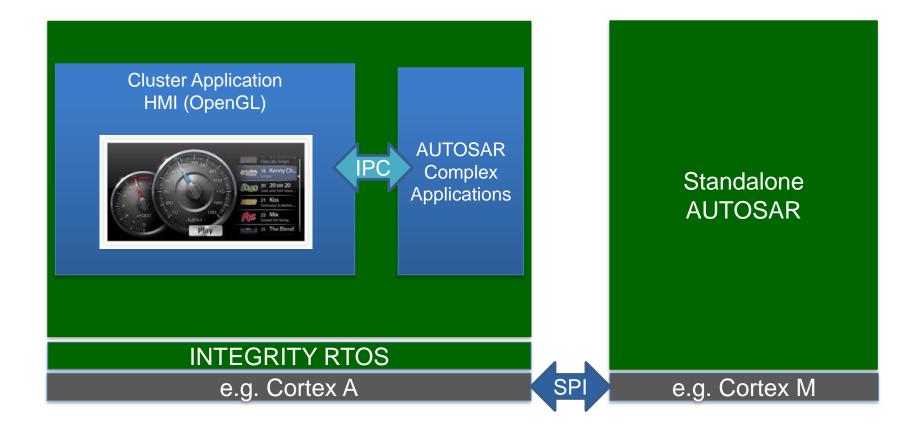




Advanced Applications

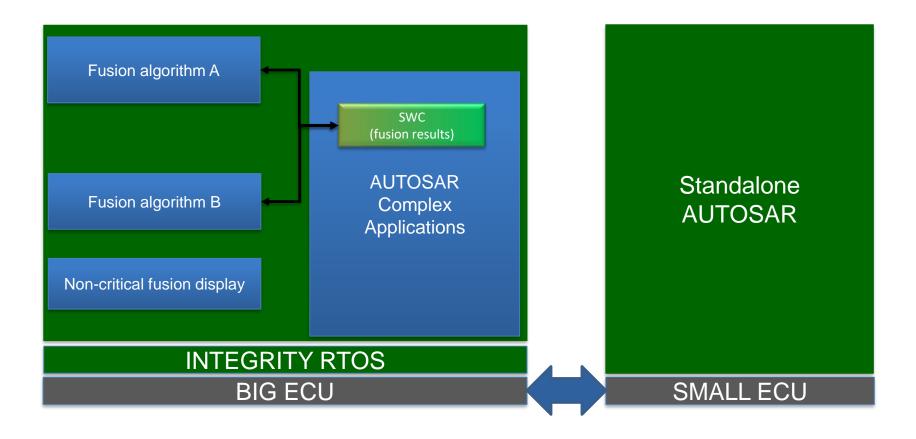
Digital Cluster





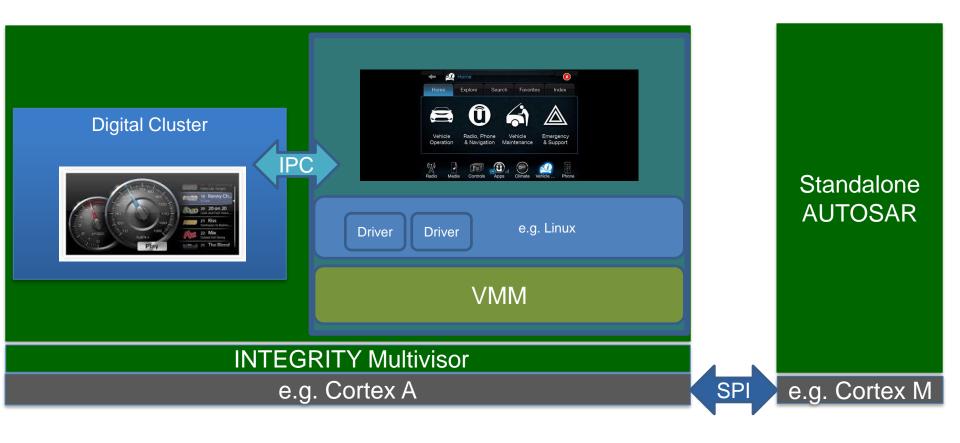
ADAS - Data Fusion





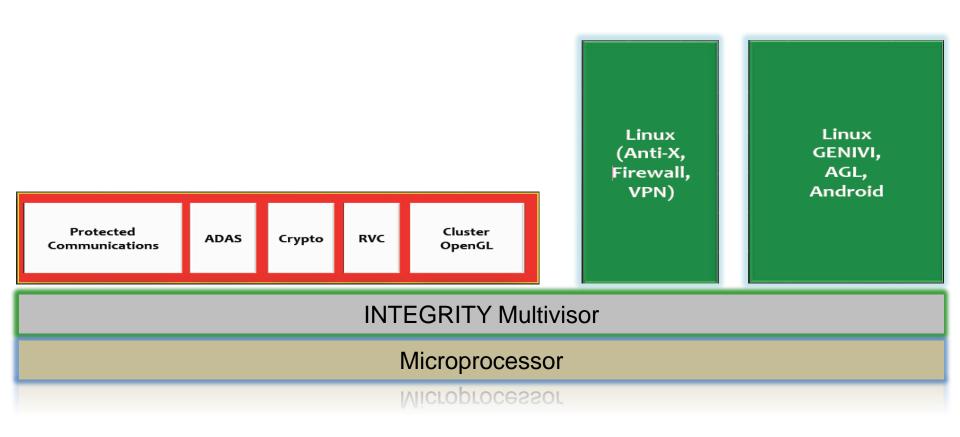
Digital Cluster + IVI





Platform for ADAS Advanced Driver Assistance Systems







Device Security Architecture

ISO 26262: Safety vs Security



Using ISO 26262 ≠ Security in your design

- □ If you design to ISO 26262, other considerations *must* be taken to achieve levels of system security
 - Secure Boot
 - Device Authentication
 - Software Authentication
 - FIPS 140-2 Cryptography
 - Use of products that adhere to and are certified to high Evaluation Assurance Levels (EAL) by BSI and/or Common Criteria
 - And more....

Purpose of Embedded Security Design

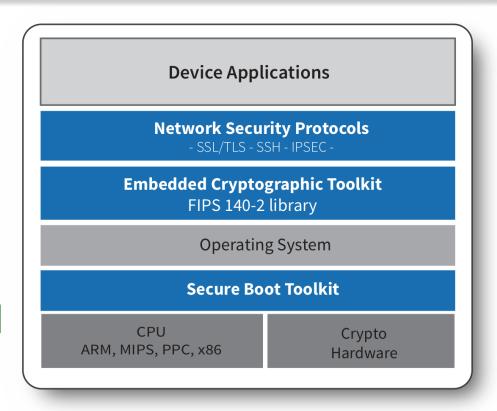


- 1. Protect data from unauthorized viewing
 - Data In Transit
 - Data In Storage
- 2. Protect operational reliability
 - Network Attacks
 - Physical Attacks
 - External Threats
 - Internal Threats

Device Security Architecture



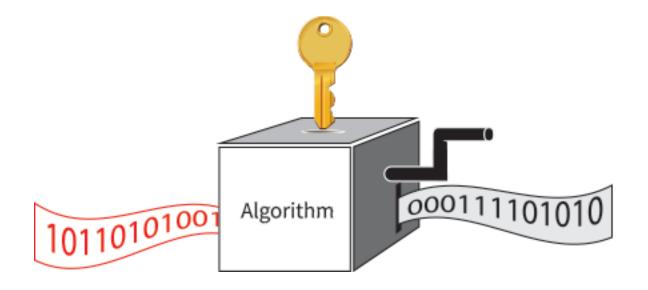
- □ Secure Data
- Verify software has not been tampered
- □ AuthenticateRemote Systems andUsers



Kerckhoff's Principle



A cryptographic system should be secure even if everything about the system, except the private key, is public knowledge.



Typical Manufacturing Flow



- 1-

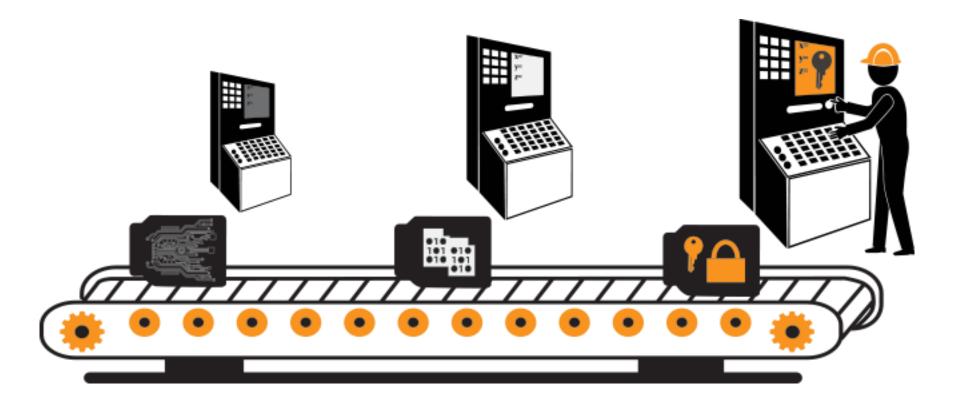
Integrated Circuit Test

- 2-

Software Load & Functional Test

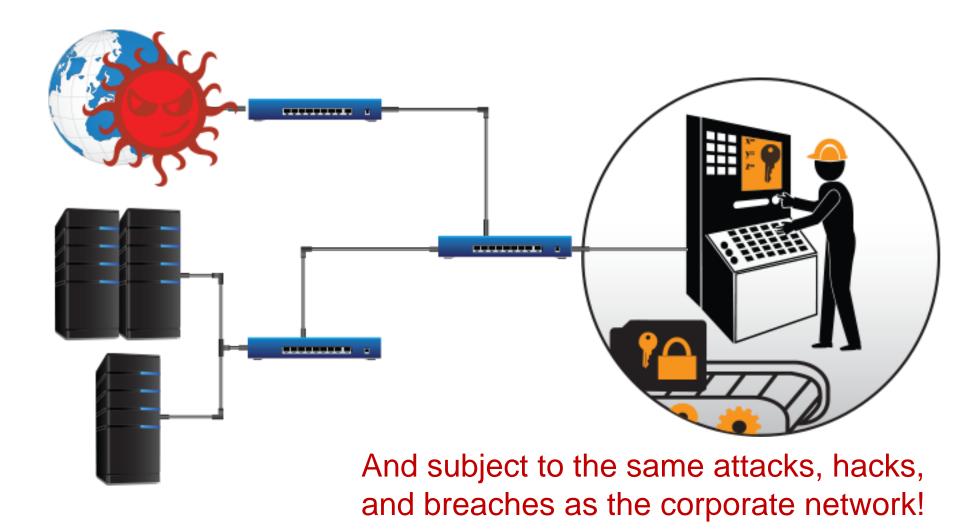
- 3-

Key Injection & Final Test



Networked for Monitoring and Updates





But This Isn't Just Any Data!



- Network Attacks
- Disgruntled Employee
- □ Accident

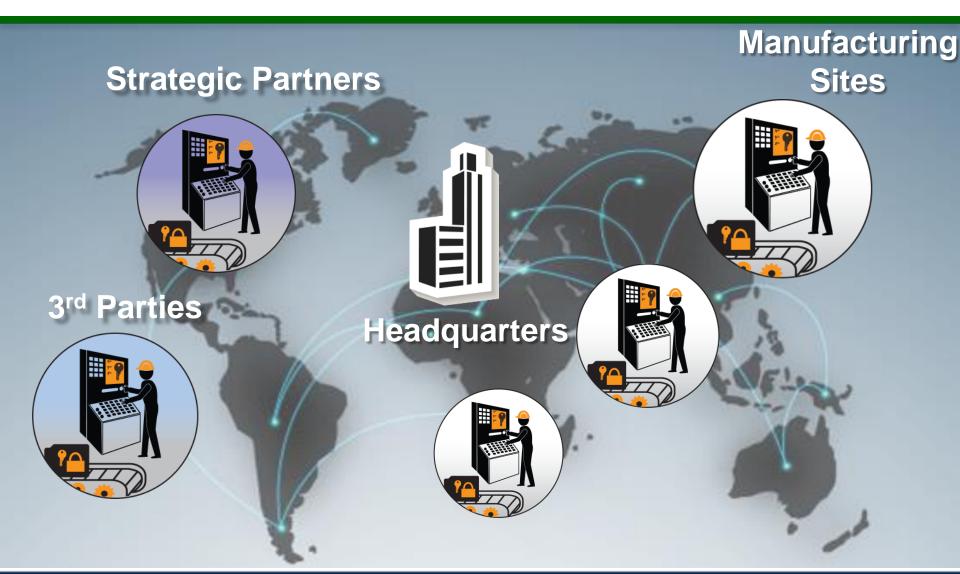
Compromised Keys

= All Devices at Risk!



Supply Chain Complexity Makes it Harder





The Purpose of a Security Infrastructure



- Protect digital trust assets from unauthorized access across ALL endpoints
- Digitally sign software & data
- Generate keys & certificates
- Distribute assets to devices



Security Infrastructure



Zero exposure of all digital trust assets within tamper protected boundaries

Digital Signing Service

- Digitally sign software, files, data, and commands

Certificate Authority Service

 Generate unique device identity certificates for authentication and encryption

Supply Chain Distribution

- Securely generate and meter digital trust assets to systems across distributed locations



Thanks for your attention!

Q & A



