

20021f4:	32 00 00 40	LDI	0x02003e14,R6 // 2003e14 <testlist>
20021f8:	32 40 3e 14		
20021fc:	0b 41 80 00	MOV	R6,R1
2002200:	13 41 80 80	MOV	\$128+R6,R2
2002204:	06 7f ff ff	LDI	\$-1,R0
2002208:	04 c4 40 00	SW	R0,(R1)
200220c:	08 80 00 04	ADD	\$4,R1
2002210:	14 04 40 00	CMP	R1,R2
2002214:	78 ab ff ec	BNZ	@0x02002204 // 2002204 <entry+0x30>
2002218:	0a 00 02 00	BREV	\$512,R1
200221c:	02 03 ff fe	BREV	\$-2,R0
2002220:	04 c4 40 00	SW	R0,(R1)
2002224:	0a 00 00 40	LDI	0x02003d78,R1 // 2003d78 <entry+0x1ba4>
2002228:	0a 40 3d 78		
200222c:	03 43 c0 01	JSR	0x02001120 // 2001120 <txstr>

r1r2


```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x02003e6c WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x02003e6c sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : sPC : 0x02002210

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002268 0x0a403b39 LDILO $15161, R1 >02ffffba4 0x00000000
0x02002214 0x78abffec BNZ @0x02002204 02ffffba8 0x00000000
>0x02002210 0x14044000 CMP R1, R2 02ffffbac 0x00000000
0x02002210 0x14044000 CMP R1, R2 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r11r2a

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x02003e70 WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x02003e70 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : sPC : 0x02002210

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002268 0x0a403b39 LDILO $15161, R1 >02ffffba4 0x00000000
0x02002214 0x78abffec BNZ @0x02002204 02ffffba8 0x00000000
>0x02002210 0x14044000 CMP R1, R2 02ffffbac 0x00000000
0x0200220c 0x08800004 ADD $4, R1 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r1r2f

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0x02002234< WDT : 0x02003d78 WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0x02002234 sR1 : 0x02003d78 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : Z sPC : 0x02002230

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02006688 0x00000000 SUB $0, R0 >02ffffba4 0x00000000
0x02006684 0x00000000 SUB $0, R0 02ffffba8 0x00000000
02ffffbac 0x00000000
>0x02002230 0x7883eeec BRA @0x02001120 02ffffbb0 0x00000000
0x0200222c 0x0343c001 MOV 0x02002234, R0 02ffffbb4 0x00000000

```

r1r2b

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x02003e8c WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x02003e8c sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : sPC : 0x02002210

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002268 0x0a403b39 LDILO $15161, R1 >02ffffba4 0x00000000
0x02002214 0x78abffec BNZ @0x02002204 02ffffba8 0x00000000
>0x02002210 0x14044000 CMP R1, R2 02ffffbac 0x00000000
0x0200220c 0x08800004 ADD $4, R1 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r1r2c

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x02003e90 WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x02003e90 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : sPC : 0x02002210

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002268 0x0a403b39 LDILO $15161, R1 >02ffffba4 0x00000000
0x02002214 0x78abffec BNZ @0x02002204 02ffffba8 0x00000000
>0x02002210 0x14044000 CMP R1, R2 02ffffbac 0x00000000
0x0200220c 0x08800004 ADD $4, R1 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r1r2d

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x02003e94 WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x02003e94 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : sPC : 0x02002210

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002268 0x0a403b39 LDILO $15161, R1 >02ffffba4 0x00000000
0x02002214 0x78abffec BNZ @0x02002204 02ffffba8 0x00000000
>0x02002210 0x14044000 CMP R1, R2 02ffffbac 0x00000000
0x0200220c 0x08800004 ADD $4, R1 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r1r2e

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff< WDT : 0x00400000 WBUS: 0x02003e94 PIC2: 0x00000000
  TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
  MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0xffffffff sR1 : 0x00400000 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffba4 sCC : Z sPC : 0x0200221c

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x02002224 0x0a000040 BREV $64, R1 >02ffffba4 0x00000000
0x02002220 0x04c44000 SW R0, (R1) 02ffffba8 0x00000000
>0x0200221c 0x0203ffffe BREV $-2, R0 02ffffbac 0x00000000
0x02002218 0x0a000200 BREV $512, R1 02ffffbb0 0x00000000
02ffffbb4 0x00000000

```

r1r2f

r1r2g

```

File Edit Tabs Help
Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0x02002234< WDT : 0x02003d78 WBUS: 0x02003e94 PIC2: 0x00000000
  TMRA: 0x00000000 TMRB: 0x00000000 TMRC: 0x02003e14 JIF : 0x00000000
  MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers
sR0 : 0x02002234 sR1 : 0x02003d78 sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x00000000 sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffb9c sCC : sPC : 0x02001124

User Registers
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x0200112c 0x2b404000 MOV R1, R5 >02ffffb9c 0x00000000
0x02001128 0x2cc74004 SW R5, $4(SP) 02ffffba0 0x00000000
>0x02001124 0x04c74000 SW R0, (SP) 02ffffba4 0x00000000
0x02001120 0x68000008 SUB $8, SP 02ffffba8 0x00000000
02ffffbac 0x00000000

```

r1r2h

File Edit Tabs Help

Peripherals CPU State: 0x00000613 Supervisor mode
>PIC > 0x02001148< WDT : 0x0000000d WBUS: 0x02003e94 PIC2: 0x00000000
TMRA: 0x00000000 TMRB: 0x0000000d TMRC: 0x02003e14 JIF : 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000

Supervisor Registers

sR0 : 0x02001148 sR1 : 0x0000000d sR2 : 0x02003e94 sR3 : 0x00000000
sR4 : 0x00000000 sR5 : 0x0000000d sR6 : 0x02003e14 sR7 : 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000
sR12: 0x00000000 sSP : 0x02ffffb94 sCC : sPC : 0x020010c8

User Registers

uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000
uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000

0x020010d0 0x78880014 BZ @0x020010e8 >02ffffb94 0x02001148
0x020010cc 0x08400020 AND \$32, R1 02ffffb98 0x02003d79
>0x020010c8 0x0b438000 MOV CC, R1 02ffffb9c 0x02002234
0x020010c4 0x284000ff AND \$255, R5 02ffffba0 0x00000000
02ffffba4 0x00000000