```
20021f4:
             32 00 00 40
                          LDI
                                  0x02003e14,R6 // 2003e14 <testlist>
20021f8:
             32 40 3e 14
                          MOV
                                    R6,R1
20021fc:
             0b 41 80 00
2002200:
             13 41 80 80
                          MOV
                                    $128+R6,R2
2002204:
             06 7f ff ff
                          LDI
                                  $-1,R0
2002208:
             04 c4 40 00
                          SW
                                  R0,(R1)
200220c:
             08 80 00 04
                          ADD
                                   $4,R1
2002210:
             14 04 40 00
                          CMP
                                   R1,R2
2002214:
             78 ab ff ec
                          BNZ
                                   @0x02002204 // 2002204 <entry+0x30>
2002218:
             0a 00 02 00
                          BREV
                                    $512,R1
             02 03 ff fe
                          BREV
                                    $-2,R0
200221c:
2002220:
             04 c4 40 00
                          SW
                                  R0,(R1)
             0a 00 00 40
2002224:
                          LDI
                                  0x02003d78,R1 // 2003d78 <entry+0x1ba4>
2002228:
             0a 40 3d 78
200222c:
             03 43 c0 01
                          JSR
                                  0x02001120
                                               // 2001120 <txstr>
```

r1r2

```
File Edit Tabs Help
Peripherals
                              CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff<
                    WDT : 0x02003e6c
                                         WBUS: 0x02003e94
                                                             PIC2: 0x00000000
TMRA: 0x00000000
                                         TMRC: 0x02003e14
                    TMRB: 0x00000000
                                                             JIF: 0x00000000
MTSK: 0x00000000
                                         MPST: 0x00000000
                    MOST: 0x00000000
                                                             MICT: 0x00000000
Supervisor Registers
sR0 : 0xffffffff
                                         sR2 : 0x02003e94
                                                             sR3 : 0x00000000
                    sR1 : 0x02003e6c
sR4 : 0x00000000
                    sR5 : 0x00000000
                                         sR6 : 0x02003e14
                                                             sR7 : 0x00000000
                                         sR10: 0x00000000
sR8 : 0x00000000
                    sR9 : 0x00000000
                                                             sR11: 0x00000000
                                                             sPC: 0x02002210
sR12: 0x00000000
                    sSP: 0x02fffba4
                                         sCC:
User Registers
                                         uR2 : 0x00000000
uR0 : 0x00000000
                    uR1 : 0x00000000
                                                             uR3 : 0x00000000
uR4 : 0x00000000
                                                             uR7 : 0x00000000
                    uR5 : 0x00000000
                                         uR6 : 0x00000000
uR8 : 0x00000000
                    uR9 : 0x00000000
                                         uR10: 0x00000000
                                                             uR11: 0x00000000
                    uSP : 0x00000000
uR12: 0x00000000
                                         ucc:
                                                             uPC: 0x00000000
                                                     >02fffba4 0x00000000
                                   $15161,R1
                                                      0x02002268 0x0a403b39
                       LDILO
                                                      02fffbac 0x00000000
                       BNZ
0x02002214 0x78abffec
                                   @0x02002204
0x02002210 0x14044000
                       CMP
                                                      02fffbb0 0x00000000
                                   R1, R2
                                                      02fffbb4 0x00000000
0x02002210 0x14044000
                       CMP
                                   R1, R2
```

r11r2a

```
File Edit Tabs Help
                              CPU State: 0x00000613 Supervisor mode
Peripherals
                    WDT : 0x02003e70
>PIC > 0xffffffff<
                                         WBUS: 0x02003e94
                                                             PIC2: 0x00000000
TMRA: 0x00000000
                    TMRB: 0x00000000
                                         TMRC: 0x02003e14
                                                             JIF : 0x00000000
MTSK: 0x00000000
                    MOST: 0x00000000
                                         MPST: 0x00000000
                                                             MICT: 0x00000000
Supervisor Registers
sR0 : 0xfffffff
                                         sR2 : 0x02003e94
                                                             sR3 : 0x00000000
                    sR1 : 0x02003e70
sR4 : 0x00000000
                    sR5 : 0x00000000
                                         sR6 : 0x02003e14
                                                             sR7 : 0x00000000
sR8 : 0x00000000
                    sR9 : 0x00000000
                                         sR10: 0x00000000
                                                             sR11: 0x00000000
sR12: 0x00000000
                    sSP: 0x02fffba4
                                         sCC :
                                                             sPC : 0x02002210
User Registers
uR0 : 0x00000000
                                         uR2 : 0x00000000
                                                             uR3 : 0x00000000
                    uR1 : 0x00000000
uR4 : 0x00000000
                                         uR6 : 0x00000000
                    uR5 : 0x00000000
                                                             uR7 : 0x00000000
                                                             uR11: 0x00000000
uR8 : 0x00000000
                    uR9 : 0x00000000
                                         uR10: 0x00000000
                                         ucc :
                                                             uPC : 0x00000000
uR12: 0x00000000
                    uSP : 0x00000000
                                                     >02fffba4 0x00000000
                                                      02fffba8 0x00000000
0x02002268 0x0a403b39
                       LDILO
                                   $15161,R1
BNZ
                                   @0x02002204
                                                      02fffbac 0x00000000
                                   R1, R2
0x02002210 0x14044000
                       CMP
                                                      02fffbb0 0x00000000
0x0200220c 0x08800004
                       ADD
                                   $4,R1
                                                      02fffbb4 0x00000000
```

File Edit Tabs Help				
Peripherals	CPU	State: 0x0000	00613 Supervisor ı	mode _
>PIC > 0x02002234<	WDT : 0x02003		0x02003e94 PI	
TMRA: 0x00000000	TMRB: 0x00000	0000 TMRC:	0x02003e14 JI	F : 0x00000000
MTSK: 0x00000000	MOST: 0x00000	0000 MPST:	0x00000000 MI	CT: 0x00000000
Supervisor Registers				
sR0 : 0x02002234	sR1 : 0x02003	3d78 sR2 :	0x02003e94 sR	3 : 0x00000000
sR4 : 0x00000000	sR5 : 0x00000	0000 sR6 :	0x02003e14 sR	7 : 0x00000000
sR8 : 0x00000000	sR9 : 0x00000	0000 sR10:	0x00000000 sR	11: 0x00000000
sR12: 0x00000000	sSP : 0x02ff	fba4 sCC:	Z sP	C : 0x02002230
User Registers				
uR0 : 0x00000000	uR1 : 0x00000	0000 uR2 :	0x00000000 uR	3 : 0×00000000
uR4 : 0x00000000	uR5 : 0x00000	0000 uR6 :	0x00000000 uR	7 : 0×00000000
uR8 : 0x00000000	uR9 : 0x00000			11: 0x00000000
uR12: 0x00000000	uSP : 0x00000	0000 uCC :	uP	C : 0x00000000
			225551	
			>02fffba4	
0x02006688 0x000000		\$0,R0		0x00000000
0x02006684 0x000000		\$0,R0		0x00000000
>0x02002230 0x7883ee		@0x02001120		0x00000000
0x0200222c 0x0343c0	91 MOV	0x02002234,F	02TTTbb4	0x00000000
				_

r1r2b

File Edit Tabs Help				
Peripherals	CPU	State: 0x000	00613 Superviso	r mode
>PIC > 0xffffffff<	WDT : 0x02003			PIC2: 0x00000000
TMRA: 0x00000000	TMRB: 0x00000			JIF : 0x00000000
MTSK: 0x00000000	MOST: 0x00000	0000 MPST:	0x00000000	MICT: 0x00000000
Supervisor Registers				
sR0 : 0xffffffff	sR1 : 0x02003	Be8c sR2 :	0x02003e94	sR3 : 0x00000000
sR4 : 0x00000000	sR5 : 0x00000		0x02003e14	sR7 : 0x00000000
sR8 : 0x00000000	sR9 : 0x00000	0000 sR10:	0x00000000	sR11: 0x00000000
sR12: 0x00000000	sSP : 0x02fff	ba4 scc:	5	SPC : 0x02002210
User Registers				
uR0 : 0x00000000	uR1 : 0x00000			uR3 : 0x00000000
uR4 : 0x00000000	uR5 : 0x00000			JR7 : 0x00000000
uR8 : 0x00000000	uR9 : 0x00000			JR11: 0x00000000
uR12: 0x00000000	uSP : 0x00000	0000 uCC :	l	JPC : 0x00000000
			>02fffba	4 0x00000000
0x02002268 0x0a403b3	39 LDILO	\$15161,R1	02fffba8	3 0x0000000
0x02002214	ec BNZ	@0x02002204	02fffba	0x00000000
>0x02002210 0x140440 6	00 CMP	R1, R2	02fffbb(0x0000000
0x0200220c 0x088000	94 ADD	\$4,R1	02fffbb4	4 0x00000000

r1r2c

```
File Edit Tabs Help
Peripherals
                              CPU State: 0x00000613 Supervisor mode
>PIC > 0xffffffff<
                    WDT : 0x02003e90
                                         WBUS: 0x02003e94
                                                             PIC2: 0x00000000
TMRA: 0x00000000
                    TMRB: 0x00000000
                                         TMRC: 0x02003e14
                                                             JIF: 0x00000000
MTSK: 0x00000000
                                         MPST: 0x00000000
                    MOST: 0x00000000
                                                             MICT: 0x00000000
Supervisor Registers
sR0 : 0xffffffff
                                         sR2 : 0x02003e94
                                                             sR3 : 0x00000000
                    sR1 : 0x02003e90
sR4 : 0x00000000
                    sR5 : 0x00000000
                                         sR6 : 0x02003e14
                                                             sR7 : 0x00000000
                                         sR10: 0x00000000
sR8 : 0x00000000
                    sR9 : 0x00000000
                                                             sR11: 0x00000000
sR12: 0x00000000
                    sSP: 0x02fffba4
                                         sCC:
                                                             sPC: 0x02002210
User Registers
                                         uR2 : 0x00000000
uR0 : 0x00000000
                    uR1 : 0x00000000
                                                             uR3 : 0x00000000
uR4 : 0x00000000
                                                             uR7 : 0x00000000
                    uR5 : 0x00000000
                                         uR6 : 0x00000000
uR8 : 0x00000000
                    uR9 : 0x00000000
                                         uR10: 0x00000000
                                                             uR11: 0x00000000
uR12: 0x00000000
                    uSP : 0x00000000
                                         ucc:
                                                             uPC: 0x00000000
                                                     >02fffba4 0x00000000
                                  $15161,R1
                                                     0x02002268 0x0a403b39
                       LDILO
                                                      02fffbac 0x00000000
                       BNZ
0x02002214 0x78abffec
                                   @0x02002204
0x02002210 0x14044000
                       CMP
                                                      02fffbb0 0x00000000
                                   R1, R2
                                                      02fffbb4 0x00000000
0x0200220c 0x08800004
                       ADD
                                   $4,R1
```

r1r2d

```
File Edit Tabs Help
                              CPU State: 0x00000613 Supervisor mode
Peripherals
                    WDT : 0x02003e94
>PIC > 0xffffffff<
                                         WBUS: 0x02003e94
                                                             PIC2: 0x00000000
TMRA: 0x00000000
                    TMRB: 0x00000000
                                         TMRC: 0x02003e14
                                                             JIF : 0x00000000
MTSK: 0x00000000
                    MOST: 0x00000000
                                         MPST: 0x00000000
                                                             MICT: 0x00000000
Supervisor Registers
sR0 : 0xfffffff
                                         sR2 : 0x02003e94
                                                             sR3 : 0x00000000
                    sR1 : 0x02003e94
sR4 : 0x00000000
                    sR5 : 0x00000000
                                         sR6 : 0x02003e14
                                                             sR7 : 0x00000000
sR8 : 0x00000000
                    sR9 : 0x00000000
                                         sR10: 0x00000000
                                                             sR11: 0x00000000
sR12: 0x00000000
                    sSP: 0x02fffba4
                                         sCC :
                                                             sPC : 0x02002210
User Registers
uR0 : 0x00000000
                                         uR2 : 0x00000000
                                                             uR3 : 0x00000000
                    uR1 : 0x00000000
uR4 : 0x00000000
                                         uR6 : 0x00000000
                    uR5 : 0x00000000
                                                             uR7 : 0x00000000
                                                             uR11: 0x00000000
uR8 : 0x00000000
                    uR9 : 0x00000000
                                         uR10: 0x00000000
                                         ucc :
                                                             uPC : 0x00000000
uR12: 0x00000000
                    uSP : 0x00000000
                                                     >02fffba4 0x00000000
                                                      02fffba8 0x00000000
0x02002268 0x0a403b39
                       LDILO
                                   $15161,R1
BNZ
                                   @0x02002204
                                                      02fffbac 0x00000000
                                   R1, R2
0x02002210 0x14044000
                       CMP
                                                      02fffbb0 0x00000000
                       ADD
0x0200220c 0x08800004
                                   $4,R1
                                                      02fffbb4 0x00000000
```

File Edit Tabs Help				
Peripherals	CPU	State: 0x000	00613 Superviso	r mode
>PIC > 0xffffffff<	WDT : 0x0040	0000 WBUS:	0x02003e94	PIC2: 0x00000000
TMRA: 0x00000000	TMRB: 0x0000	0000 TMRC:	0x02003e14	JIF : 0x00000000
MTSK: 0x00000000	MOST: 0x0000	0000 MPST:	0x00000000	MICT: 0x00000000
Supervisor Registers				
sR0 : 0xffffffff	sR1 : 0x0040	0000 sR2:	0x02003e94	sR3 : 0x00000000
sR4 : 0x00000000	sR5 : 0x0000	0000 sR6:	0x02003e14	sR7 : 0x00000000
sR8 : 0x00000000	sR9 : 0x0000	0000 sR10:	0x00000000	sR11: 0x00000000
	sSP : 0x02ff	fba4 sCC:	Z	sPC : 0x0200221c
User Registers				
	uR1 : 0x0000			uR3 : 0x00000000
	uR5 : 0x0000			uR7 : 0x00000000
	uR9 : 0x0000			uR11: 0x00000000
uR12: 0x00000000	uSP : 0x0000	0000 uCC:		uPC : 0x00000000
			225551	
				4 0x00000000
0x02002224 0x0a00004		\$64,R1		8 0x00000000
0x02002220 0x04c4400		R0,(R1)		C 0x00000000
>0x0200221c 0x0203fff		\$-2,R0		0 0x00000000
0x02002218 0x0a00020	0 BREV	\$512,R1	02TTTbb	4 0x00000000

r1r2f

r1r2g

```
File Edit Tabs Help
Peripherals
                              CPU State: 0x00000613 Supervisor mode
>PIC > 0x02002234<
                     WDT: 0x02003d78
                                         WBUS: 0x02003e94
                                                              PIC2: 0x00000000
TMRA: 0x00000000
                     TMRB: 0x00000000
                                          TMRC: 0x02003e14
                                                              JIF: 0x00000000
MTSK: 0x00000000
                     MOST: 0x00000000
                                         MPST: 0x00000000
                                                              MICT: 0x00000000
Supervisor Registers
sR0 : 0x02002234
                     sR1: 0x02003d78
                                          sR2: 0x02003e94
                                                              sR3 : 0x00000000
sR4 : 0x00000000
                     sR5 : 0x00000000
                                          sR6: 0x02003e14
                                                              sR7 : 0x00000000
                                          sR10: 0x00000000
                                                              sR11: 0x00000000
sR8 : 0x00000000
                     sR9 : 0x00000000
                                                              sPC : 0x02001124
sR12: 0x00000000
                     sSP: 0x02fffb9c
                                          sCC:
User Registers
uR0 : 0x00000000
                     uR1 : 0x00000000
                                          uR2 : 0x00000000
                                                              uR3: 0x00000000
                                                              uR7 : 0x00000000
uR4 : 0x00000000
                     uR5 : 0x00000000
                                          uR6 : 0x00000000
                                          uR10: 0x00000000
                                                              uR11: 0x00000000
uR8 : 0x00000000
                     uR9 : 0x00000000
uR12: 0x00000000
                     uSP : 0x00000000
                                                              uPC : 0x00000000
                                          ucc:
                                                      >02fffb9c 0x00000000
                                                       02fffba0 0x00000000
0x0200112c 0x2b404000
                        MOV
                                   R1, R5
                                                       02fffba4 0x00000000
0x02001128 0x2cc74004
                        SW
                                   R5, $4(SP)
>0x02001124 0x04c74000
                                                       02fffba8 0x00000000
                        SW
                                   R0, (SP)
0x02001120 0x68000008
                                                       02fffbac 0x00000000
                        SUB
                                   $8, SP
```

Peripherals
TMRA: 0x00000000
MTSK: 0x00000000 MOST: 0x00000000 MPST: 0x00000000 MICT: 0x00000000 Supervisor Registers R0: 0x02001148 SR1: 0x00000000 SR2: 0x02003e94 SR3: 0x00000000 SR4: 0x00000000 SR5: 0x00000000 SR6: 0x02003e14 SR7: 0x00000000 SR8: 0x00000000 SR9: 0x00000000 SR10: 0x00000000 SR11: 0x00000000 SR12: 0x00000000 SSP: 0x02fffb94 SCC: SPC: 0x020010c8 User Registers uR0: 0x00000000 uR1: 0x00000000 uR2: 0x00000000 uR3: 0x00000000 uR4: 0x00000000 uR3: 0x00000000 uR1: 0x00000000 uR1: 0x000000000
Supervisor Registers sR0 : 0x02001148 sR1 : 0x0000000d sR2 : 0x02003e94 sR3 : 0x00000000 sR4 : 0x00000000 sR5 : 0x00000000d sR6 : 0x02003e14 sR7 : 0x00000000 sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000 sR12: 0x00000000 sSP : 0x02fffb94 sCC : sPC : 0x020010c8 User Registers uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000 uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
sR0 : 0x02001148 sR1 : 0x0000000d sR2 : 0x02003e94 sR3 : 0x00000000 sR4 : 0x00000000 sR5 : 0x0000000d sR6 : 0x02003e14 sR7 : 0x00000000 sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x0000000 sR12: 0x00000000 sSP : 0x02fffb94 sCC : sPC : 0x020010c8 User Registers uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000 uR8 : 0x000000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
sR0 : 0x02001148 sR1 : 0x0000000d sR2 : 0x02003e94 sR3 : 0x00000000 sR4 : 0x00000000 sR5 : 0x0000000d sR6 : 0x02003e14 sR7 : 0x00000000 sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x0000000 sR12: 0x00000000 sSP : 0x02fffb94 sCC : sPC : 0x020010c8 User Registers uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000 uR8 : 0x000000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
sR4 : 0x00000000 sR5 : 0x0000000d sR6 : 0x02003e14 sR7 : 0x00000000 sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000 sR12: 0x00000000 sSP : 0x02fffb94 sCC : sPC : 0x020010c8 User Registers uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
sR8 : 0x00000000 sR9 : 0x00000000 sR10: 0x00000000 sR11: 0x00000000 sR12: 0x00000000 sSP : 0x02fffb94 sCC : sPC : 0x020010c8 User Registers uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000 uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
sR12: 0x00000000 sSP: 0x02fffb94 sCC: sPC: 0x020010c8 User Registers uR0: 0x00000000 uR1: 0x00000000 uR2: 0x00000000 uR3: 0x00000000 uR4: 0x00000000 uR5: 0x00000000 uR6: 0x00000000 uR7: 0x00000000 uR8: 0x00000000 uR9: 0x00000000 uR10: 0x00000000 uR11: 0x00000000
User Registers uR0 : 0x00000000
uR0 : 0x00000000 uR1 : 0x00000000 uR2 : 0x00000000 uR3 : 0x00000000 uR4 : 0x00000000 uR5 : 0x00000000 uR6 : 0x00000000 uR7 : 0x00000000 uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR4 : 0x00000000
uR8 : 0x00000000 uR9 : 0x00000000 uR10: 0x00000000 uR11: 0x00000000
uR12: 0x00000000 uSP : 0x00000000 uCC : uPC : 0x00000000
>02fffb94 0x02001148
0x020010d0 0x78880014 BZ
0x020010cc 0x08400020 AND \$32,R1 02fffb9c 0x02002234
>0x020010c8 0x0b438000 MOV CC,R1 02fffba0 0x00000000
0x020010c4 0x284000ff AND \$255,R5 02fffba4 0x00000000