

*****DRAFT*****

./jpeg cputest hello tests ./arm-main_tb
04/16/19

*****DRAFT*****

vidal@laptop:~/testbuilds/catzip/sw/host\$./pc-zipload -v ../board/hello

Halting the CPU

Memory regions:

Block RAM: 00a00000 - 00a02000

SDRAM : 01000000 - 02000000

Loading: ../board/hello

Section 0: 01000000 - 010048ec

Writing to MEM: 01000000-010048ec

Clearing the CPUs registers

Setting PC to 01000000

The CPU should be fully loaded, you may now
start it (from reset/reboot) with:

> wbrege cpu 0x0f

CPU Status is: 0000060f

vidal@laptop:~/testbuilds/catzip/sw/host\$./pc-wbrege cpu 0x0f

02000000 ()-> 0000000f

< [CLOSED]

Hello, World

vidal@laptop:~/testbuilds/catzip/sw/host\$./pc-zipload -v ../board/cputest

Halting the CPU

Memory regions:

Block RAM: 00a00000 - 00a02000

SDRAM : 01000000 - 02000000

Loading: ../board/cputest

Section 0: 01000000 - 01003e94

Writing to MEM: 01000000-01003e94

Clearing the CPUs registers

Setting PC to 01000000

The CPU should be fully loaded, you may now
start it (from reset/reboot) with:

> wbrege cpu 0x0f

CPU Status is: 0000060f

vidal@laptop:~/testbuilds/catzip/sw/host\$./pc-wbrege cpu 0x0f

02000000 ()-> 0000000f

Running CPU self-test

SIM Instructions Is this a simulator?
CIS Instructions Supported
Break test #1 Pass
Break test #2 Pass
Break test #3 Pass
Early Branch test Pass
Trap test/AND Pass

Trap test/CLR	Pass
Overflow test	Pass
Carry test	Pass
Loop test	Pass
Shift test	Pass
Pipeline test	Pass
Mem-Pipeline test	Pass
Conditional Execution test	Pass
No-waiting pipeline test	Pass
Conditional Branching test	Pass
Ill Instruction test, NULL PC	Pass
Ill Instruction test, two	Pass
Comparison test, ==	Pass
Comparison test, !=	Pass
CC Register test	Pass
Multi-Arg test	Pass
Multiply test	Pass

chg #define DBUG 1 to #define DBUG 0 which removes the printing of hex values

String values continue to be printed

Compile jpeg

Examine where the ptrs is located

pi@mypi3-1:~/testbuilds/catzip/sw/board \$ make clean ; make ; make jpeg.txt

```
1000058: 5a 02 00 80 LDI 0x010050f4,R11 // 10050f4 <ptrs>
```

chg the location in wrsdram.cpp & rdsdram.cpp

Compile the programs arm-wrsdram & arm-rdsdram

pi@mypi3-1:~/testbuilds/catzip/sw/host \$ make

On Unbuntu

```
./pc-wbregs 0x00A01000 0x0;./pc-wbregs 0x00A01004 0x1;./pc-zipload -v ../board/jpeg;./pc-
wrsdram rgb_pack.bin;./pc-wbregs cpu 0x0f
```

On RPi3B+

```
./arm-wbregs 0x00A01000 0x0;./arm-wbregs 0x00A01004 0x1;./arm-zipload -v ../board/jpeg;./arm-
wrsdram rgb_pack.bin;./arm-wbregs cpu 0x0f;
```

```
00a01000 ( )-> 00000000
```

```
00a01004 ( )-> 00000001
```

Halting the CPU

Memory regions:

```
Block RAM: 00a00000 - 00a02000
```

```
SDRAM : 01000000 - 02000000
```

Loading: ../board/jpeg

```
Section 0: 01000000 - 01045160
```

```
Writing to MEM: 01000000-01045160
```

Clearing the CPUs registers

Setting PC to 01000000

The CPU should be fully loaded, you may now start it (from reset/reboot) with:

```
> wbregs cpu 0x0f
```

CPU Status is: 0000060f

The size of the buffer is 0x00ffff or 65535 words

READ-COMPLETE

02000000 ()-> 0000000f

splitting red sub ban

fwd lifting step onl

starting red dwt

in singlelif

> Z

CMD: Only sent 0 bytes of 3!

in singlelif

> Z

CMD: Only sent 0 bytes of 3!

in singlelif

in singlelif

> Z

CMD: Only sent 0 bytes of 3!

in singlelif

in singlelif

testing test_fwd

finished ted dwt

pi@mypi3-1:~/testbuilds/catzip/sw/host \$ rm -f dwt.bin ;./arm-rdsdram dwt.bin

Write-COMPLETE

The size of the buffer is 0x00ffff or 65535 words

