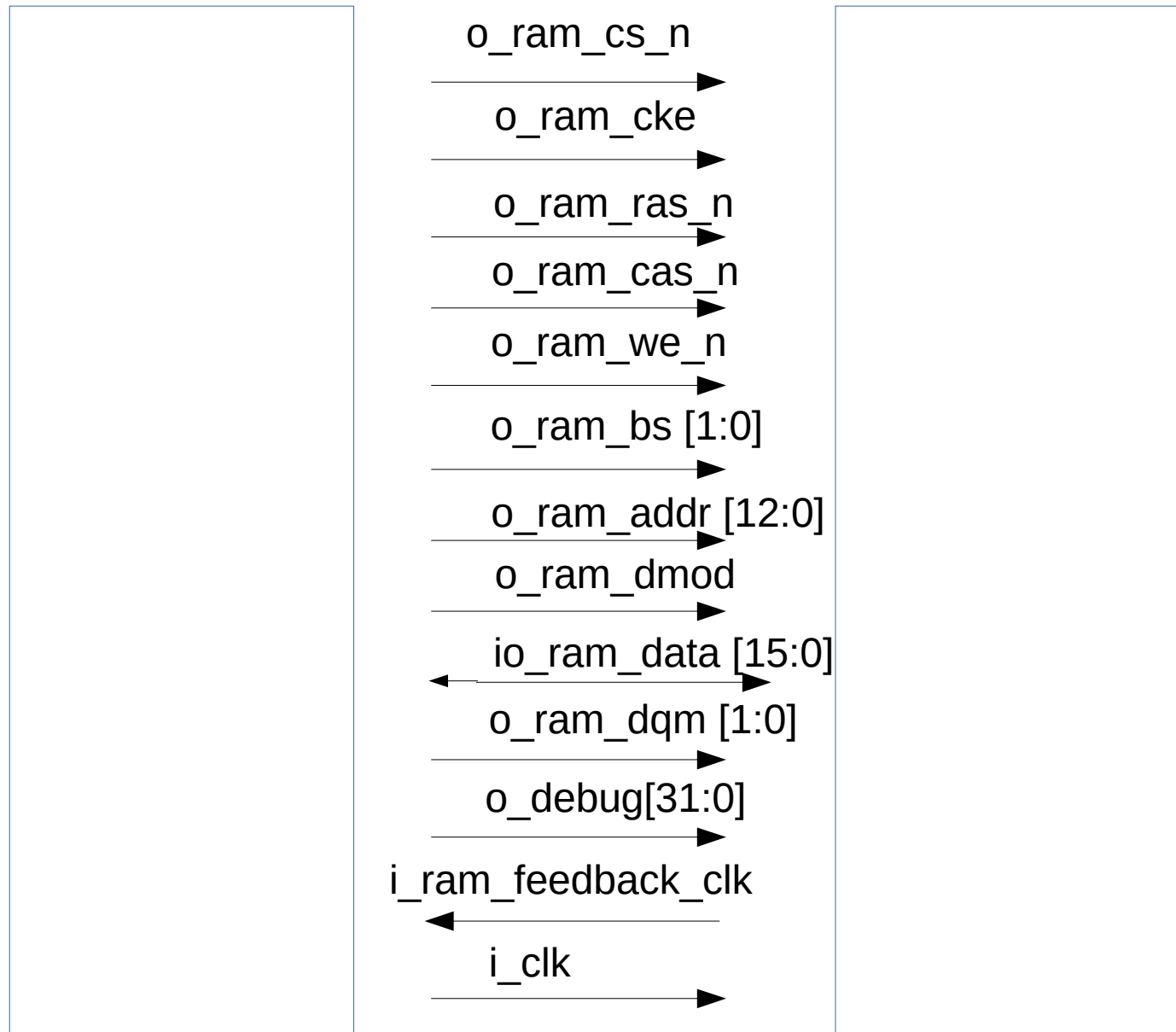
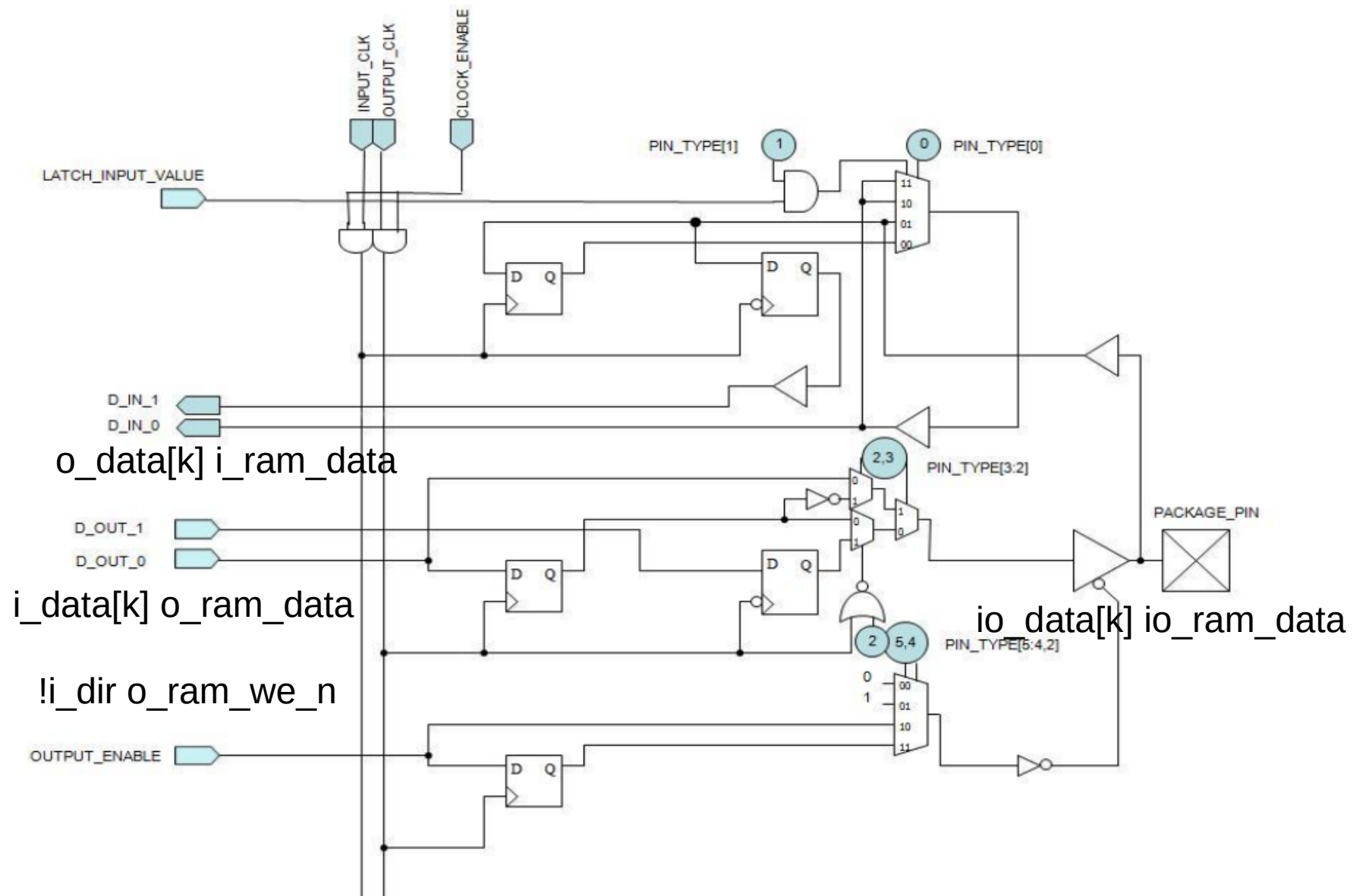
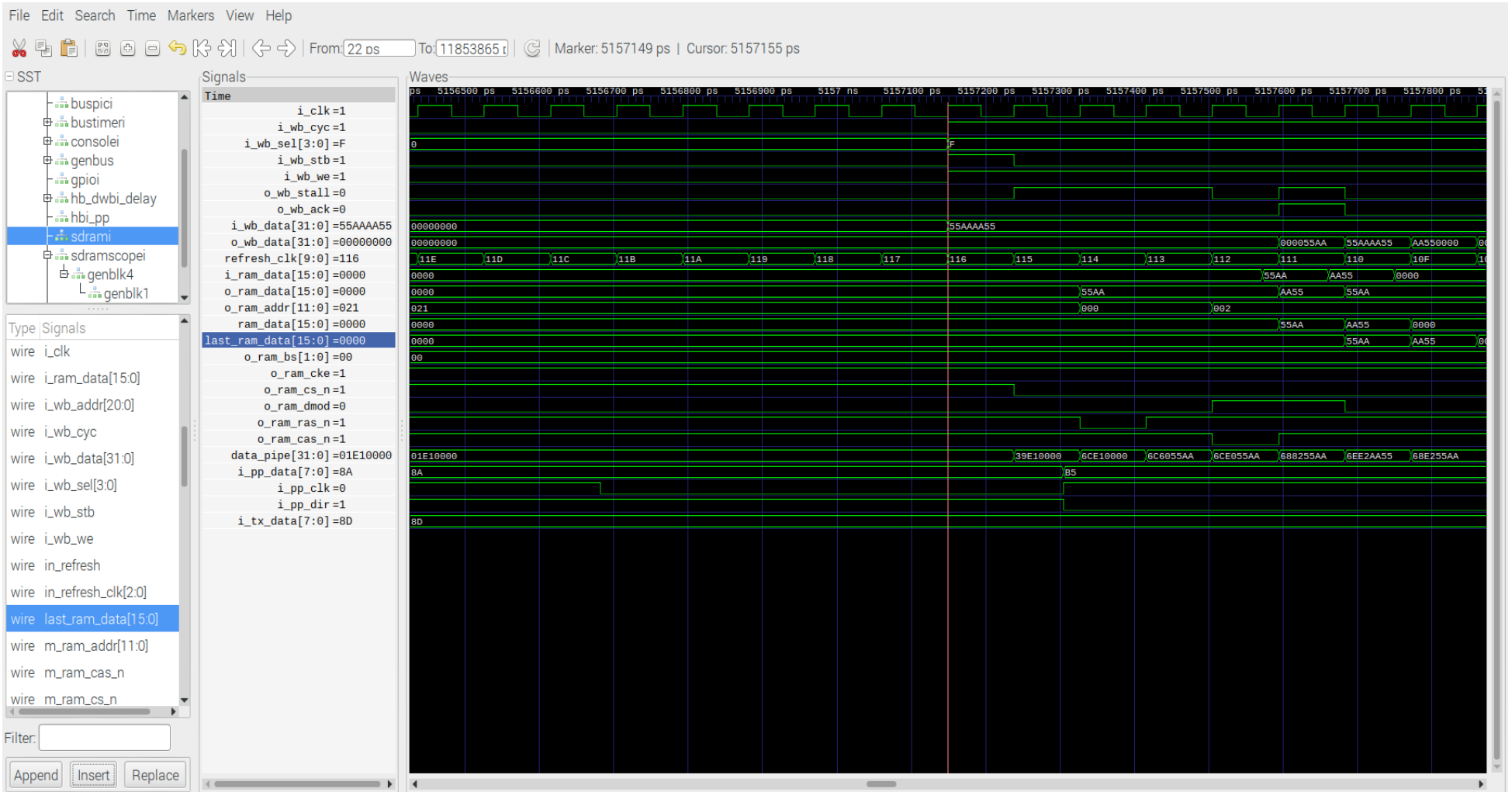


FPGA

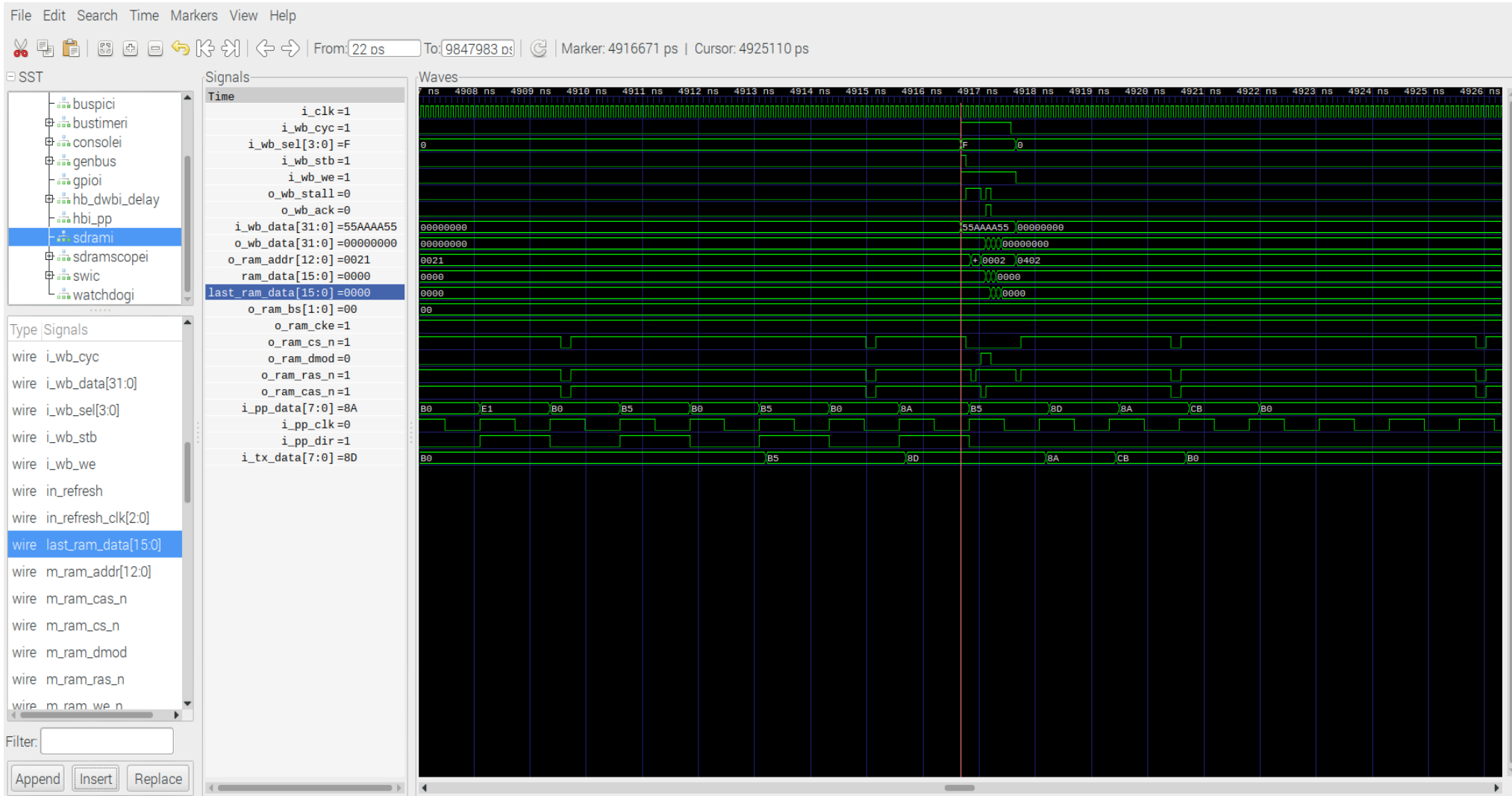
wbsdram



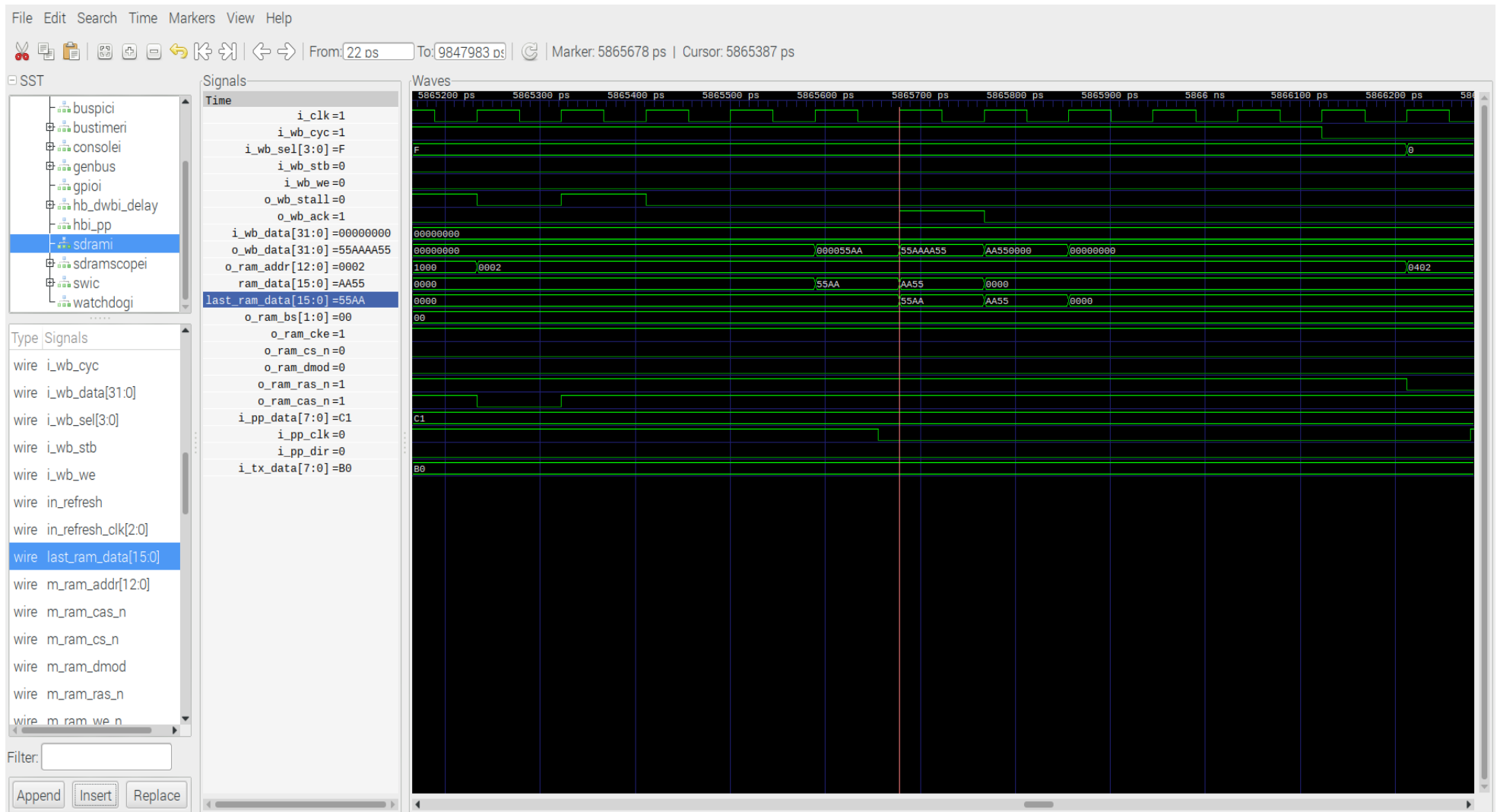




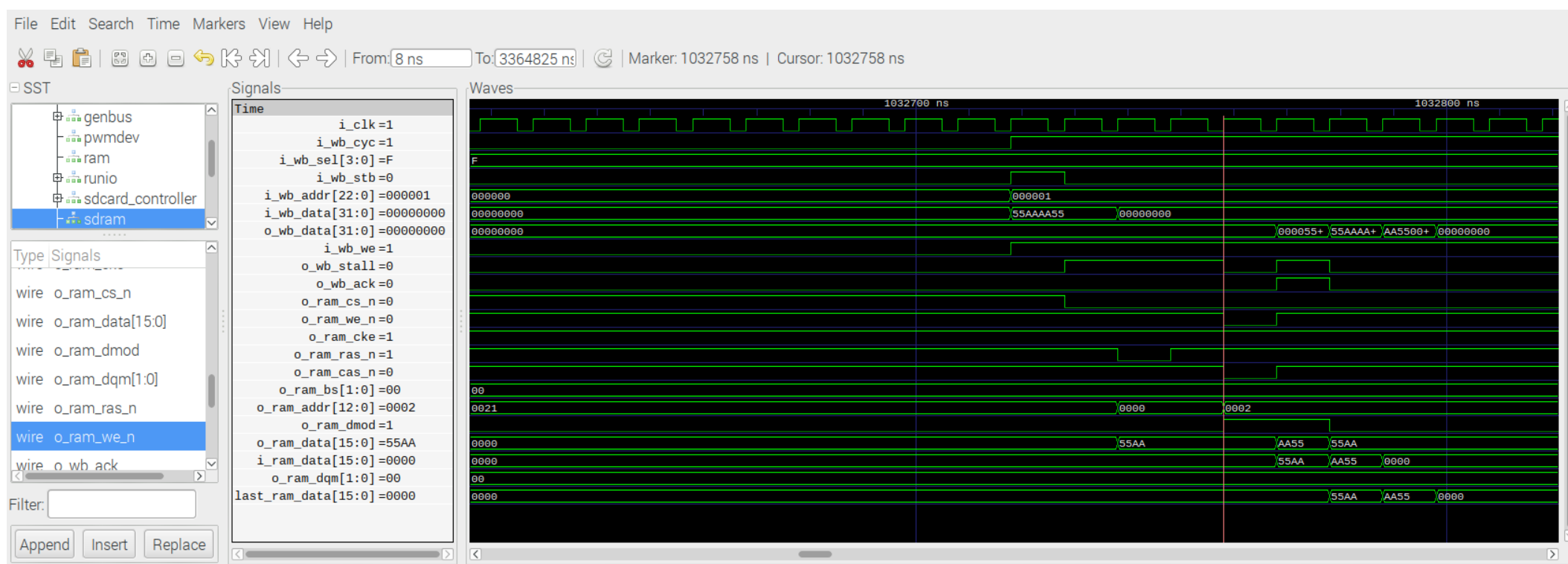
Catzip Write 0x01000004 0x55aaaa55



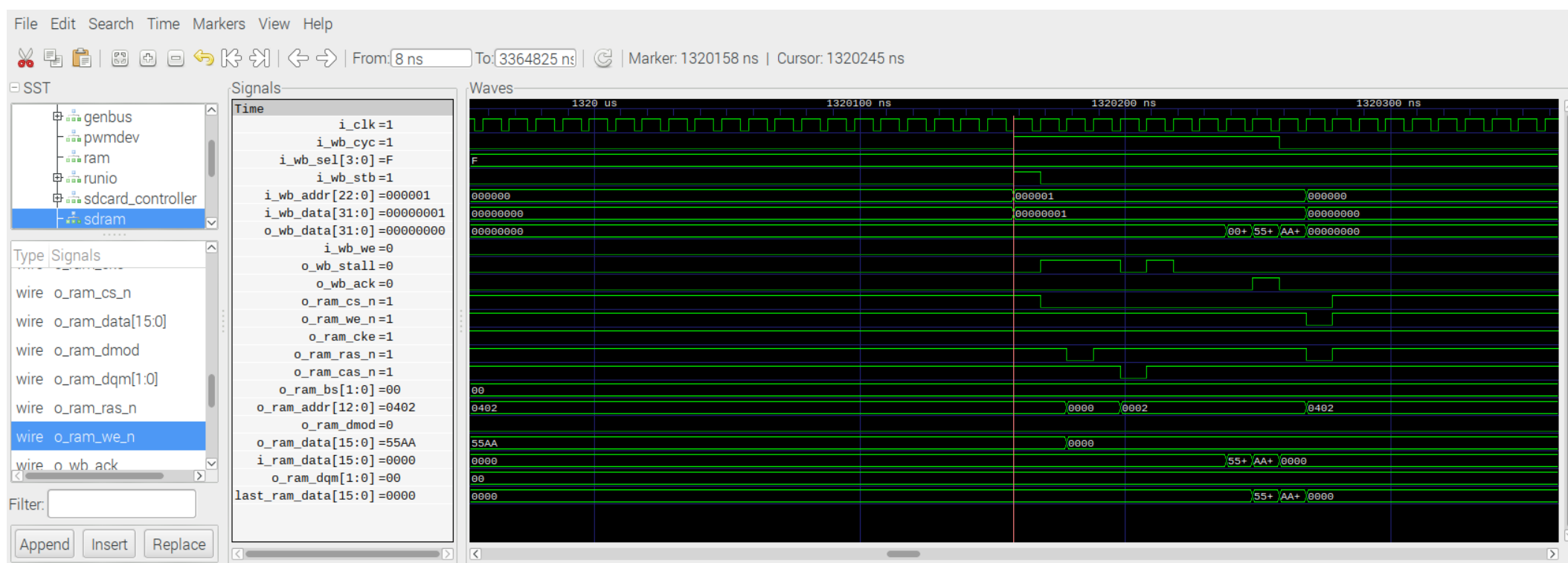
Catzip Read 0x01000004



Xula Write



Xula Read



Write Times

- xulalx25soc wr
- i_clk 7nsec hi 3nsec lo 70 % duty cyc
- 718 i_clk goes hi
- 725 i_clk goes lo
- 228 i_clk goes hi
- 718 i_wb_cyc goes hi
- 728 o_ram_cs_n goes lo
- 738 o_ram_ras_n goes lo
- 758 o_ram_we_n goes lo & o_ram_cas_n goes lo & o_ram_dmod goes hi
- 40nec
-
- i_clk 36nsec hi 35nsec lo 50 % duty cyc
- 345 i_clk goes hi
- 381 i_clk goes lo
- 416 i_clk goes hi
- 345 i_wb_cyc goes hi
- 416 o_ram_cs_n goes lo
- 487 o_ram_ras_n goes lo
- 629 o_ram_we_n goes lo & o_ram_cas_n goes lo & o_ram_dmod goes hi
- 284nsec 7.1 times longer than xula
-