

*****Default*****
pico-ice with Digilent PmodVGA program using PipelineC
oss-cad-suite
ghdl
ghdl-yosys-plugin
Raspberry Pi5 8Gb
blue blinky led
uart-echo
12/06/24
*****Default*****

git clone <https://github.com/develone/PipelineC.git> -b test-dev.

Note: The oss-cad-suite for arm does not include ghdl & ghdl-yosys-plugin.

Download from <https://github.com/YosysHQ/oss-cad-suite-build/releases/>

oss-cad-suite-linux-arm64-20241128.tgz

```
export PATH="/home/devel/oss-cad-suite/bin:$PATH"
```

```
git clone https://github.com/ghdl/ghdl
```

```
cd ghdl
```

```
./configure --with-llvm-config
```

```
make
```

```
sudo make install
```

```
git clone https://github.com/ghdl/ghdl-yosys-plugin.git
```

```
cd ghdl-yosys-plugin
```

```
make
```

```
sudo make install
```

```
export PIPELINEC_REPO="$HOME/PipelineC"
```

```
devel@pi5-80:~/pico-ice/PipelineC/vga-pll/ice_makefile_pipelinec $
```

```
make pipelinec
```

```
make gateway.bin
```

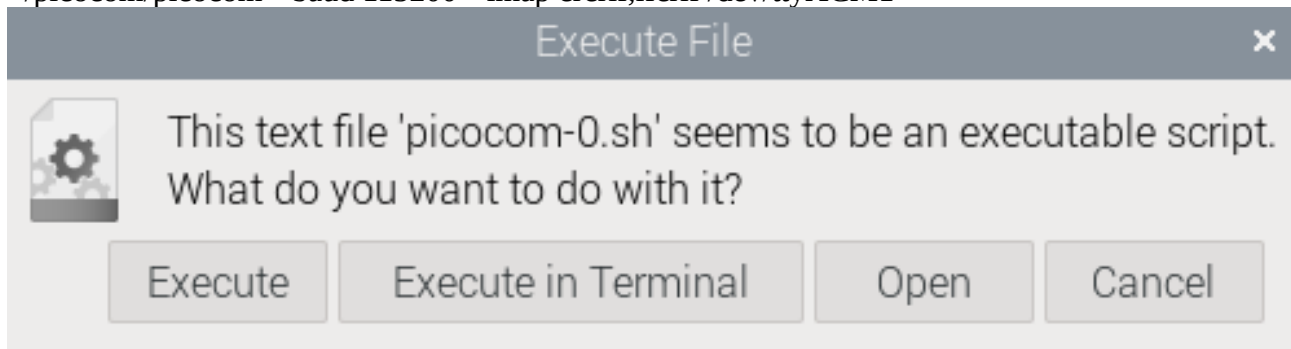
```
bin2uf2 -o gateway.uf2 gateway.bin
```

```
co gateway.uf2 /media/devel/pico-ice
```

```
picocom-0.sh
```

```
#!/bin/bash
```

```
~/picocom/picocom --baud 115200 --imap crclrf,lfcrlf /dev/ttyACM1
```



Depress Execute in Terminal

```
picocom.sh
File Edit Tabs Help
parity is      : none
databits are   : 8
stopbits are   : 1
escape is      : C-a
local echo is  : no
noinit is      : no
noreset is     : no
hangup is      : no
nohlock is     : no
send_cmd is    : sz -vv
receive_cmd is : rz -vv -E
imap is        : crcrlf,lfcrLf,
omap is        :
emap is        : crcrlf,delbs,
logfile is     : none
initstring     : none
exit_after is  : not set
exit is        : no

Type [C-a] [C-h] to see available commands
Terminal ready
JUST RECEIVED MY vga pmod.  iT HADT DELAYED IN aNCHORAGE aK. COMING FROM nEW tAI
PEI cITY, tw. i JUST ORDERED 2 UNITS ON 12/01/24 tHIS IS WAS PROGRAMMED WITH pIP
ELINEc i SUPPORTS THE UART ALSO
```

Uart echo



pico-ice with PMODVGA

