## 

## Steps to add a UART to iCE40UP5K Nandland UART\_RX.v Nandland UART\_TX.v 09/11/24

pico\_ice\_default\_firmware\_v1.6.1.uf2 RP2040

```
File Edit Tabs Help
Welcome to minicom 2.8
OPTIONS: I18n
Port /dev/ttyACM0, 09:55:13
Press CTRL-A Z for help on special keys
pico-ice> cdcd_control_xfer_cb: coding=200014B2 itf=0
tud_cdc_line_coding_cb: coding=200014B2 baud=115200
pico-ice default firmware
  https://github.com/tinyvision-ai-inc/pico-ice/tree/main/Firmware/pico-ice-dt
Serial port #0 - this shell, with commands:
 v - print pico-ice-sdk version
Serial port #1 - forwarding to UART
 UART TX on RP0 = ICE27
 UART RX on RP1 = ICE25
Serial port #2 - forwarding to SPI:
 https://pico-ice.tinyvision.ai/group__ice__usb.html#autotoc_md2
pico-ice> vpico-ice-sdk v1.6.1
pico-ice>
CTRL-A Z for help | 115200 8N1 | NOR | Minicom 2.8 | VT102 | Offline | ttyACM0
```

minicom myusb0 sudo minicom -s

blu tx ice25 red rx ice27

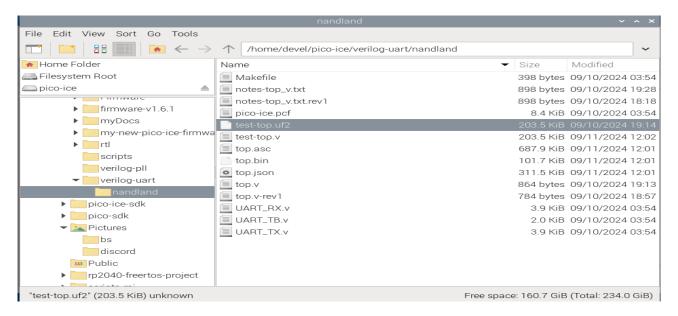
/dev/ttyUSB0 /dev/ttyACM1

devel@pi5-80:~/pico-ice/verilog-uart/nandland \$ make devel@pi5-80:~/pico-ice/verilog-uart/nandland \$ bin2uf2 -o test-top.v top.bin

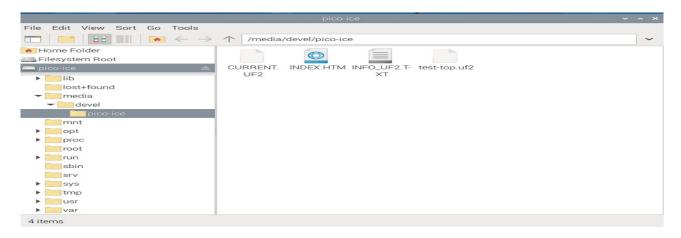
### Open pico-ice



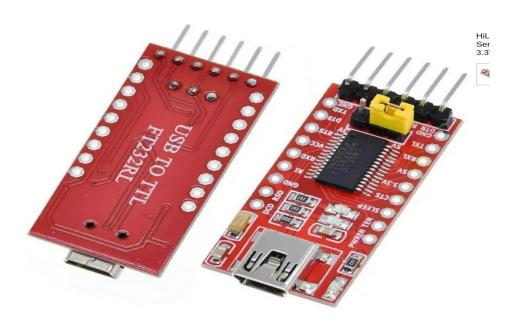
## Select test-top.uf2



#### Paste test-top.uf2 in pico-ice.



# HiLetgo FT232RL Mini USB to TTL Serial Converter Adapter Module 3.3V/5.5V

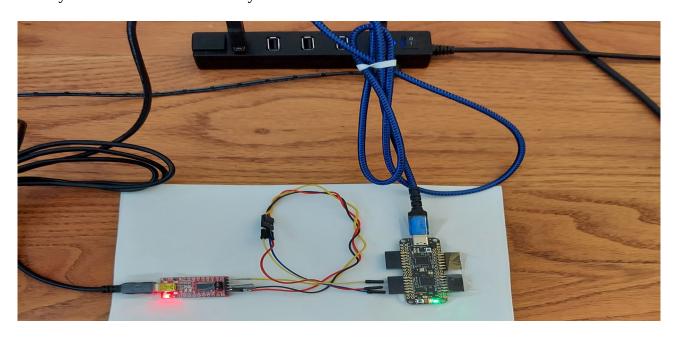


## Below are the connections between the USB to FTDI

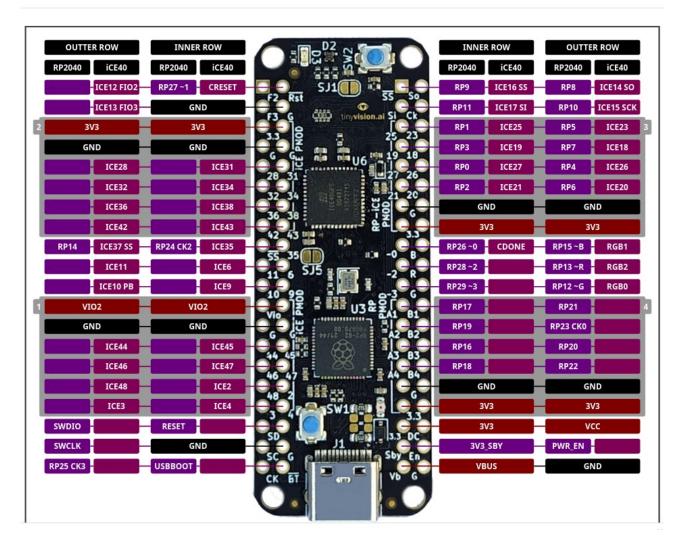
sudo minicom -s ice25 minicom myusb0

blu tx ice27 red rx

/dev/ttyACM1 /dev/ttyUSB0



## **Pinout Diagram**



left minicom myusb0 /dev/ttyUSB0 right sudo minicom -s /dev/ttyACM1

Change default /dev/ttyACM0 to dev/ttyACM1

### Depress enter

```
Serial Device
                           : /dev/ttyACM0
В
    Lockfile Location
                             /var/lock
      Callin Program
     Callout Program
       Bps/Par/Bits
                             115200 8N1
    Hardware Flow Control
                             No
G - Software Flow Control
                             No
        RS485 Enable
                             No
      RS485 Rts On Send
                             No
    RS485 Rts After Send
                             No
    RS485 Rx During Tx
                             No
    RS485 Terminate Bus
                             No
M - RS485 Delay Rts Before:
                             0
N - RS485 Delay Rts After :
                             0
   Change which setting?
```

Type A enter

```
/dev/ttyACM1
       Serial Device
В
    Lockfile Location
                             /var/lock
      Callin Program
D
     Callout Program
       Bps/Par/Bits
                             115200 8N1
    Hardware Flow Control
                             No
    Software Flow Control
                             No
        RS485 Enable
                             No
      RS485 Rts On Send
I
                             No
J
     RS485 Rts After Send
                             No
     RS485 Rx During Tx
                             No
     RS485 Terminate Bus
                             No
    RS485 Delay Rts Before:
                             0
    RS485 Delay Rts After :
   Change which setting?
```

## Change 0 to 1

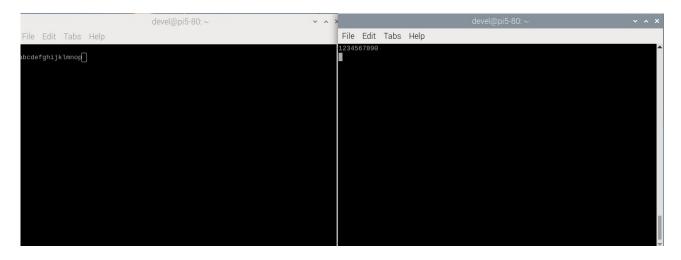
scroll to Exit enter

Typing in left terminal is displayed in right terminal.

Testing 09/11/24 after adding the pll with test-top.uf2

When characters **1234567890** are typed in the window *on the* left which is connected to "/dev/ttyUSB0". They appear in the window on the right connected to "/dev/ttyACM1".

When characters **abcdefghijklmnop** are typed in the window on the right which is connected to "/dev/ttyACM1". They appear in the window on the left connected to "/dev/ttyUSB0".



"/dev/ttyUSB0"

"/dev/ttyACM1"

The tx from left is connect pico-ice PMOD ICE\_25 and rx is MOD ICE\_27 see the pico-ice Pinout Diagram above.

With minor changes to the top.sv to detect the toggle of ICE\_25 the blinking green led of the picoice show a spec of red.

```
diff --git a/pico-ice/my-new-pico-ice-firmware/ice_makefile_blinky/top.sv
b/pico-ice-sdk/examples/ice_makefile_blinky/top.sv
index 8c90cb5..88f0a17 100644
--- a/pico-ice/my-new-pico-ice-firmware/ice_makefile_blinky/top.sv
+++ b/pico-ice-sdk/examples/ice_makefile_blinky/top.sv
@@ -4,28 +4,17 @@ module top (
 output LED_R,
 output LED_G,
 output LED_B,
- input ICE_25,
);
 localparam N = 22;
 reg [N:0] counter;
- reg in1;
- always @(posedge CLK) begin
- counter <= counter + 1;</pre>
- end
 always @(posedge CLK) begin
  if (ICE_25 == 0)
  begin
```

```
in1 <= 1'b0;</li>
end
else
in1 <= 1'b1;</li>
counter <= counter + 1;</li>
end
```

XX

steps to program the pico-ice ice40UP5K

 $\label{lem:condition} \mbox{devel@pi5-70:$$\sim$/pico-ice/my-new-pico-ice-firmware/ice_makefile_blinky $.$$ $\sim$/OSS_CAD_SUITE.sh}$ 

/usr/local/

devel@pi5-70:~/pico-ice/my-new-pico-ice-firmware/ice\_makefile\_blinky \$ make clean rm -f \*.json \*.asc \*.bin \*.uf2

devel@pi5-70:~/pico-ice/my-new-pico-ice-firmware/ice\_makefile\_blinky \$ make /usr/local//bin/yosys -q -p "read\_verilog -sv top.sv; synth\_ice40 -top top -json gateware.json" /usr/local//bin/nextpnr-ice40 -q --randomize-seed --up5k --package sg48 --pcf ice40.pcf --json gateware.json --asc gateware.asc

/usr/local//bin/icepack gateware.asc gateware.bin

devel@pi5-70:~/pico-ice/my-new-pico-ice-firmware/ice\_makefile\_blinky \$ bin2uf2 -o grn-red.uf2 gateware.bin

Drag-Drop the grn-red.uf2 file on the pico-ice.

