Simulation/Co-Simulations Tools

Edward Vidal Jr.

Tools

- MyHDL
- VHD2VL
- Iverilog
- Python

```
MyHDL ──┬──▶ VHDL files
        │
        ├──▶ Verilog files
        │
        └──▶ Simulator/      ──▶ Verilog files
             co-simulator
```

```
VHDL files        ──▶  vhd2vl  ──▶  Verilog files  ──▶  Iveriog/vvp
from another                                             cosimulation
source
```