

190130107010

GOVERNMENT ENGINEERING COLLEGE GANDHINAGAR



MICROPROCESSOR AND INTERFACING

(Subject Code - 3160712)

Name: SAKSHI BHEDA

Enrollment No. : 190130107010

Class :CE 6th SEM

Division : A

Batch : A1

190130107010

GOVERNMENT ENGINEERING COLLEGE,
SECTOR-28, GANDHINAGAR



CERTIFICATE

This is to certify that Mr./Miss SAKSHI BHEDA Enrollment No. 190130107010 has satisfactorily completed the term work for the subject **MICROPROCESSOR AND INTERFACING(3160712)** prescribed by Gujarat Technological University during the academic term 2021-2022.

Date:

Signature of faculty

Institute (GECG):

Vision:	To be a premier engineering institution, imparting quality education for innovative solutions relevant to society and environment.
Mission:	<ul style="list-style-type: none">● To develop human potential to its fullest extent so that intellectual and innovative engineers can emerge in a wide range of professions.● To advance knowledge and educate students in engineering and other areas of scholarship that will best serve the nation and the world in future.● To produce quality engineers, entrepreneurs and leaders to meet the present and future needs of society as well as the environment.

Department (CE):

Vision:	To achieve excellence for providing value based education in Computer Engineering through innovation, teamwork and ethical practices.
Mission:	<ul style="list-style-type: none">● To produce computer science and engineering graduates according to the needs of industry, government, society and scientific community.● To develop partnership with industries, government agencies and R and D Organizations● To motivate students/graduates to be entrepreneurs.● To motivate students to participate in reputed conferences, workshops, symposiums, seminars and related technical activities

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SR NO.	NAME	SIGN	DATE
1.	To study basic terminology and architecture of microprocessor.		
2.	To study features and architecture of 8085 microprocessor with its pin functions.		
3.	To study and execute basic instruction of 8085.		
4.	To study interfacing memory as well as input/output elements with a microprocessor.		
5.	To study features of advanced processors.		