| | Page No. | | | | |
|--------------|--|--|--|--|--|
| 4/1/19 | Date | | | | |
| and the same | Tabel CPUs = x86 | | | | |
| | Computer - CPU, RAM, Pods (I/O) | | | | |
| | - System bus to connect these | | | | |
| | No user application duredly accesses transducte, but through of | | | | |
| | | | | | |
| | - Convenience for programmers | | | | |
| | | | | | |
| | 05 provides a set of functions to programmers - "System Call" | | | | |
| | | | | | |
| , | - Isolation C | | | | |
| | - Sharing between programs, weers, etc | | | | |
| | | | | | |
| | - Efficiency - of memory allocation, etc. | | | | |
| | • | | | | |
| | secretaria de la companya della companya della companya de la companya della comp | | | | |
| | → CPU _s | | | | |
| | - follows some ('ossembly') language instructions | | | | |
| | - has registers to LOAD to a STORE from | | | | |
| | Program being executed is in main memory (RAM) | | | | |
| | Compilation: - c - executable file | | | | |
| | (Assembly) | | | | |
| | - Executable files contain instructions that CPU can understand | | | | |
| | - Same of these could be OS-specific instructions. | | | | |
| | - Executable files are stored in ROM, have to be shifted to | | | | |
| 1 | RAM for execution | | | | |
| | - They cooten instructions and data (variables) | | | | |
| | | | | | |
| | Fxecution: | | | | |
| | - CPU loads stuff from RAM to registers. | | | | |
| | - PC points to instruction being executed. | | | | |
| | - Fetch + Decode + Execute instruction | | | | |

| | Paga Na. Dotto | Page No. Date |
|---------------|--|--|
| | System calls | |
| | . There are 05-specific instructions in executable file that | 5 des clls Va F I II |
| | CPU their not understand (accessing transmare, etc) | System cells 1/s Function cells |
| | - PC is paused. OS code is accessed. OS helps run that | og - printf() in C library. |
| | instruction, then returns to executable instructions, | . Library and is at the same privilege level as after user orders |
| | - Privileged' made of execution for system calls. | The simply a convenient packaging for common codes. |
| | - Function cells run in normal mode. | · System calls work in privileged made |
| | | System sails took in principles mode. |
| | * Hierarchy of eaches for efficiency | and the state of the |
| | | |
| | | M4 |
| | Process Busing program | |
| | - Job of O5 is process management' | |
| | - life cycle of a process - Creation, support, tilling | |
| | - OS should also multiplex processes for afficiency. | |
| | - Many processes are created and each takes turns | |
| | to be executed on the CPU, whether pertuly or | |
| | entirely No | |
| | - They process perceives this multiplexing. | |
| | Witholizing of CDV | the state of the s |
| | · Momany for a single process may not be configurably stored. | the state of the s |
| | - Tech individual process believes it has the entire RAM to dealf. | and the set of the set |
| | - 05 manages this RAM allocation. | |
| | | |
| | Concurrency management' | |
| | | - A |
| | | |
| \rightarrow | Parts of course: D Process management | |
| | @ Memory management | |
| | 3 Concurrency | The State of the S |
| | @ T/O management Davice drivers | The second secon |
| 10 | | |
| | | The state of the s |

| Page No. Dotto | Page No. |
|---|--|
| /ILIA PROCESS MANAGEMENT | Date |
| Memory image of program is loaded from ROM to RAM. | . Memony image has addresses from zero to max RAM value |
| 2 CPU registers (like PC) contain intermedial appear | - Every process believes it can use the coher RAM |
| being run through its memory image | |
| · CPU Context & Mamory mage + CPU information | → System Calls |
| | - PC in CPU context jumps from memory image codo to so |
| · Memory image is destroyed when process is completed, but may | external specified code for OS work |
| not be destroyed asap | |
| · IF program uses dynamic memory allocation, the executable | T MULTIPPE PROCESSES |
| closs not know how much momory to allocate to that variable | |
| - Memory image contains some vacant momony (heap) for | · Some system cells are 'blocking' by nature |
| such variables. | - That process is paused, some after process is executed in the |
| - IF necessary, heap size can be increased for a process by | eg scant (): Reading from keyboaded |
| · Local variables to be initialized by a Function black are not | The contains a blocking system call |
| allocated memory in executable file, because the function | and the second s |
| might be called for many times or not called at all | • Blacking :- |
| - These are managed using a 'Stack' in the mercury image, | Store CPU context of current process somewhere. Retrieve CPU of some other process. |
| Memory Image = Code + Data + Ham + Stack | of same street process. |
| Corporation | Pragrama Control Block (PCR) |
| exercisis Code+Date To hoop and stock collide, | - Data structure that contains into about a process. |
| Heap as much do and mare memory | - One PCB for each process |
| Stack | - CPU context is stored in its PCB - "State" of a process (Running or Blocked) is stored in I |
| | State of a passage |
| | Polling ' for external hardware |
| - Hierarchy of stacke frames | - Wait for external hordware to do its thing |
| | |
| | |
| | AND THE STATE OF T |

| | - | |
|--|---|---|
| Page Na. Date | | Page No. |
| Ditercupts' from external hardware. Current process is stopped even though it did not make a blocking system call and its info is saved in PCB. | | - Programming languages (C library) any way repartage system calls under different functions So, programmer does not |
| Execution is passed on to 'Interrupt Hander' which is a part of OS sade. Rocean Pl words input from toyboard. Pl is blacked, P2 is | H | SYSTEM CALLS |
| unburned headler. - 3rd 'state' = "Ready" (for execution) | 1 | - Parent process forks to produce a child process - Torking is the only view to create processor |
| - P2 becomes 'roody' when interrupt is being executed - On recovery input from keyboard, P1 becomes 'roody' | | - The first process is called "init" process Fermind "shell process (Comend prompt). GUI GUI process GUI process |
| Tital 5 states of process: - {Running, Blockert, Rearly, Embrya, Terminated} bong pending created cleanup. | | eg - man () () > Clippany function which samply colle |
| Burning - Ready) Interrupt occurs | | if (ret==0) fock system call 3 // child program } else {// parent program } |
| Discretion of schooling for too long. Discretion of schooling | | - Child continues executing the same program as parent, but value of rat() is different - To the parent, the return value 'rel' is the process ID (P(D) of the child |
| Agreems written in any language can be run an any OS. - "POSIX" API - Programming OS Interface - Common interface that is charad by many OSes | 2 | The exact) system cell |
| - Names of system cells for different Posix complications of system cells for different Posix complications on any different position of system cells for different positions of system cells for different po | | - Used in child pragram (ret = a) black to Takes argument as an executable file or binary file. Then the child pracess on CPU context is entirely renowed |
| - Source code cop by run on different OSes, | | bryn the new cock instead. |

| | Page No. Deta | Page No. |
|----------|--|---|
| | | |
| | * Many shell commands are just pra-compiled (C) programs | . If a child's parent is terminated before it is, the child's parent PD |
| | , | is changed be 'init' |
| | | - init periodically runs west () to dostray its adapted orphans |
| 3 | get pid () | 7. Process |
| | - returns that process' PID. | If you know that C2 will take a long time to complete and you don't want its perent to wait ground to till it, the perent |
| | - init has PID= 1 | don't want its parent to wait ground to trill it, the parent |
| | PCB of any process contains self-PID and parent's PID | torks twice. |
| - | to help construct hierarchy of processes | P -> C1 -> C2 |
| | - It does not have child's PID. | Then CI is exited, P works to destroy CI. Then C2 is |
| | | adopted by init and P can die in peace. |
| - | exec() (Cotd) | |
| | - Entire memory image is rewritten. | |
| | - No useful code should fallow exec() statement a child code | * Inter-process Communication (IPC) |
| | - Further code is run only if exec() falls (to return an | • Signals |
| | e rrar | eg - When you press Ctrl+C, a signit signal is sent to |
| | | current process, |
| <u> </u> | exit () | - kill () (C function & system call) |
| | - Terminates process | - can be used to send a signal from one process to another |
| | - This is added by compiler at the end of code. | - Signal Handler - Handles recoved signals of different types |
| ,—— | - Termination is not instant. Initially, the state of process | - Programmer can overwrite signal handler code to modify |
| | is changed to 'Terminated' | how receiver process reacts to a signal |
| | - F'Terminated' process is destroyed (cleaned up) only when | - OS allows pracesses to send eignals only to processes created |
| | its parent calls wait () | by the same user, for security reasons |
| | - V | |
| 5 5 | wait() - Reaping process | - Signal number 9 is 'kill', which formingtes process. |
| | · Destroys or anything of your 'terminated' child processes | - Signal handler for till cannot be overwritten, so that |
| | Blacks sucreat process if no child is terminated | processes don't continue to run forever. |
| - | · Every process must run wait () ance for each child | AN A |
| | Exery process must ain wait () ance for each child building wait () ance for each child is himselfed | - By default, a signal is sent to an entire process group |
| 6 0 | vait pid() | (containing children and grandchildren along with parent) |
| | Destroys a specific p child process | |
| - | Done not black if an child is terminated | |

| | Page No. | Page No. Date |
|--------------|--|---|
| | ey - On pressing Ctrl+C, parent and all its children should die But by middled signal handling, a hash shell, an recovering Ctrl+C, hills its children but not itself: | - Os has an "Intercupt Descriptor Table" (IDT) - Mapping: Type of Intercupt/system call → Address in Karnel Mamar - Used by update PC - Loaded during boot-up at the end of which into process is created |
| * | Function Call Function Call Figure Frecutable | ** Kernal = Core part of OS = 05 - Utilhes like desktop, wellpaper, els |
| | mein() } f() Jmp f() } f() } When function is called, Store old PC in stack, along with arguments, local variables, right | Trop' instruction — Before system cell interrupt pregram faults (1) - Celled just before system cell (2) (3) (4) Update SP b kernel stack (5) Push old PC, etc. (6) Update PC b within beenel memory by checking IDT |
| | New stack frame ' 2) Update PC 3) Restore stack stuff. | - Refore calling trap, the required arguments (relating to which system cell is to be called) must be stored in pre-defined registers (specified in CPU manual) |
| - | The PC of Fraction might as well be in a different file (eg - | (2) - CPU gutomotically calls trop for external events, interrupts - Every type of interrupt has an IRQ (Interrupt Request) number That corresponding entry in IDT is jumped to |
| D | System Call | (3) - Trop is called for any fault in programs - Segmentation fault, clivide-by-O, inclex out of bounds, etc. - Process is eventually killed |
| | - Present in a part of memory that users are not allowed to access. | Return from Trep, instruction 1) Restare stock stuff 1) Update 5P to user stock 2) Go to user made of CPU |
| | - CPU can access to protected memory to Kornel made | a) Reslare PC |

| Page Na | |
|---|---|
| Date | Page No. |
| | Date |
| . It you don't want to return from trop call back to that process | II FILE DESCRIPTOR ARRAY |
| (eg - process calls exit), program fault, process calls blacking | * Along with self and parent's process ID, PCB also stores a |
| System call) | |
| Kernel scheduler decides which process to return to | to stores the file descriptors of all files that the process is allowed |
| "Content Switch" | - File descriptor : Are interest allatted to each file and |
| - Type of Keened schedulers: | - File descriptor . An integer allotted to each file used in code |
| 1) Non-pracomptive - south paxess only when current process | when a child is born (forked), it inherits the parent's FDA (with same files and indices) |
| is completed or gives an error. | (with same files and indices) |
| 2) Pre-emptive - Switch processes regularly may way | - The init process already has 3 fd's, which all processes |
| are polynite | |
| - Contact switch: | a) STDIN: Standard Toput (usually beyboard) read(0,) 1) STDOUT: Output screen write(1,) **2) STDERP: Free |
| Tump from keinel made of one process to that of gnother process | X 2) STREAD. |
| which had moved to bornel made some time in the post | - Sillen - Freae |
| Rangel made -> Kernel Mode | · open() and class() are system calls for files. |
| eg P2 was in user mode (U) | • open() and close() are system calls for files. • "I/O Redirection" |
| - It switched to become prode after atoming user antext | eg print ("Hollo"); // Prints on screen |
| based on where it left user rade from | C/020 (1) - |
| trend context, based on where it loft herael code from | printf ("Holle"); // Error |
| - Execution moved to bernel mode of PI based on the | fd = open (for tx1) II fd = 1 (first empty across inday printf ("Hollo") I writes to footh! |
| ternal contest that PI had proviously stored | proff ("Hollo") // writes to foo. 1x1. |
| - It Plante a new process, its kernel context had been | eg Shell command: \$./a.out > footxl |
| tostalized to some dummy stuff. This is done by bock () | Shall prints nothing. All printed output goes to facitat. |
| rteal. | Implementation from shell: - ret = Fak(); |
| | if (rot == 0) { close(1); |
| - This is why parent has to clean up its terminated shid. | mon(fon tet) |
| - When child calls exit() it is a goes into its frence mode | expc (9,04); |
| Tou cappat erase kernel stark while you are coorking in the | 7 |
| transfer process can be to changed and by some | · exec() does not clear file descriptor array. |
| other pracese, after jumping to its kernel made. | · · |
| | |

| | Page No. | Ν. | amed / Aronymous pipas |
|---------------|--|----------|---|
| | Dota | | Page No. Date |
| 70 -> | Titer process Her Communication (TPC) (Contra) | | |
| | Shared Memory | 11 | - IF pipe buffer is full, water will block |
| | Shared Memory - Can be shared between processes by calling a few system all | | empty, read() |
| | 77777777 | | - Reading data also clears, up the buffer - Can be made non-blacking and simply return back. |
| , | PI CQ | | |
| | - Boltz processes can read or write, edit variables, etc. | ſα | Sockets |
| | - Other process is does not understand activities of siner | 7 | fd = socket (): adds to file descriptor arra |
| | processes in some memory (not 'notified' of edits) | | Each process are can create one sochet, then connect those two. |
| B] * | Signals (done before) | | - Both can read and write. |
| | | | |
| | Rpes = Location in memory | | Socket connections also work between processes on different computers. |
| | - Pipa () returns two in file descriptors | | Connection: |
| | - One fol is for road end. Other is for write end. | | Socket binds to an it address |
| | - FPC | | Socket 2 corrects to that address. |
| | Parent creates a pipe. Forks. Parent clases one of fd's. Child | | Societ & connects to that address. |
| | | 22/1 | |
| - | Can cause cirars | T | SCHEDULING BLUES |
| | - Only one guy writes to pipe. Only one guy reads from pipe | | hilas a concers southbas to based made the OS alls the scheduler to |
| | eg \$ ls grap "fee.tv!" | 4 | When a process switches to bernel made, the OS calls the scheduler to possibly context-switch. |
| | Shell makes the shilden > De | - | Most OS's are pre-emptine |
| | Shell makes the children -> ls | | 'D I O O ' had been seen |
| | 2. 9.00 | • | "Ready Queue" has all ready processes |
| | | → | Performance Metrics: 1 Formess, efficiency, & averbee |
| | Mpe , in the second sec | | Tringraved time = (Exit() time) - (Creation time) |
| ro need for a | Both processes can run simultaneously while exchanging data, | 3 | CPU burst - Run-time of process Response time = (Time when process is first given CPU) - (Creation time) |

| Pago No. Doto | Page No. |
|--|--|
| None Policies - Knows hurst times for all processes beforehead | → Linux - Mulhlevel Foodback Queue (MLFQ) |
| - Non-pre-employe by definition | Principy-wise arrays of processes |
| 2 Sharlost Job First - I average broad time | 2 0-0-3 |
| - Could be implemented by a min beap - Lowest average broground time among all non-pre-emptive policies | - The rice() command can be used to change default practly a |
| a Procemptive Starlest Jab first / Shortest Time to completion First. | - Always execute trighest priority lovel till completion |
| - Traces that applied the | - Within a princity level, do a RD. |
| Practical Policies - Burst times unknown. | - From process keeps getting demated |
| - Pracesses in ready queue are each given a "quantum" of time | - Processes that only do a little T/o should not be |
| - Voy small quantum > 1 Propurting of exertered ? | - To prevent storyation of demoted processes, all processes are |
| - Typical quantum ~ ms - If a process blocks or localists, move to the next. | pariedically brought to privary ! |
| - Compared to FCFS: I response time is | |
| 1 Fairness 2 Priority Scheduler | |
| - Always no process with highest priority till completion | |
| A STATE OF THE STA | A Company of the comp |
| | |

| 24/1 | MEMORY MANAGEMENT | Fage No. | |
|------|--|--|--------|
| | Memory allocated to a process is usually not contiguous, for efficiency | | |
| | Every process believes it has memory & [a, some max volve] Virtual Address Space | Code segment 50 100 3000 | 2 |
| | Cada Data Heap Stack | 5170 15 fixed | fac |
| | - Virtual address - What programmer perceives | Better 2 Pages | ze |
| | - CPU also deals with virtual addresses only - OS must maintain a map: Virtual addresses - Boul addresses | - Divides virtual morning into "logical pages" of equal size, a | wthout |
| | - Map is stored on PCB. | - Granularity of allecation is 'page' - Some westage of space, as each process might not us | Se. |
| | Memory Management Unit. (MMU) - Performs address translation. Present between CPU and RAM - Input: Yukul address from CPU | the cotice page "Tolernal Fregmentstan" IDGAF | |
| | Output - Roul address to RAM - OS manages, modifies MMU | ** Wastage caused by variable segment sizes in segments was the OS's headache "External fragmentshop" | than |
| | CAU - Carbo - MMU - RAM | - Page Table': Logical Page No> Physical R-France No Stored in PCB, given to MMV. | |
| - | 5.20 of virtual address space - Dictated by number of bts in PC 32-bit architecture has 32-bit PC => 232 bytes of virtual memory | - Typical page size = 4kB -> Expansion of a process's allocated memory | |
| | Greatlerity of chunts that virtual memory is divided into, each function gets a configuous physical address space | - Duang fack, child is allotted same memory as pacent. | |
| - | | - Taitally, there is empty memory between beep end s' | back. |
| | - Davides without memory into arguments, each of which is | - System call must be made to increase heap 5120. 5 bk () or abok () , | |
| | A STATE OF THE STA | | |

