

→ NMOS char.

$$I_{DS} = 0 ; V_{GS} \leq V_T , \text{ cut-off / sub-threshold}$$

$$= \frac{H_n C_{ox} W}{L} \left[(V_{GS} - V_T) V_{DS} - \frac{V_{DS}^2}{2} \right]$$

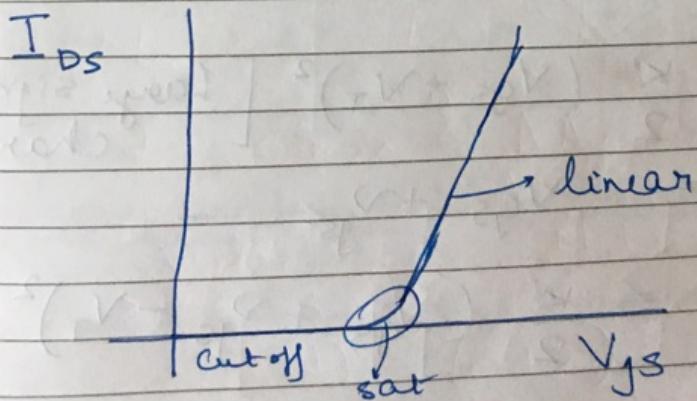
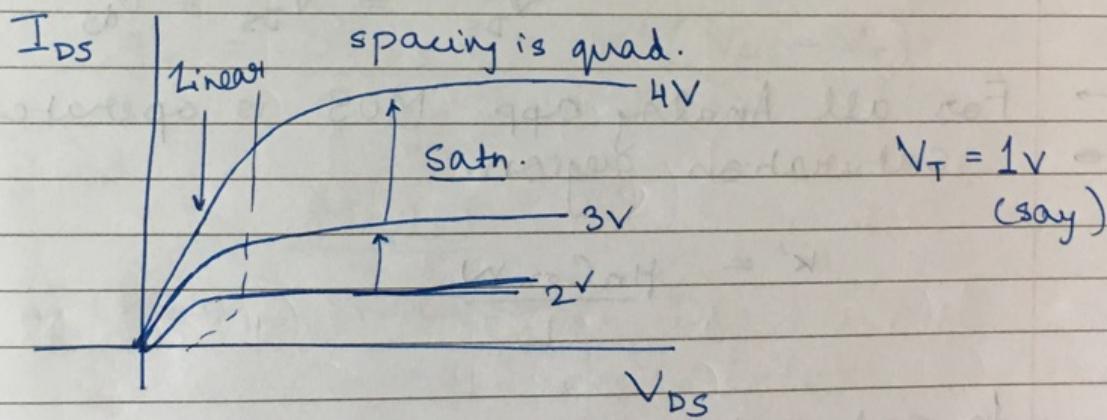
$$V_{DS} < V_{GS} - V_T$$

[Linear Reg.]

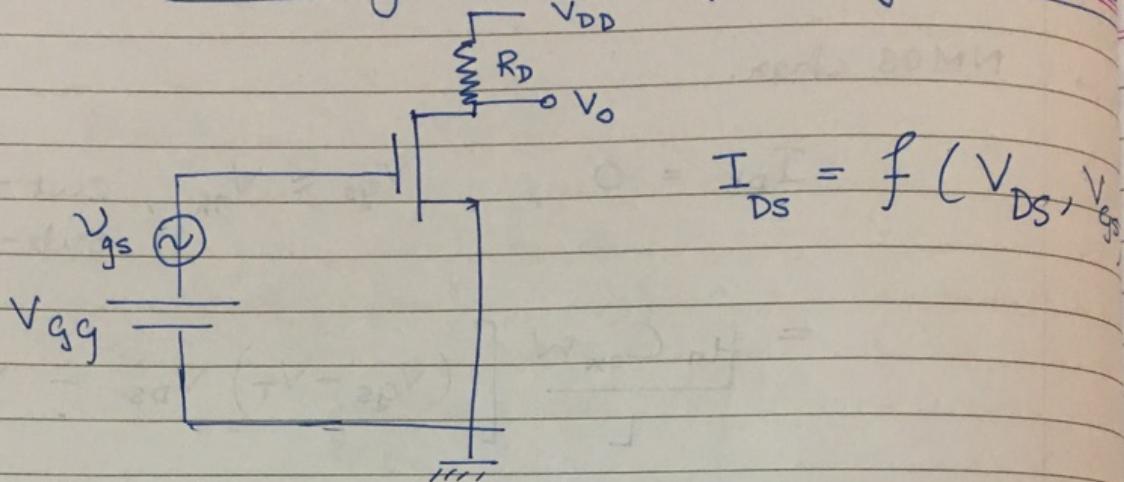
$$= \frac{H_n C_{ox} W}{2L} (V_{GS} - V_T)^2 ; V_{DS} \geq V_{GS} - V_T$$

[satn]

→ Char.



Small Signal AC equiv. of MOSFET



$$I_{DS} = f(V_{DS}, V_{GS})$$

Notation:

DC values $\rightarrow V_{GS}, V_{DS}$

AC " $\rightarrow v_{GS}, v_{DS}$

$$\begin{aligned} \text{DC + AC} \rightarrow V_{GS} &= V_{GS} + v_{GS} \\ &= V_{DS} + v_{DS} \end{aligned}$$

- For all Analog app. MOS is operated in saturation region.

$$k' = \frac{H_n C_{ox} W}{L}$$

In saturation:

$$I_D = \frac{k'}{2} (V_{GS} - V_T)^2 \quad [\text{Large signal char.}]$$

$$v_{GS} = V_{GS} + v_{GS}$$

$$I_{DS} + i_{ds} = \frac{k'}{2} (V_{GS} + v_{GS} - V_T)^2$$

$$= \frac{k'}{2} \left[(V_{GS} - V_T)^2 + 2(V_{GS} - V_T)V_{GS} + V_{GS}^2 \right]$$

I_{DS}

$$\dot{I}_{DS} = k'(V_{GS} - V_T)V_{GS} + \frac{k'}{2}V_{GS}^2$$

$$\dot{I}_{DS} \approx k'(V_{GS} - V_T)V_{GS} \quad (V_{GS} - V_T \geq V_{GS})$$

$$\dot{I}_{DS} = g_m V_{GS}$$

Transconductance

$$g_m = k'(V_{GS} - V_T)$$

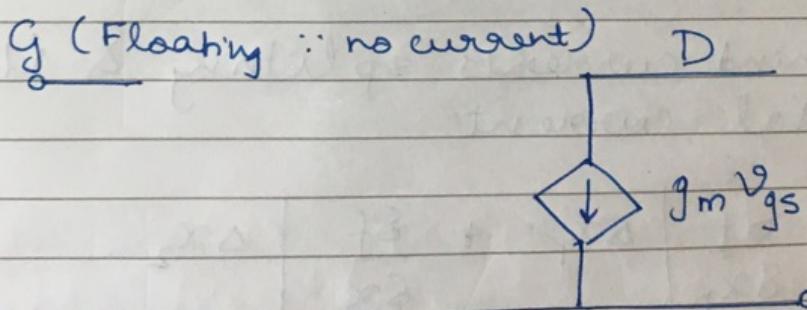
} small signal
 } Approx.

→ Alternatively, using Derivation

$$I_{DS} = \frac{k'}{2} (V_{GS} - V_T)^2$$

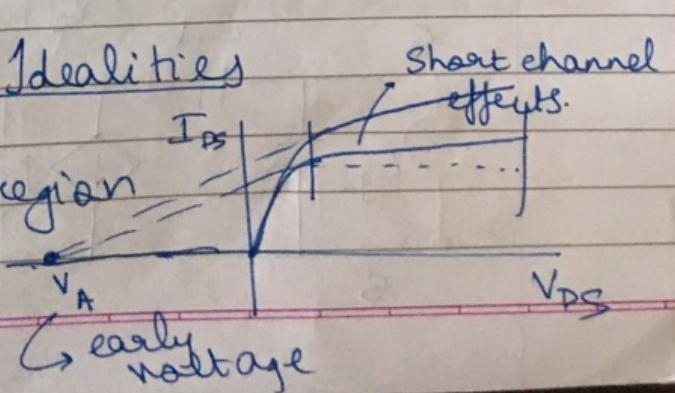
$$g_m = \frac{8I_{DS}}{8V_{GS}} = k'(V_{GS} - V_T)$$

Model



→ Incorporating Non-Idealities

- Slope in saturation region



$$I_{DS} = \frac{k'}{2} (V_{GS} - V_T)^2 [1 + \lambda (V_{DS} - V_{DS,sat})]$$

→ $V_{DS,sat} = V_{GS} - V_T$

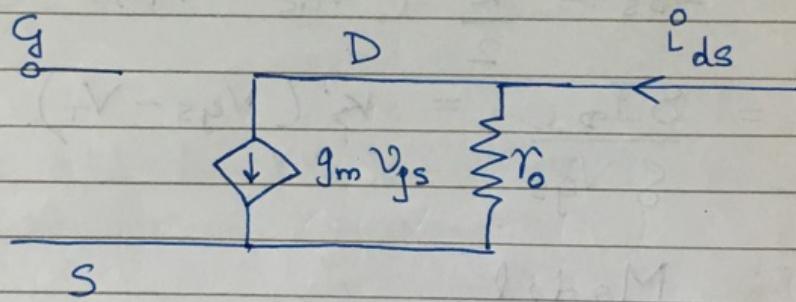
$$\frac{\delta I_{DS}}{\delta V_{DS}} = \frac{k'}{2} (V_{GS} - V_T)^2 \lambda$$

$\approx \lambda \boxed{I_{DS}}$

↓ Approximation

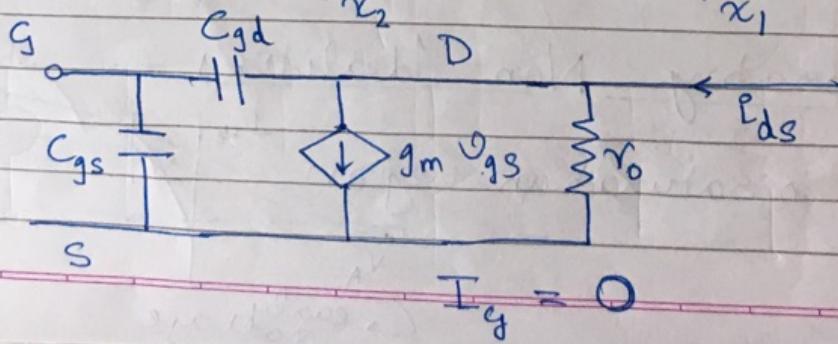
Ignoring λ for DC Comp.

$$\frac{\delta V_{DS}}{\delta I_{DS}} = \frac{1}{\lambda I_{DS}} = r_o = \boxed{\frac{V_A}{I_{DS}}} \quad \text{Early voltage}$$



→ Idea behind currents splitting & I_{DS} being total current

$$\Delta Y = \frac{\delta f}{\delta x_1} \left| \frac{\Delta x_1}{x_2} \right| + \frac{\delta f}{\delta x_2} \left| \frac{\Delta x_2}{x_1} \right|$$



→ Results to rem

In satn.

$$C_{gs} = \frac{2}{3} C_{ox} \quad (\text{Due to channel bent towards Source})$$

$$C_{gd} = 0$$

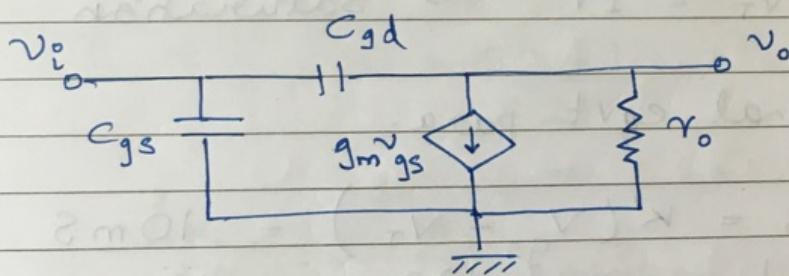
Linear

$$C_{gs} = C_{gd} = \frac{C_{ox}}{2} \quad (\text{Due to Symmetric channel})$$

9.1.18

Recap

Small Signal AC eqvt CRT



$$g_m = k' (v_{gs} - V_T)$$

$$r_o = \frac{1}{\lambda I_D}$$

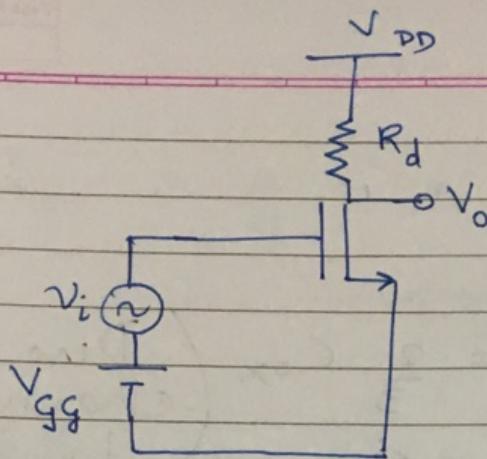
$$C_{gs} = \frac{2}{3} C_{ox} \times WL$$

$$C_{gd} \approx 0$$

Typically $C_{gd} < 0.1 C_{gs}$

C_{gd} at high $\omega \rightarrow$ Approaches short
 \Rightarrow Can't act like an amplifier

→ Low $\omega \Rightarrow$ Can be assumed to be open CKT.



Typical values

$$V_{DD} = 15V$$

$$V_T = 1V$$

$$K' = 10 \text{ mA/V}^2$$

$$R_d = 1k\Omega$$

$$V_{GG} = +2V$$

$$\gamma = 0.01 \text{ V}^{-1}$$

$$I_D = \frac{K'}{2} (V_{GS} - V_T)^2 = 5 \text{ mA}$$

$$V_o = 15V - 5 \text{ mA} \times 1k\Omega = 10V = V_{DS}$$

→ For saturation: $V_{DS} > V_{GS} - V_T$

$$V_{DS} = 10V$$

$$V_{GS} - V_T = 1V \Rightarrow \text{Saturation}$$

→ Small Signal eqvt para.

$$g_m = K' (V_{GS} - V_T) = 10 \text{ mS}$$

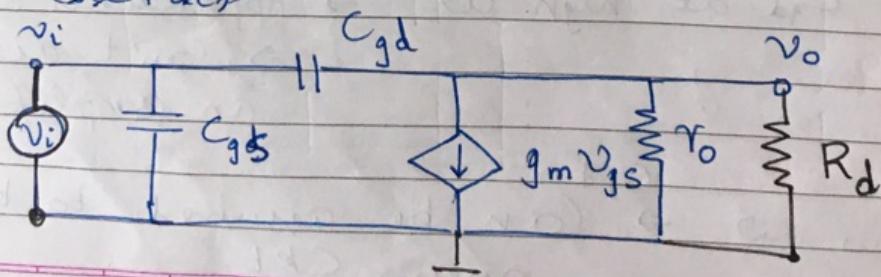
$$r_o = \frac{1}{\gamma I_D} = \frac{100}{5} = 20k\Omega$$

$$C_{GS} \approx 0 \text{ (for now)}$$

$$C_{GD} \approx 0$$

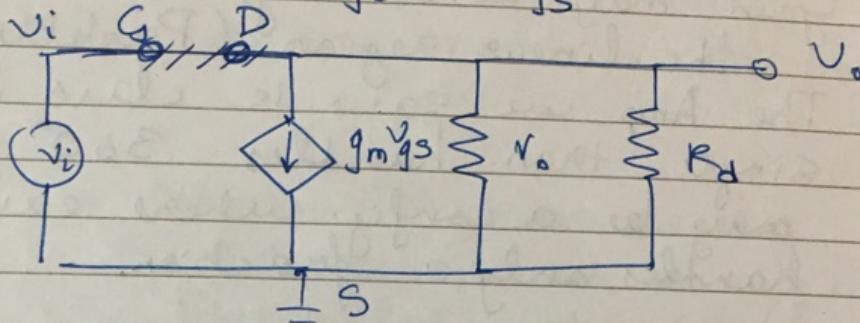
→ Combined Equivalent Ckt

→ Combined Small signal Acqvt ckt.
(See back)



- V_{DD} & V_{gg} are shorted since DC values
- F_E are set to 0 in AC equivalent circuit

- Open Ckt thru C_{gd} & C_{gs}



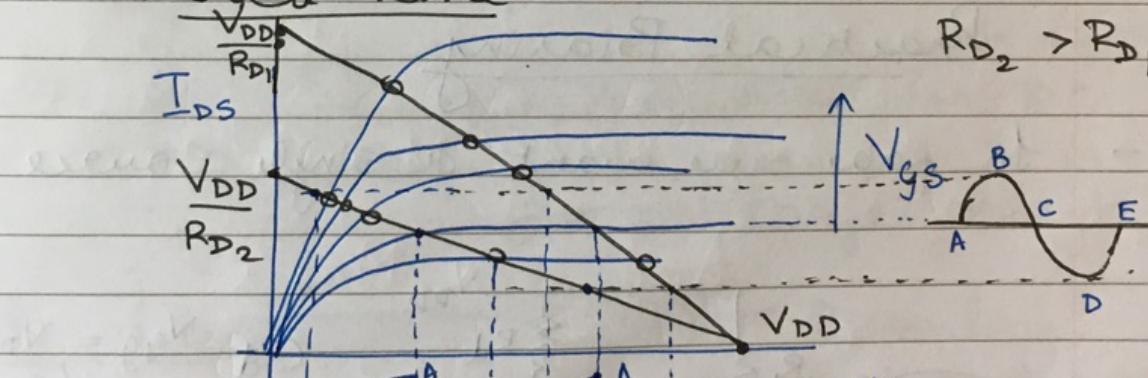
$$V_o = -g_m V_{gs} (r_o \parallel R_d)$$

$$= -10ms \times$$

$$A_v = -\frac{V_o}{V_i} = 10ms \times \left(\frac{r_o R_d}{r_o + R_d} \right)$$

$$A_v = 10 \times \frac{20 \times 1}{21} = \frac{200}{21} \approx 9.5$$

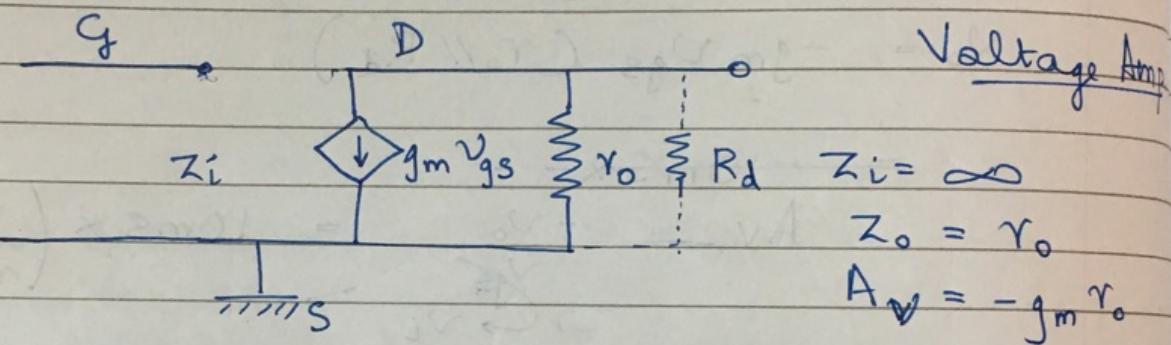
Load Line



$$V_o = V_{DS} = V_{DD} - I_{DS} R_D$$

$I_{DS} = 0, \quad V_{DS} = V_{DD}$ } Load line.
 $V_{DS} = 0, \quad I_{DS} = \frac{V_{DD}}{R_D}$

- With a larger value of R_D we get a larger gain.
- Gain may be so large that it hits the linear region (Problem)
- The Amp. we saw is class A, i.e. a single tran. handles 360° phase. There may be a config. where each tran. handles only a fraction.

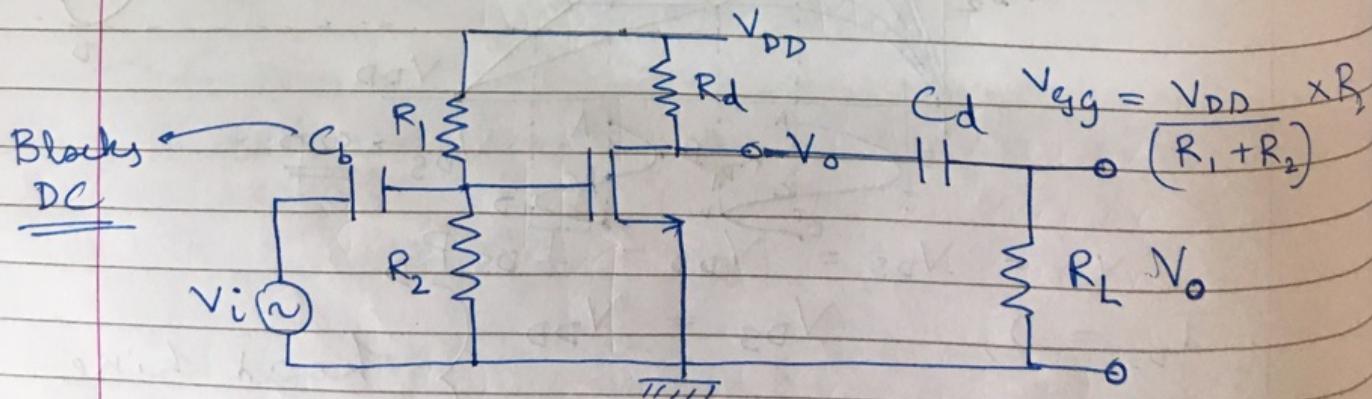


- Ideal voltage Amp.

$$Z_i = \infty, Z_o = 0$$

Practical Biasing

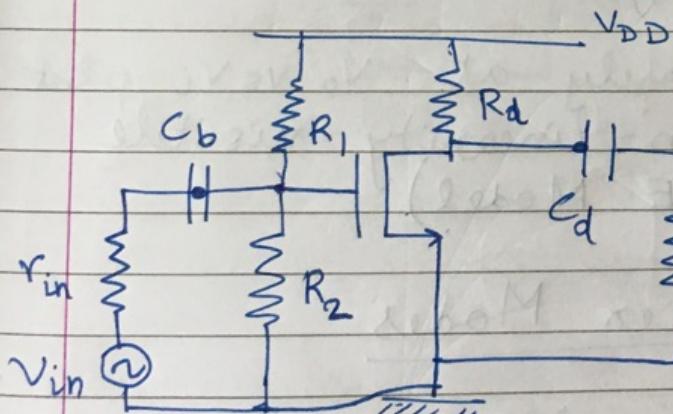
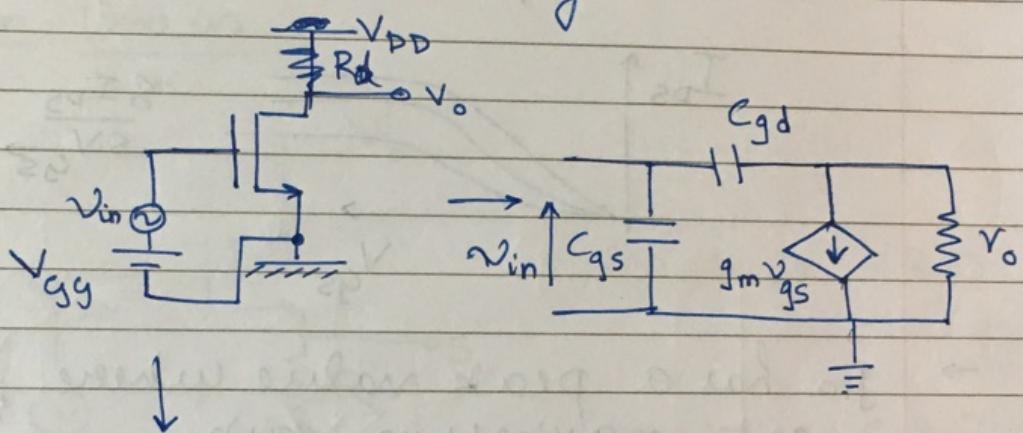
- In pract. we want a single source



- As the earlier ckt stands, R_d controls both Gain & Biasing
- \Rightarrow Add R_L , and a capacitor to block DC.

→ C_b = Blocks DC from mixing with AC or vice versa
 C_d = "Decouples" the o/p from DC biasing

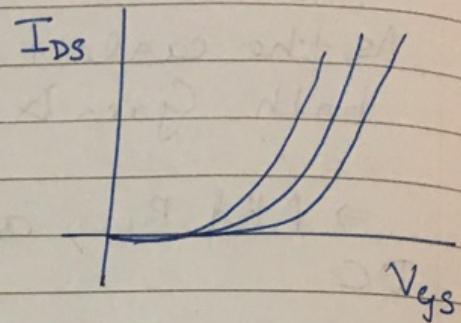
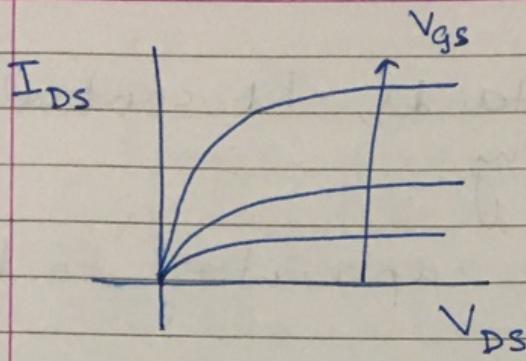
11.1.18



C_b is short for AC & open for DC provided it is large enough
 $C_d \rightarrow$ Decoupling transistor output from final o/p

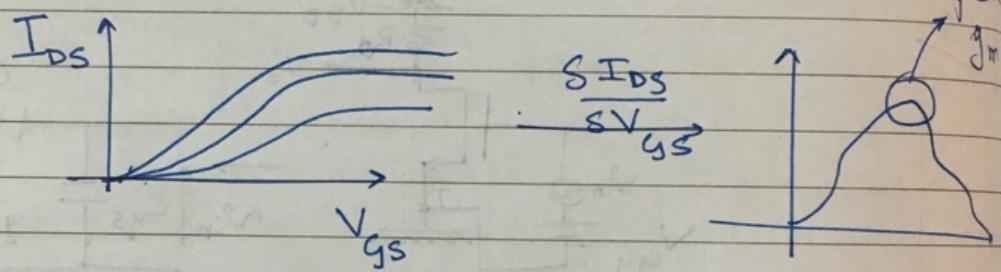
$$A_v = -g_m (\gamma_o \parallel R_d)$$

$$A_v = \frac{S V_o}{S V_{in}}$$



Mobility Degradation: $k' = \frac{1}{L} C_{ox} W$

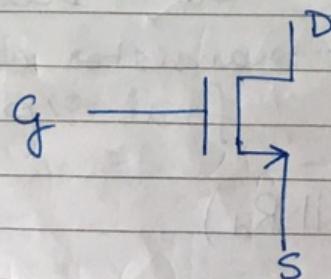
→ In reality in I_D V_{gs} plot current flattens out



→ g_m has a peak value where we can get maximum gain

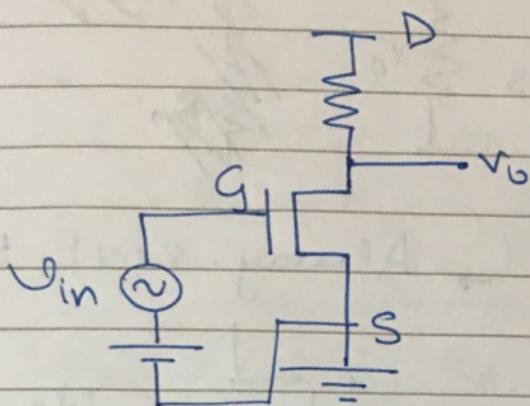
→ When we look closely at V_o vs V_i plot there is some non-linearity visible (check SPICE Model)

Amplifier Modes



- Common Source
- Common Drain
- Common Gate

→ Till Now: Common Source (CS Amp.)



CS

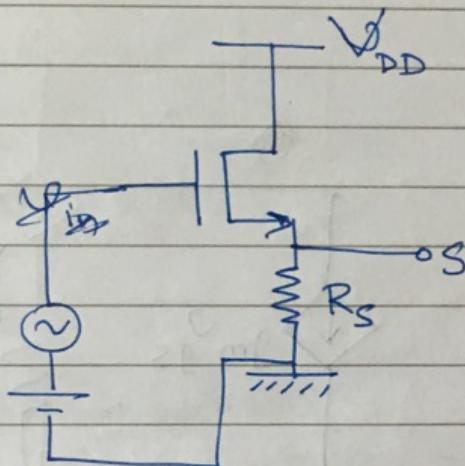
$$A_v = -g_m (R_d // r_o)$$

$$Z_o = r_o \left[R_o // R_d \right]$$

if c_d
is comn

$$Z_i = \infty$$

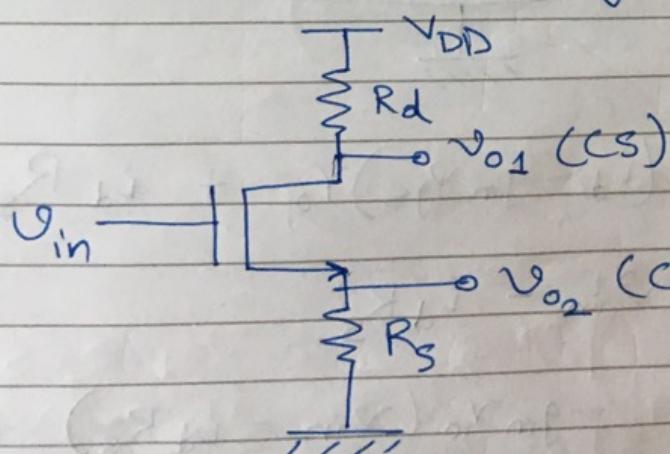
→ Common Drain



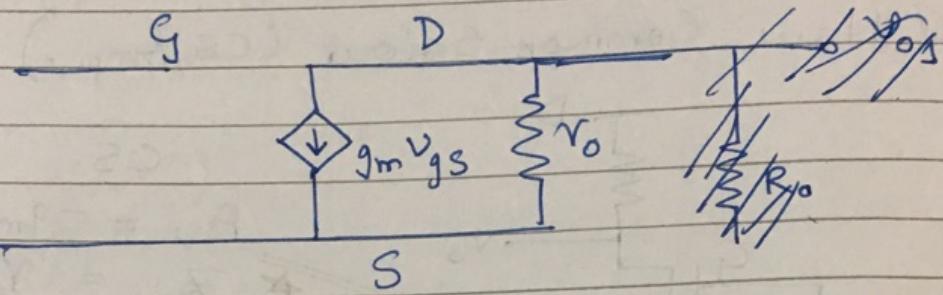
O/P b/w S & D
L(Gnd)

I/P b/w G & D
L(Gnd)

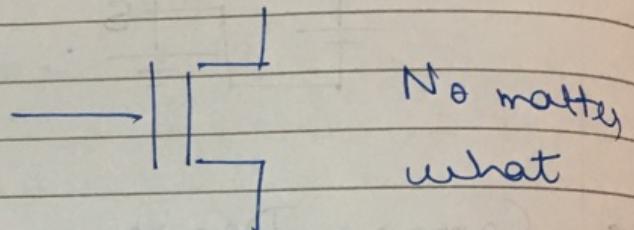
↓ Modify



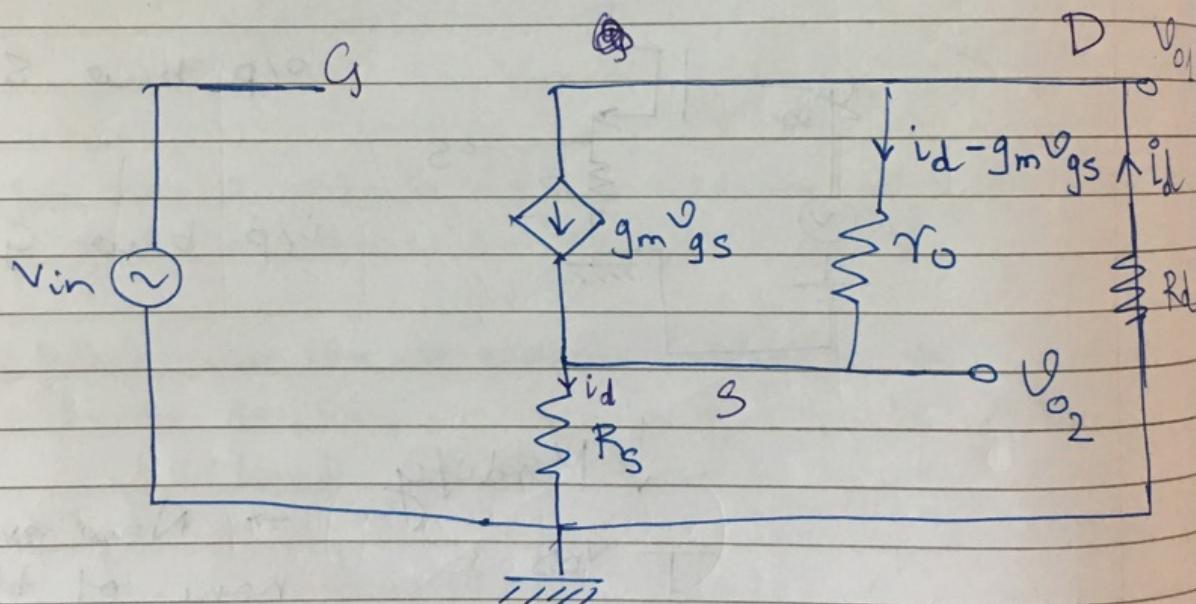
→ Now even though none of the terms CS/CD is strictly valid, we still use them due to similarity in form.



→ The above is the \hookrightarrow Always true to
universally true
small signal model



→ Here



$$i_d R_d + (i_d - g_m v_{gs}) r_o + i_d R_s = 0$$

$$v_{gs} = v_{in} - i_d R_s$$

$$i_d(R_d + r_o) - g_m r_o (v_{in} - i_d R_s) + i_d R_s = 0$$

$$i_d (R_d + r_o + g_m r_o R_S + R_S) = g_m r_o V_i$$

$$i_d = \frac{g_m r_o V_i}{r_o + R_d + (g_m r_o + 1) R_S}$$

$$V_{o_1} = -i_d R_d$$

$$V_{o_2} = i_d R_S$$

$$A_{CS} = \frac{V_{o_1}}{V_i} = \frac{-R_d g_m r_o}{[r_o + R_d + (1 + g_m r_o) R_S]}$$

$$A_{CD} = \frac{V_{o_2}}{V_i} = \frac{R_S g_m r_o}{[r_o + R_d + (1 + g_m r_o) R_S]}$$

Approximations;

$$\textcircled{1} \quad g_m r_o \gg 1 \quad (\text{Gain} \gg 1)$$

$$A_{CS} \approx \frac{-R_d g_m r_o}{r_o + R_d + g_m r_o R_S}$$

$$\textcircled{2} \quad r_o \gg R_d \quad \left[\begin{array}{l} \text{We also said this before} \\ A = -g_m (r_o \parallel R_d) \approx \\ -g_m R_d \end{array} \right]$$

$\hookrightarrow \lambda \ll M \ll r_o$ (λ is small from short channel effect)

$$A_{CS} \approx \frac{-R_d g_m r_o}{r_o + g_m r_o R_S}$$

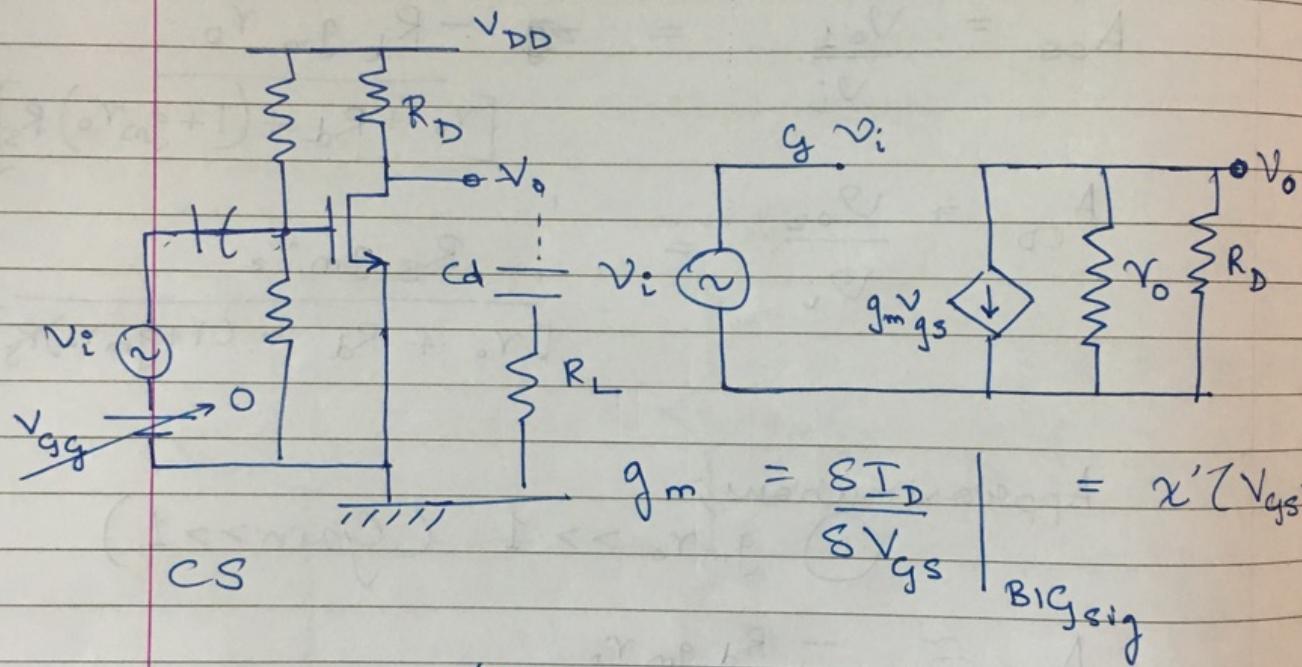
(3)

$$g_m R_s \gg 1$$

$$\Rightarrow \approx -\frac{g_m R_D V_o}{g_m V_o R_S} \approx -\frac{R_D}{R_S}$$

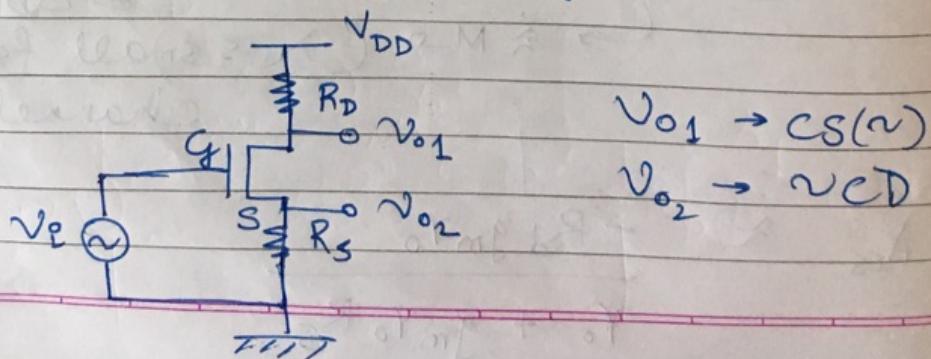
15.1.18

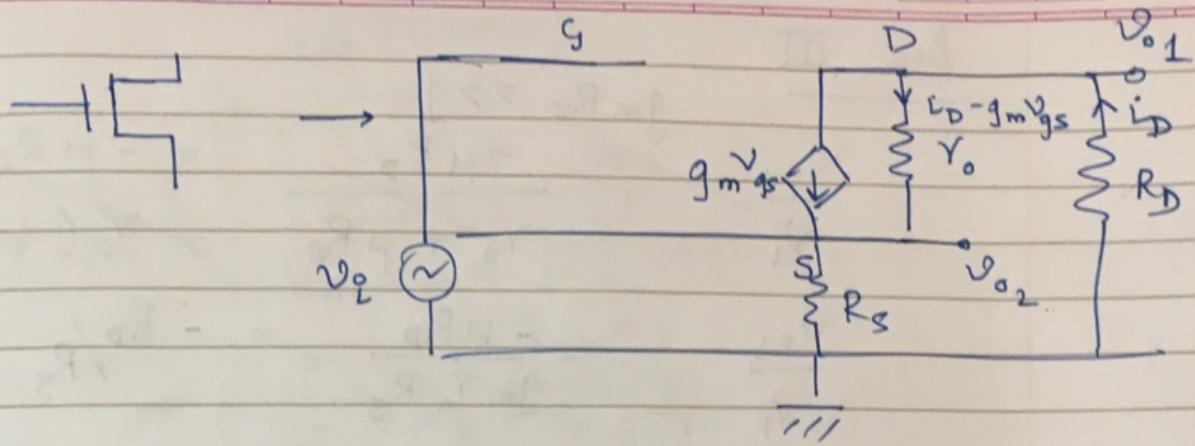
Transistor at Low frequency



$$g_m R_o = \mu \quad (\text{Gain Def. when no } R_L)$$

→ Model for low frequency





$$i_D R_D + r_o (i_D - g_m v_{gs}) + i_D R_S = 0$$

$$v_i = v_{gs} + i_D R_S$$

$$i_D R_D + r_o (i_D - g_m (v_i - i_D R_S)) + i_D R_S = 0$$

$$i_D (R_D + r_o + R_S) \times (g_m r_o + 1) = r_o g_m v_i$$

$$i_D = \frac{g_m r_o v_i}{R_D + r_o + R_S (1 + g_m r_o)} = \frac{H v_i}{R_D + r_o + R_S (1 + H)}$$

$$v_{o1} = -i_D R_D, \quad \frac{v_{o1}}{v_i} = \frac{-H R_D}{R_D + r_o + R_S (1 + H)} = -1.587$$

Assumptions

(I) Say $H = 50$ (typical)
 $H \gg 1$

$$\frac{v_{o1}}{v_i} = \frac{-H R_D}{R_D + r_o + H R_S} = -1.612$$

(II) $\frac{v_{o2}}{v_i} \neq \frac{v_{o1}}{v_i}$ $r_o \gg R_D$

$$\frac{v_{o1}}{v_i} = \frac{-H R_D}{r_o + H R_S} = \frac{-H R_D}{r_o (1 + g_m R_S)} = -1.66$$

Typical
~~(ignoring)~~
 $H = 50$

$r_o = 50 \text{ k}\Omega$
 $g_m = 1 \text{ mS}$
 $R_D = 10 \text{ k}\Omega$
 $R_S = 5 \text{ k}\Omega$

Ass III

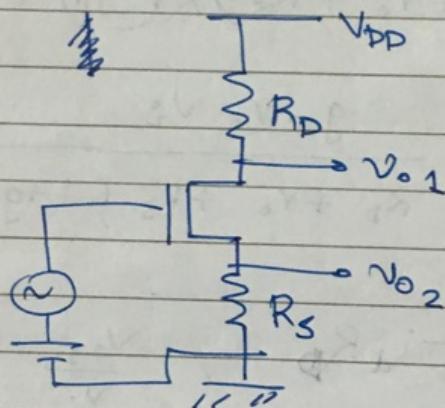
$$\frac{V_{o_1}}{V_i} = \frac{g_m R_s >> 1}{\frac{-H R_D}{V_o + H R_S}} = \frac{-H R_D}{V_o (1 + g_m R_S)} = -H R_D / R_S = -2$$

$$\frac{V_{o_1}}{V_i} = \frac{-H R_D}{g_m V_o R_S} = -R_D / R_S = -2$$

→ Common Drain

$$V_{o_2} = i_d R_S$$

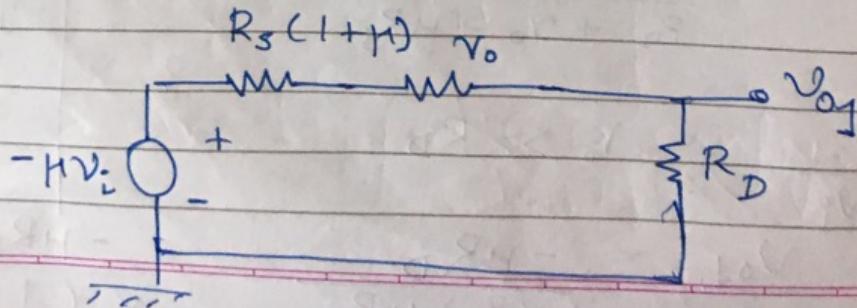
$$\frac{V_{o_2}}{V_{in}} = \frac{H \cdot R_S}{R_D + r_o + R_S(1+H)}$$

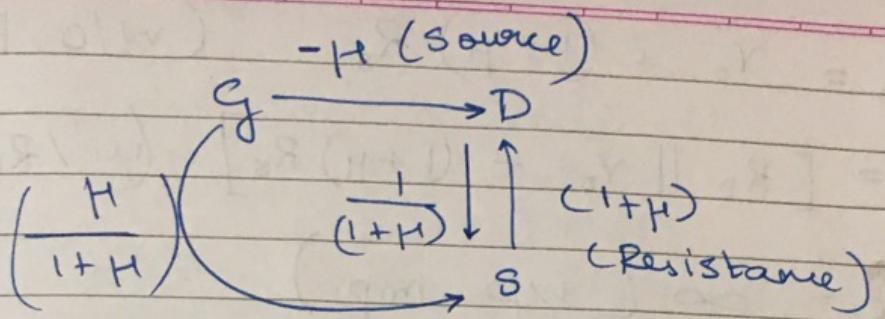


→ Rewriting earlier exp.

$$V_{o_1} = - \frac{H V_i R_D}{R_D + r_o + R_S(1+H)}$$

→ Eqvt CKT

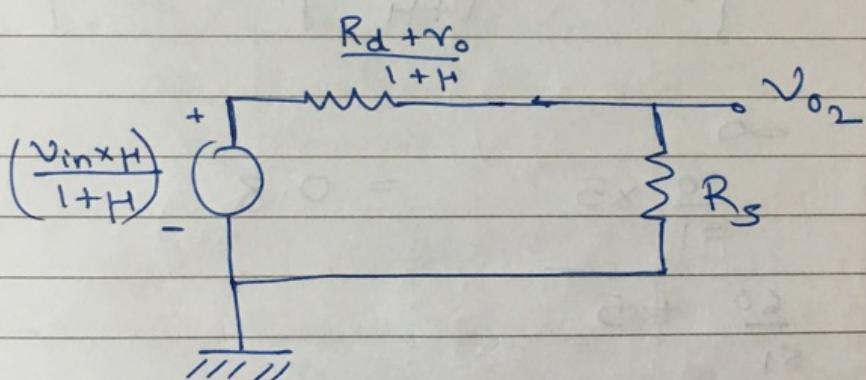




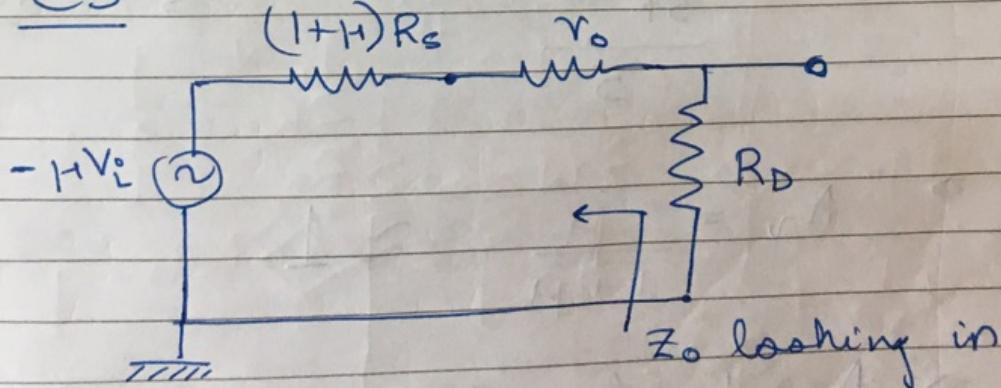
$$\frac{V_{o2}}{V_{in}} = \frac{HR_S}{R_D + r_o + R_S(1+H)}$$

$$V_{o2} = \frac{V_{in} \times \left(\frac{H}{1+H}\right) R_S}{\frac{R_D + r_o}{1+H} + R_S}$$

Eqv + Ckt



CS



$$A_v = \frac{V_{o1}}{V_i} = \frac{-HR_D}{r_o + R_D + (1+H)R_S}$$

$$Z_o = r_o + (1 + H) R_S \quad (\text{w/o } R_D \text{ i.e. } R_D = \infty)$$

$$Z_i = \infty \quad (\text{i/p imp.})$$

$$A_v = \frac{V_{o2}}{V_1} = \left(\frac{H}{1+H} \right) R_S$$

) $\sim \sqrt{gg}$

$$Z_o = \frac{R_D + r_o}{1+H} [\text{w/o } R_S]$$

$$= \left[R_S \parallel \frac{R_D + r_o}{1+H} \right] [\omega / R_S]$$

$$Z_i = \infty$$

$$\rightarrow A_v = \frac{\frac{50}{51} \times 5}{\frac{60}{51} + 5} = 0.8$$

2) \downarrow

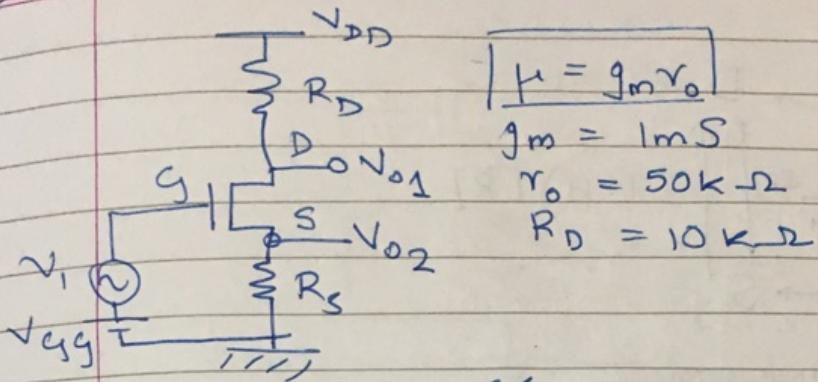
$$\approx \frac{\frac{50}{51} \times 5}{5} \quad \text{i.e. } R_S \gg \frac{R_D + r_o}{1+H}$$

$$\Rightarrow A_v = \frac{H}{1+H} \approx 1$$

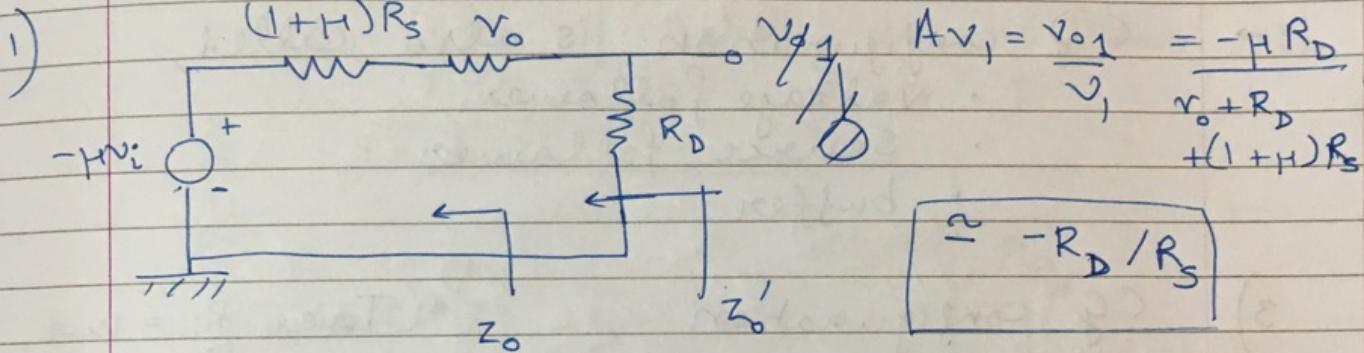
$A_v = 1$ for CD config. (No Gain)

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Page No.:



CS



$$Z_o = r_o + (1+H)R_S$$

$$Z_o = R_D \parallel Z_o$$

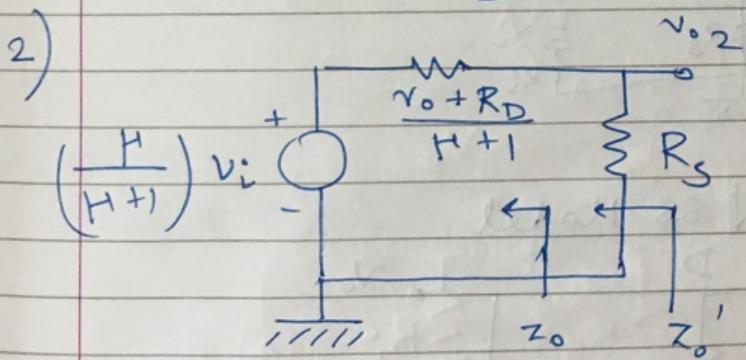
$$Z_i = \infty$$

$$A v_1 = -1.6 \rightarrow -2$$

$$Z_o = ? \rightarrow 305 \text{ k}\Omega$$

$$Z'_o = ? \approx 10 \text{ k}\Omega$$

CD



We get

$$A v_2 = 0.8 \dots 1.0$$

$$Z_o = ? \approx \frac{60}{51} \text{ k}\Omega = 1.2 \text{ k}\Omega$$

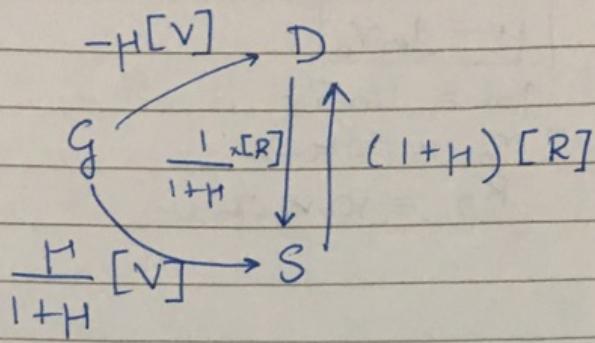
$$Z'_o = ? \approx 1.2 \text{ k}\Omega$$

$$A v_2 = \frac{V_{o2}}{V_i} = \frac{\left(\frac{H}{H+1}\right) R_S}{\frac{r_o + R_D}{H+1} + R_S} \approx 1$$

$$Z_o = r_o + R_D$$

$$Z'_o = R_S \parallel Z_o$$

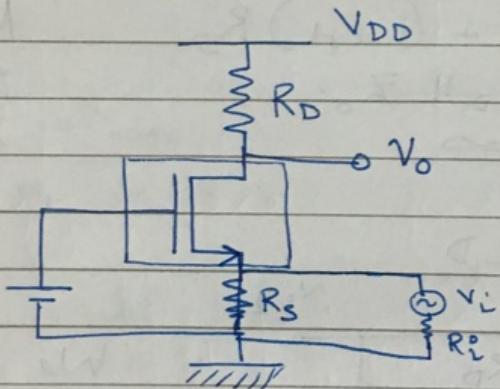
$$Z_i = \infty$$



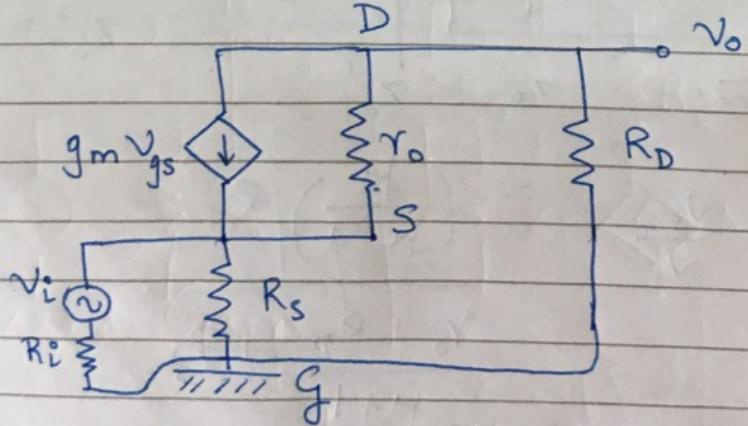
- CD configuration is also called,
- voltage follower
 - source follower
 - buffer

3) CG configuration

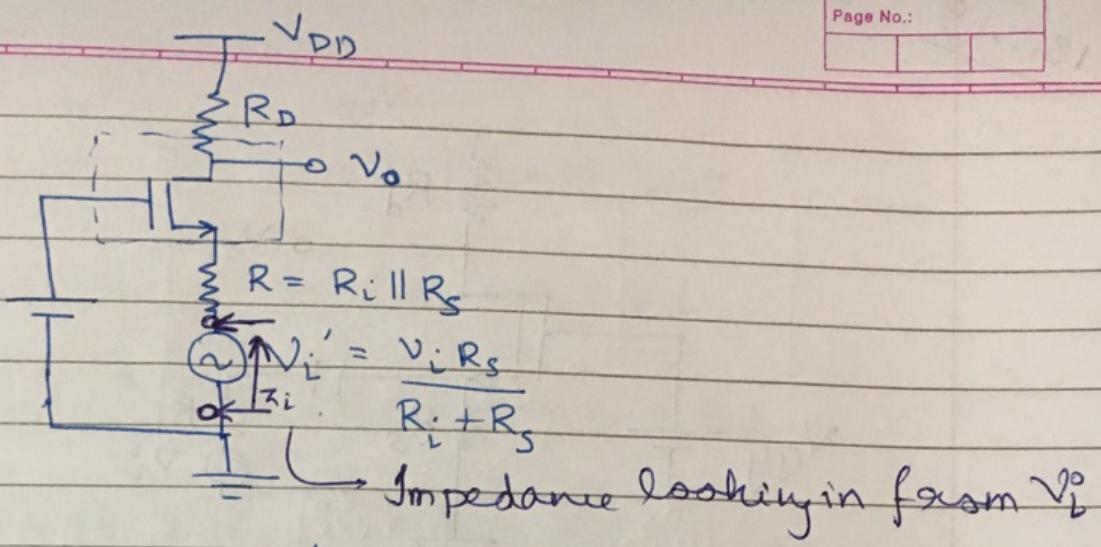
(Take $R_i = 1\text{k}\Omega$, gives us control over Z_i)



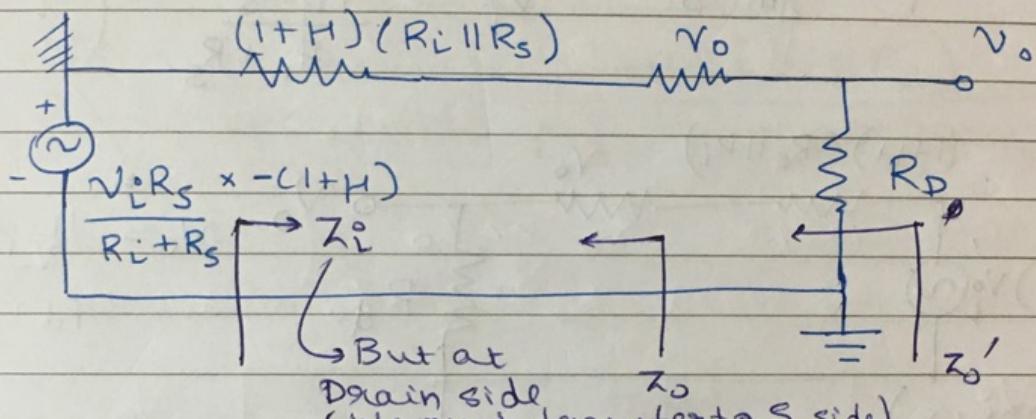
↓ SS Model



Appr



Taking all to Drain side



$$A_v = \frac{V_o}{V_i} = - (1+H) \frac{R_s \times R_D}{R_i + R_s}$$

$$(1+H)(R_i \parallel R_s) + r_o + R_D$$

Approximations

$R_i \approx 0$, (Source resistance is important)

$$A_v \approx \frac{-R_D \times g_m r_o}{r_o} = [-g_m R_D]$$

$$Z_o = r_o + (1+H)(R_i \parallel R_s)$$

$$Z_i = R_D + r_o + (1+H) \frac{(R_i \parallel R_s)}{(R_i \parallel R_s)}$$

$$Z_o' = Z_o \parallel R_D$$

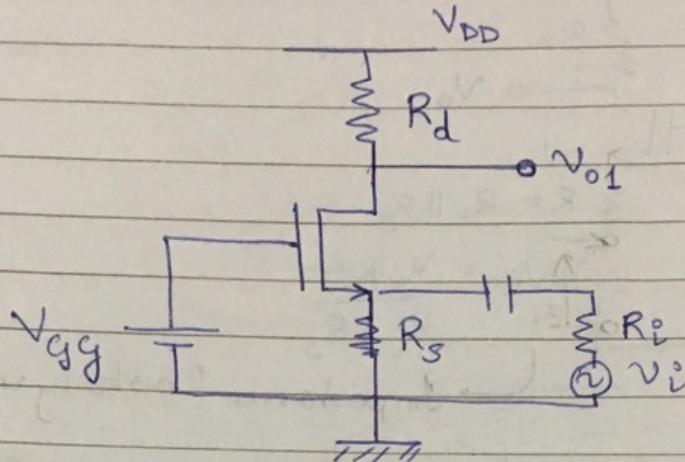
$$(1+H)$$

$$A_v = \frac{-4.1}{0.4/10} \quad \text{(We need } R_i \text{ to be even smaller for approx)}$$

$$= \frac{R_D + r_o + (R_i \parallel R_s)}{(1+H)}$$

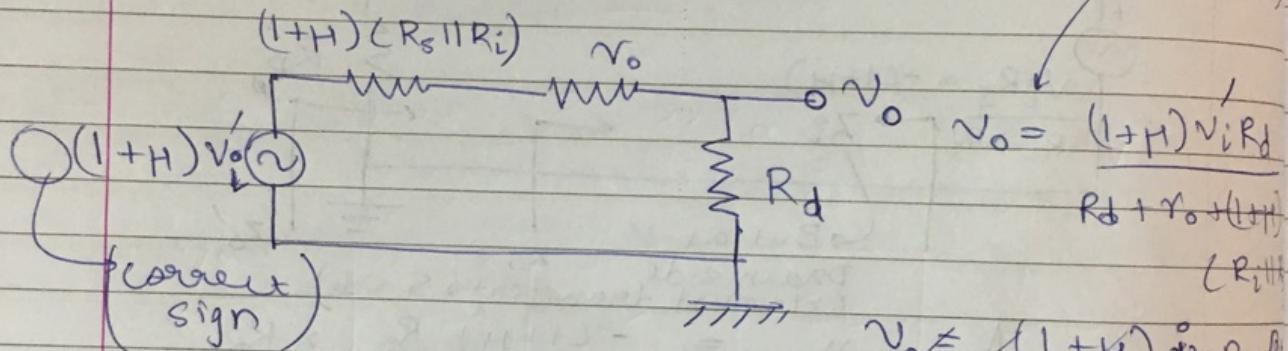
18.1.18

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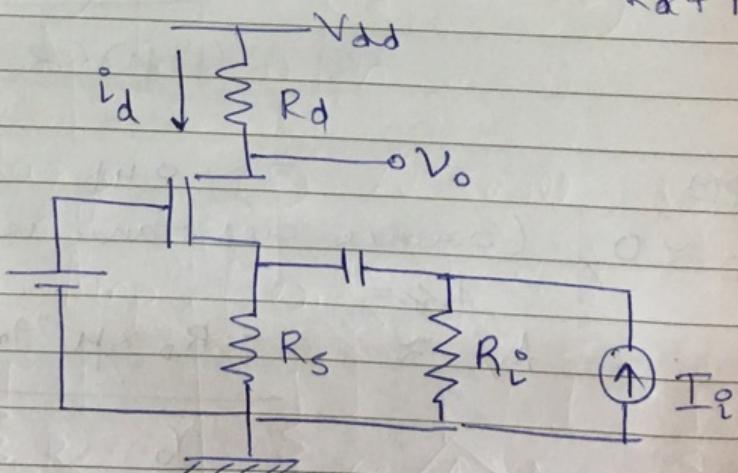
$$H = g_m r_o$$

$$V'_i = \frac{V_i R_s}{R_i + R_s} \quad \left(I'_i = \frac{V'_i}{R_i} \right)$$



→ Current Source

$$V_o = \frac{(1+H) V'_i R_d}{R_d + r_o + (1+H) (R_i || R_s)}$$



Current Gain

$$V_o = -i_d R_d$$

$$-i_d = \frac{v_o}{R_d} \quad \cancel{(1+H) i_i}$$

$$N_o = \frac{(1+H) i_i (R_i || R_s) R_d}{(1+H) (R_i || R_s) + r_o + R_d}$$

$$-i_d = \frac{v_o}{R_d} = \frac{(1+H) i_i (R_i || R_s)}{(1+H) (R_i || R_s) + r_o + R_d}$$

$$A_i = \frac{i_d}{i_i} = -\frac{(1+H) (R_i || R_s)}{(1+H) (R_i || R_s) + r_o + R_d}$$

→ Notes: $|A_i| \leq 1$

$$\rightarrow A_i = -\frac{R_i || R_s}{R_i || R_s + (r_o + R_d)} \approx -1$$

→ Summary Gate current is 0
(Dielectric)

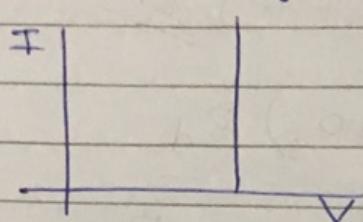
A_v	r_s	CD	CG
High	High	unity	High
A_i	(—)	unity (Best Gain)	

Z_L High High Low

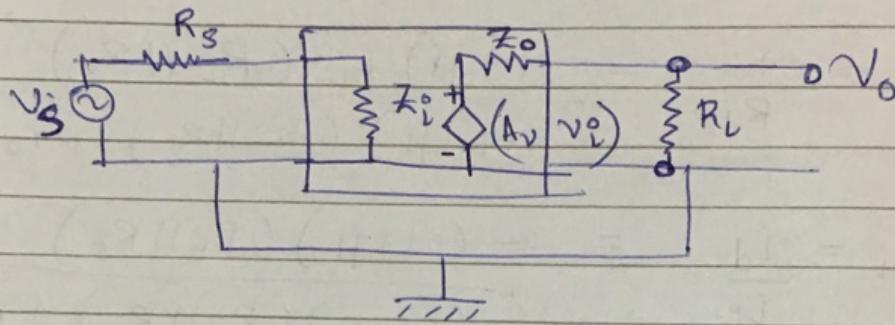
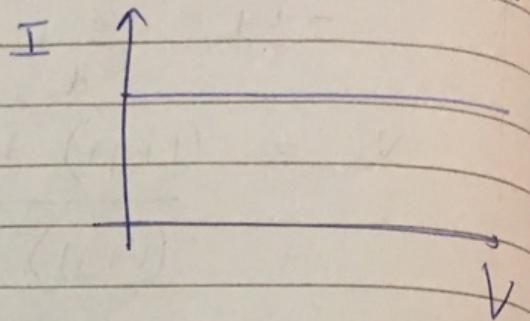
Z_o High Low High

→ Note: High means r_o or H term.

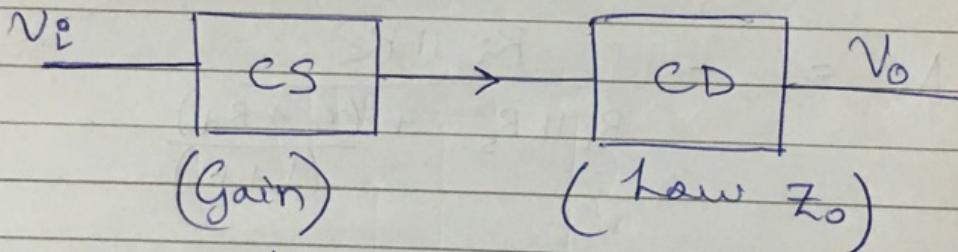
→ Ideal Voltage source



Ideal current



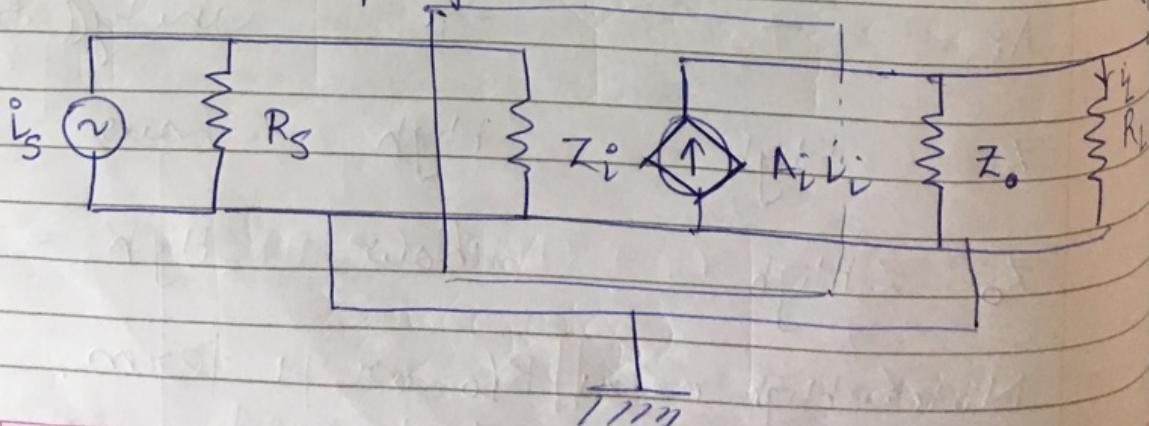
→ We want $R_L \gg Z_0$ ($V_o = A_v V_i \times \frac{R_L}{R_L + Z_0}$)
Hence we want a small Z_0



→ Voltage Amplification

- ① A_v large
- ② Z_0 small
- ③ Z_i low

→ Current Amplifier

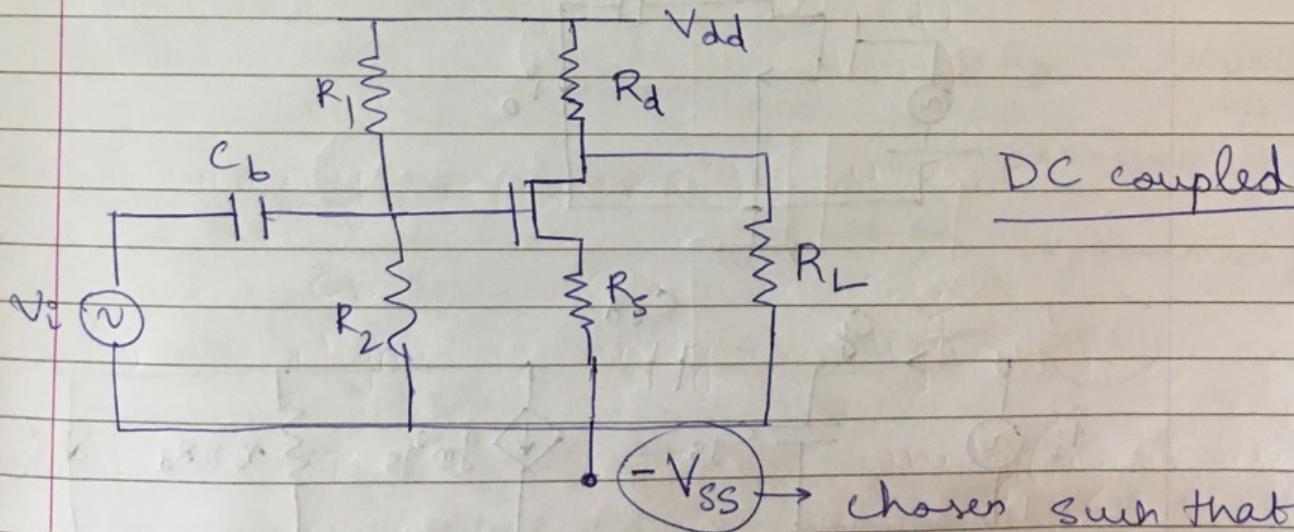
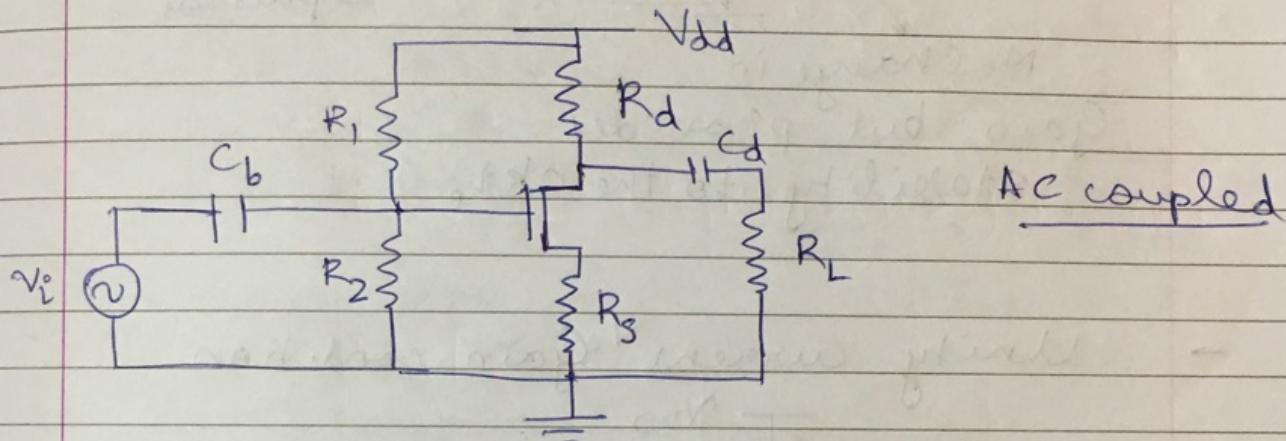


Current Amp.

- ① A_i Large ② Z_o large ③ Z_i small

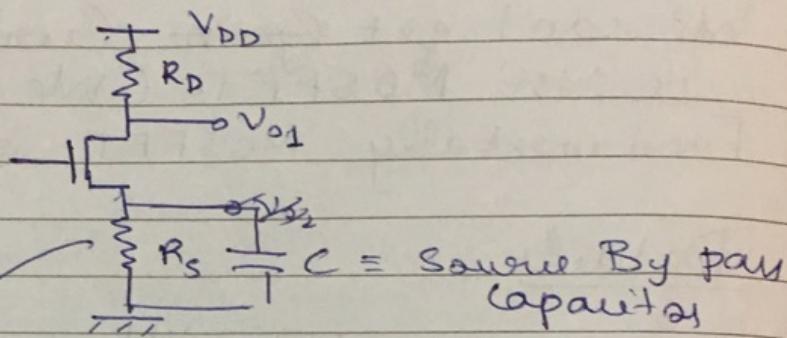
→ We can't get Current Gain with MOSFETs (We use BJTs)
 Fundamentally MOSFET is V_{CCS}

→ Details



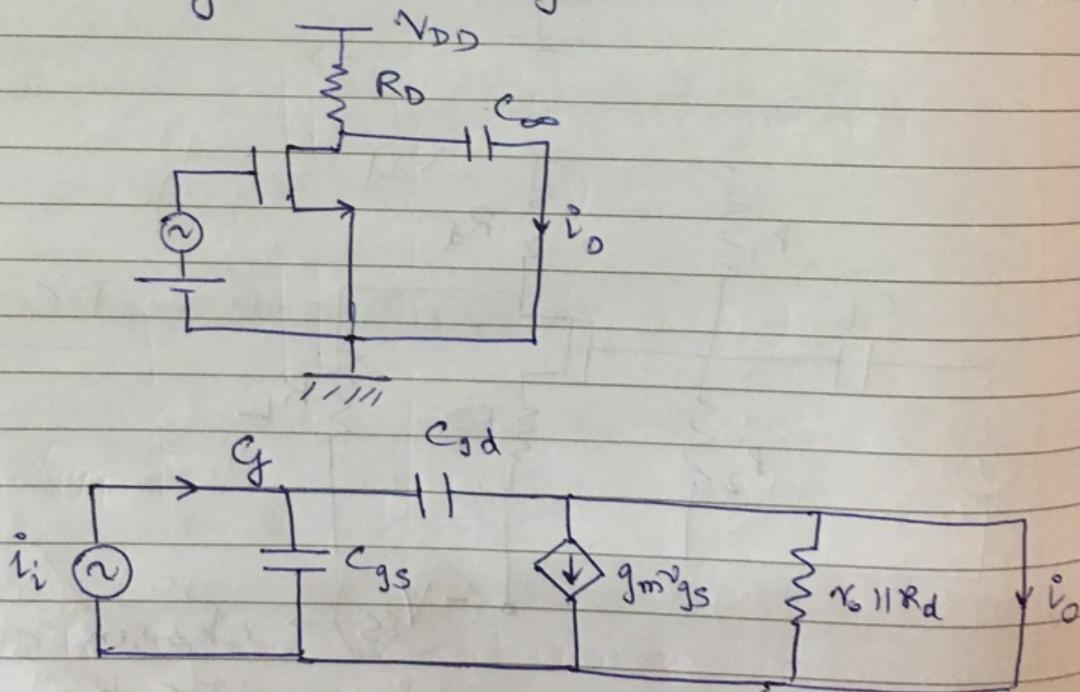
$-V_{ss}$ chosen such that
 DC bias at
 drain is 0

→ Source Bypass capacitor



No Change in Gain, but provides stability to the ckt.

→ Unity current Gain condition



$$f_T = \frac{g_m}{2\pi(C_{gs} + C_{gd})}$$

H.W

→ Choosing value of R_d

- Find value of R_d that maximizes the gain.
- Drawbacks of large R_D ?
- Soln. Active load, low pot. across terminals

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$$k' = 1 \text{ mA/V}^2$$

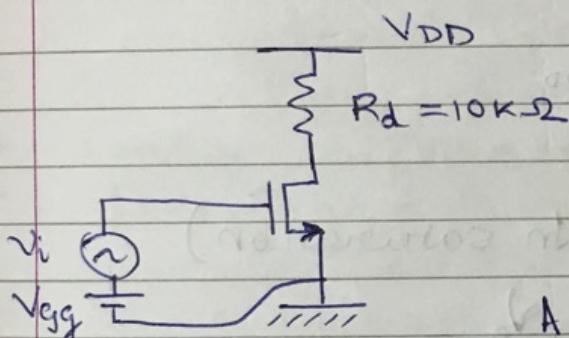
$$V_T = 1 \text{ V}$$

$$\lambda = 0.01 \text{ V}^{-1}$$

$$I_{DS} = 2 \text{ mA}$$

$$g_m = \frac{\sqrt{2k' T}}{2mS} = 2 \text{ mS}$$

$$r_o = \frac{1}{\lambda I_{DS}} = \frac{100}{2} = 50 \text{ k}\Omega$$



$$H = g_m r_o = 100$$

$$A_v \Big|_{R_D = 10 \text{ k}\Omega} = -\frac{HR_D}{60} = -\frac{100 \times 10}{60} \xrightarrow{10 \text{ k}\Omega + 50 \text{ k}\Omega} -16.6$$

Using $(A_v = \frac{-HR_D}{R_D + r_o})$

$$A_v \Big|_{R_D = 50 \text{ k}\Omega} = -\frac{100 \times 50}{100} = -50$$

$\circled{-16.6}$ Improvement!

→ V_{DD} calculation

$$I_{DS} = \frac{k'}{2} (V_{GS} - V_T)^2$$

$$V_{GS} = \sqrt{\frac{2I_{DS}}{k'}} + V_T$$

$$= \sqrt{2 \times 2} + 1 = 3 \text{ V}$$

$$V_{DS} > V_{GS} - V_T$$

$$> 2V$$

$$V_{DS} = 2 + 1 = 3V \quad (\text{With Tolerance})$$

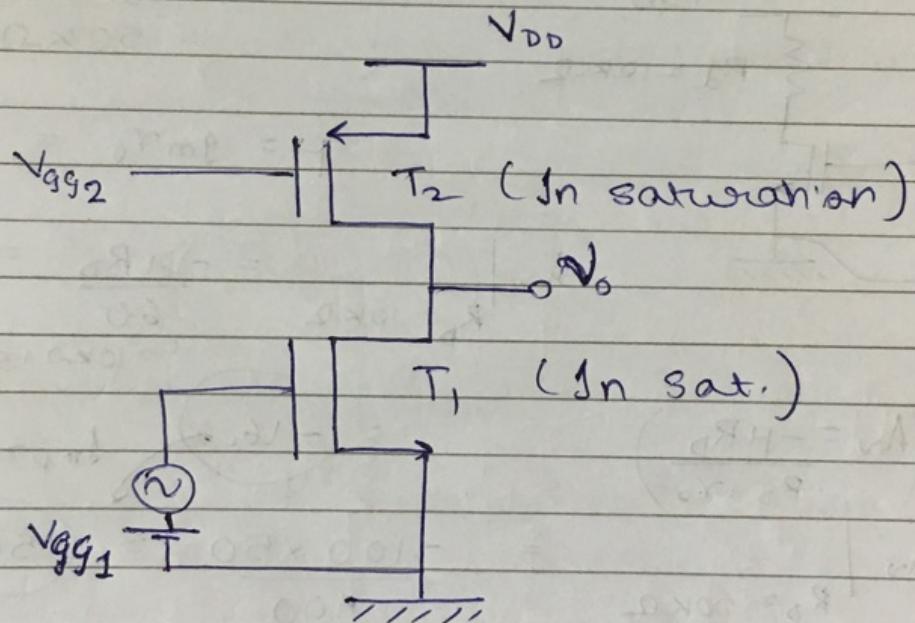
→ $R_D = 10k\Omega, \quad I_D R_D + 3 = V_{DD}$

$$= 2 \times 10 + 3$$

$$= 23V$$

→ $R_D = 50k\Omega, \quad \rightarrow 103V$

→ Solution: Use a PMOS



T_1, T_2 are in saturation

→ Parameters of T_1 remain the same

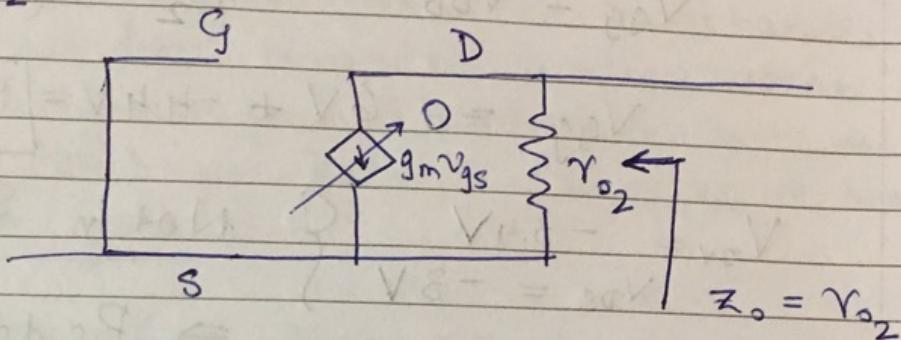
→ T_2

$$V_{T_2} = -1V$$

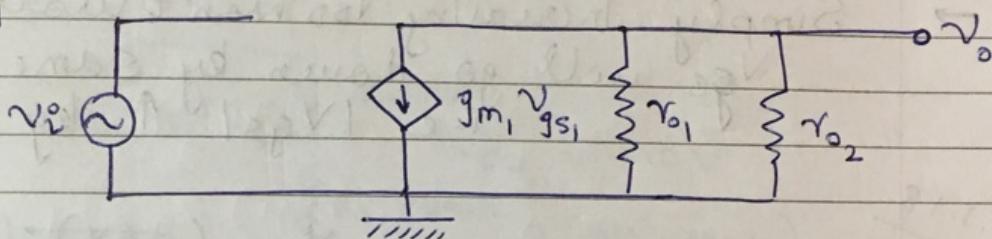
$$\lambda_2 = 0.01V^{-1}$$

$$K'_2 = \frac{1}{3} K'_1 = \frac{1}{3} \text{mA/V}^2$$

→ For T_2



→ For T_1



→ Looking In from a Node \Rightarrow Impedance b/w that node & Ground.

→ r_o2 is large, \Rightarrow Problem solved!

→ Find v_{gg2} , we know that $v_{ds} = 3V$,
Pick $v_{ds}/_{PMOS} = -3V \Rightarrow$ Required $v_{dd} = 6V$

→ Find v_{gg2} if $I_{ds} = 2mA$

$$I_{ds} = \frac{k_2'}{2} (v_{gs2} - v_t)^2$$

$$|I_{ds}| = 2mA = \frac{1}{3 \times 2} (v_{gs2} - v_t)^2 v_{ov}^2$$

$$v_{ov} = \sqrt{12} = \pm 3.4V$$
 ~~$v_{gs} - v_t = 3.4V \propto (v_{gs} > v_t)$~~

$$v_{gs} - v_t = 3.4V$$
 ~~$v_t - v_{gs} = 3.4V$~~

$$v_{gs} = -4.4V$$

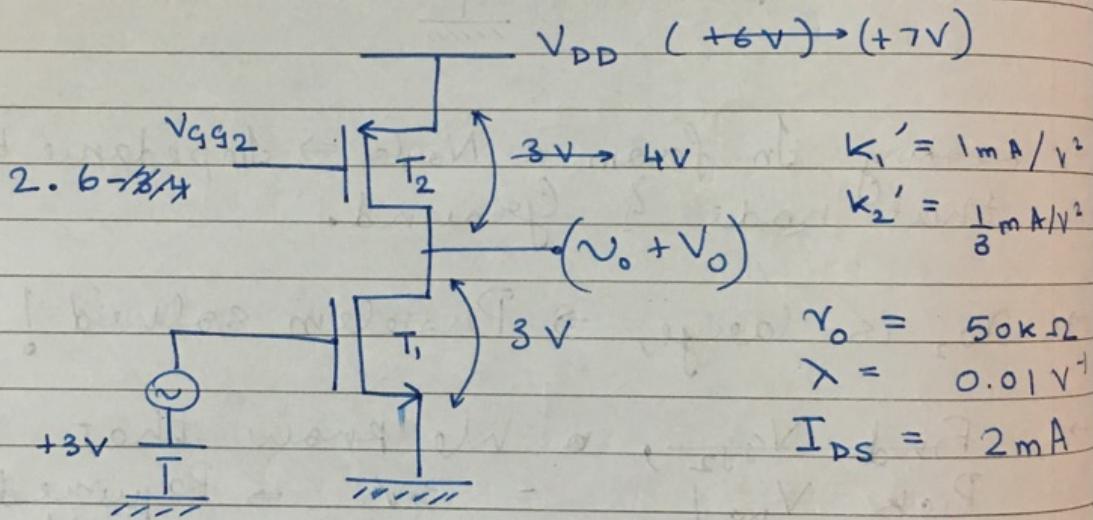
$$V_{gg} - V_{DD} = V_{gs2} \quad (\text{Defn.})$$

$$V_{gg} = 6V + -4.4V = 1.6V$$

$$\begin{aligned} V_{ov} &= -3.4V \\ V_{DS} &= -3V \end{aligned} \quad \left. \begin{array}{l} \text{Not in Saturation} \\ \Rightarrow \text{Redesign} \end{array} \right.$$

→ Simply Increasing V_{DD} won't work since V_{gs} will go down by same amount i.e. $|V_{gs}| \uparrow$ by same.

~~25.1.18~~



→ In simple model V_o is undeterministic (In a range), ~~but~~

$$I_{DS} = \frac{k'}{2} (V_{gs} - V_t)^2 \rightarrow \text{Parab.}$$

$$I_{DS} = \frac{k'}{2} (V_{gs} - V_t)^2 (1 + \lambda V_{DS})$$

(VH.8) → deterministic

over here $(V_{DS})_1 + (V_{DS})_2 = 7V$

& Since I_{DS} values are the same it will be midpoint in this case.

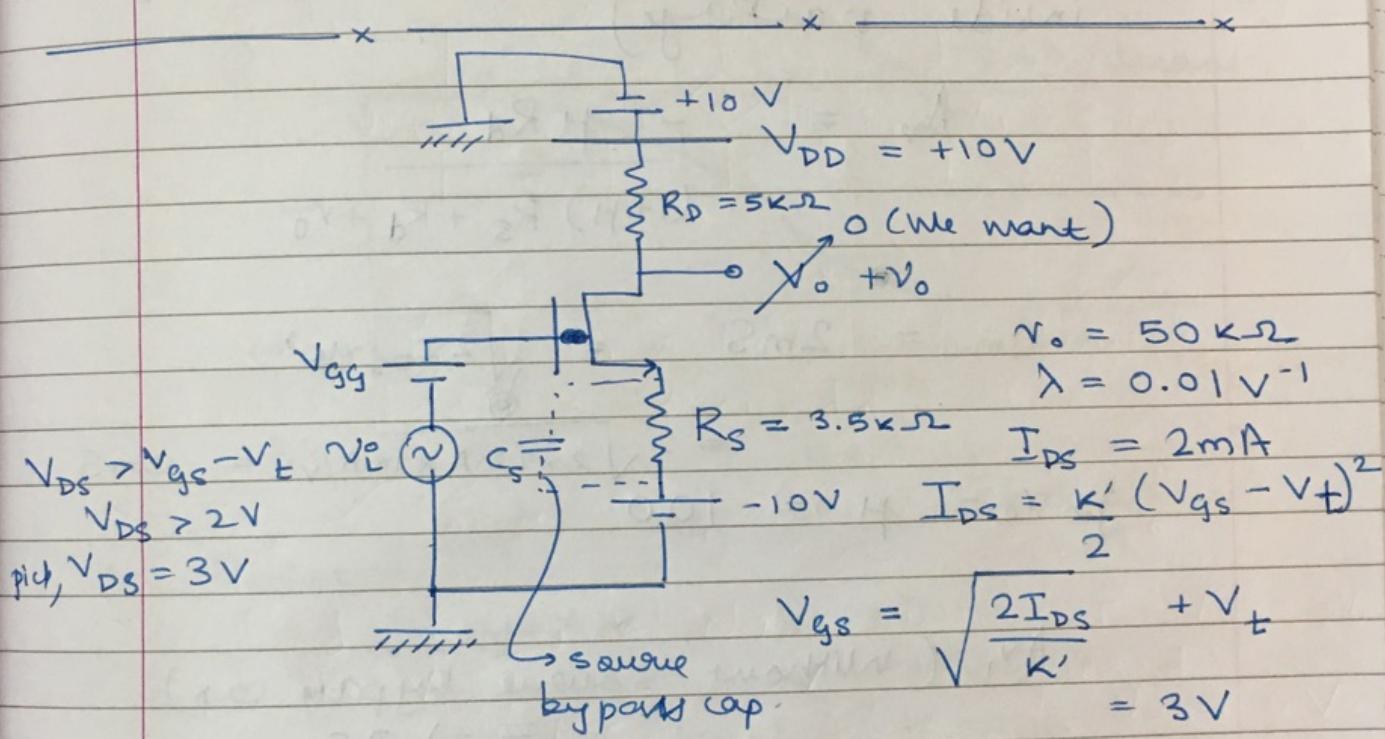
→ Why we care about DC bias: Theoretical
max headroom is V_{DS} (DC bias)

Page No.:

→ Advantages of PMOS

- ↓ footprint compared to resistances.
- We can have inverted configuration with NMOS load and PMOS Gain, but we loose out on both V_o and $g_m \Rightarrow$ Not as Good.

→ HW: solve for V_{DS} at junction of transistors.



$$\rightarrow V_o = 0$$

$$\Rightarrow V_{DS} + I_{DS} R_S = 10V$$

$$R_S = \frac{10V - 3V}{2mA} = 3.5k\Omega$$

$$V_{DD} = I_D R_D$$

$$\Rightarrow R_D = 5k\Omega$$

→ Hence it is possible to get a DC coupled
 (No need for cap. @ o/p) Amplifier

But we need a Bipolar source
 (Darlington)

→ Generally we use it as a middle stage
 amp. in op amp IC's (Ansaid cap.
 inside package)

$$A_v = - \frac{H R_d}{(1+H) R_s + R_d + r_o}$$

$$\begin{aligned} g_m &= 2mS &= \sqrt{2 I_{DS} \times k} \\ &= \sqrt{2 \times 2mA \times 1mA/V^2} = 2mS \\ g_m r_o &= H = 100 \end{aligned}$$

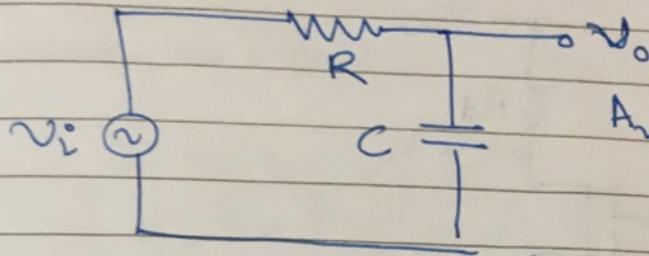
$$A_{v1} \text{ (Without source bypass cap)} = -1.25$$

R_s is pulling $A_v \downarrow$

⇒ Use a source bypass cap.

$$A_{v2} = \frac{-H R_d}{R_d + r_o} = \underline{\underline{-10}}$$

High frequency Response



$$A_v = \frac{V_o}{V_i} = \frac{1/j\omega C}{R + j\omega C}$$

$$= \frac{1/j\omega C}{R + \frac{1}{j\omega C}} = \frac{1}{1 + j\omega RC}$$

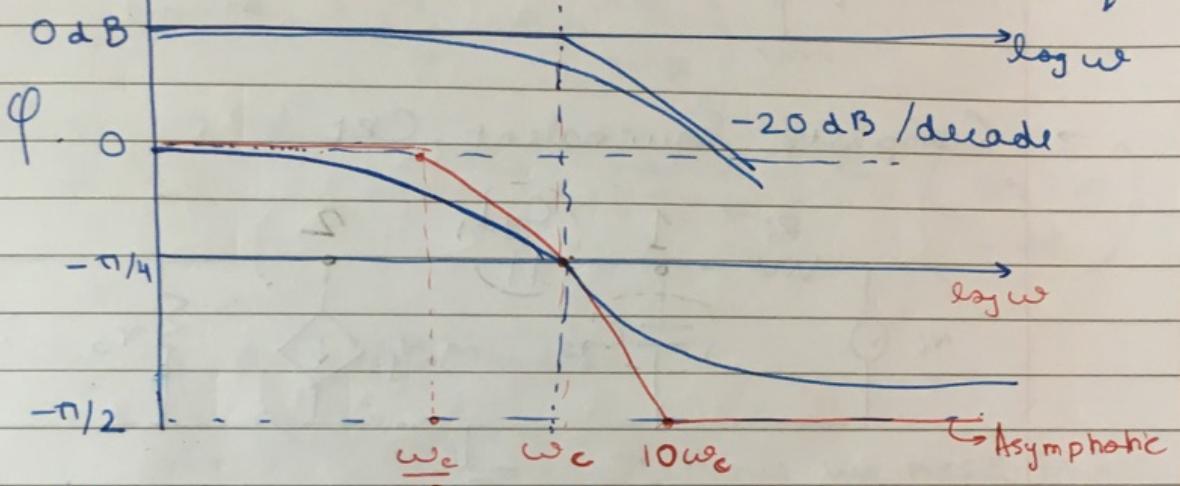
$$\omega = \frac{1}{RC} \rightarrow \omega_c = \frac{1}{RC}$$

RC Freq.

$20 \log |A_v|$

Gain / Mag. plot

$1/R_C = \omega_0 = \text{corner freq.}$



$$\text{At } \omega_0 = 1/R_C \rightarrow A_v = \frac{1}{\sqrt{2}} \text{ (corner/3dB freq.)}$$

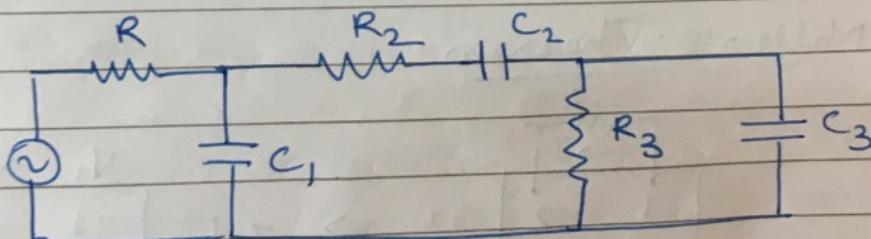
→ Phase;

$$\omega = 0, \phi = 0$$

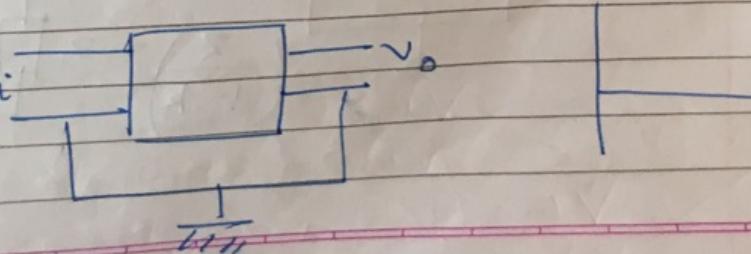
$$\omega = \infty, \phi = -\pi/2$$

$$\omega = 1/R_C, \phi = -\pi/4$$

Ex

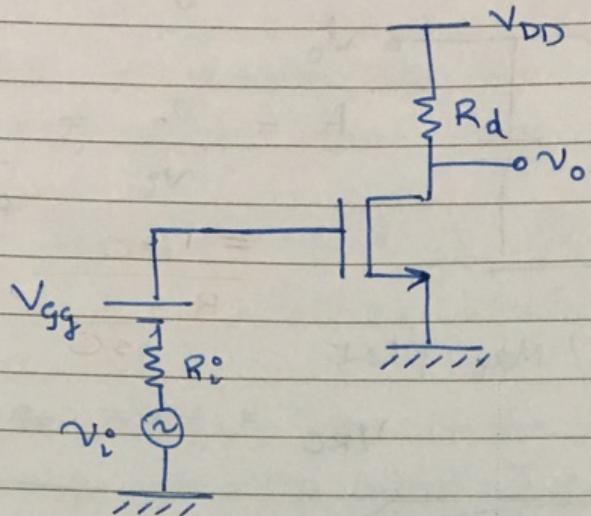


→ Consider it as a cascade of poles ω_i with transfer function Multi.

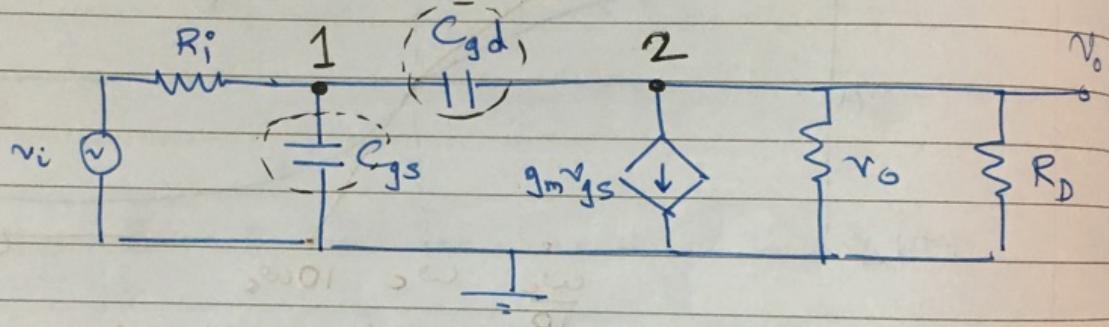


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Transistor at High frequency



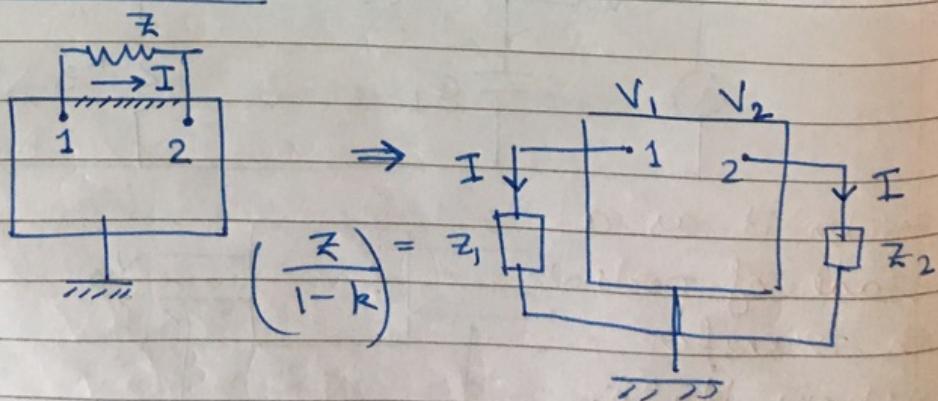
→ Complete Equivalent Circuit



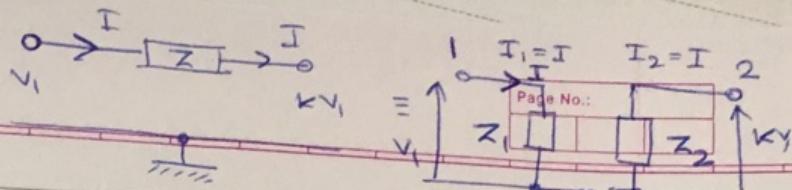
$$\frac{v_o}{v_i} = -\frac{s m (R_{o\parallel} \parallel R_o)}{R_i} (g_m - s C_{gd})$$

$$s^2 C_{gs} C_{gd} (R_o \parallel R_d) + s (C_{gs} + C_{gd} + C_{gs\parallel} \parallel R_d) \times (g_m + 1/R_i)$$

→ Miller's Theorem



Better Diagram



→ Suppose we know, $\frac{V_2}{V_1} = k \in \mathbb{C}$

$$I = \left(\frac{V_1 - V_2}{Z} \right) = \frac{1 - V_2/V_1}{Z/V_1} = \frac{(1-k)V_1}{kZ}$$

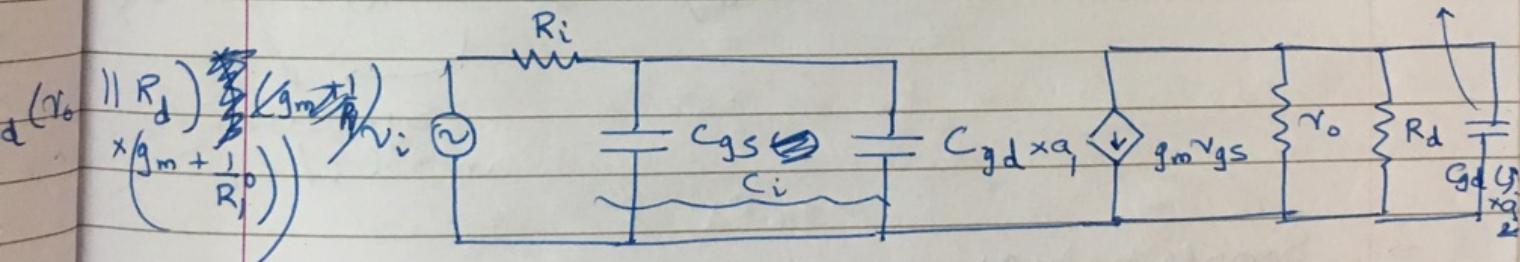
Now In 2nd model:

$$Z_1 = \frac{V_1}{I} = \left(\frac{Z}{1-k} \right)$$

$$kV_1 \frac{V_2}{I} = Z_2 = \left(\frac{k}{1-k} \right) Z \quad (\text{erig -ve})$$

→ Apply to our SS Model:

$$k = \frac{V_2}{V_1} = \left(\frac{V_o}{V_{GS}} \right) \rightarrow \text{Hence we need the Gain } \underline{\underline{C_o}}$$



$$\alpha \quad a_1 = (1-k) \quad \left\{ \begin{array}{l} Z = \frac{1}{SC} \\ \Rightarrow \text{Invertor factor for } C \end{array} \right.$$

$$a_2 = \left(\frac{k-1}{k} \right)$$

Define; $C_i = C_{GS} + C_{GD}(1-k)$
 $C_o = C_{GD} \frac{(1+k)}{k} \frac{(k-1)}{R}$

$$A_v = \frac{v_o}{v_i} = -g_m v_{gs}$$

$$V_o = -g_m v_{gs} \left(r_o \parallel R_d \parallel \frac{1}{sC_0} \right)$$

$$V_o = \frac{-g_m \left(r_o \parallel R_d \parallel \frac{1}{sC_0} \right) \times v_i \times \frac{1}{sC_i}}{\frac{1}{sC_i} + R_i}$$

$$A_v = \frac{-g_m \left(r_o \parallel R_d \parallel \frac{1}{sC_0} \right)}{\left(1 + sC_i R_i \right)} =$$

$$\frac{-g_m}{\left(1 + sC_i R_i \right)} \left(\frac{R_d' \frac{1}{sC_0}}{R_d' + \frac{1}{sC_0}} \right) = \frac{-g_m}{\left(1 + sC_i R_i \right)} \frac{R_d'}{\left(1 + sC_i R_i \right) \left(1 + sC_i R_i \right)}$$

Symmetric
in C_i/C_0

→ Sample values

$$g_m = 2mS, R_i = 1k\Omega, C_{gs} = 10pF, C_{gd} = 1pF$$

$$V_o = 50k\Omega, R_d = 10k\Omega$$

Comparing Expressions

1) Design Expression (Previous page)

$$= \frac{-8.3 \left(2 \times 10^{-3} - s \times 10^{-12} \right)}{s^2 C_{gs} C_{gd} \left(r_o \parallel s^2 (8.3 \times 10^{-20}) + s (3.59 \times 10^{-12}) \right) + 10^{-3}}$$

2) Our expression using modified

$$K = \frac{V_2}{V_i} = \left(\frac{V_o}{V_{GS}} \right) \approx -g_m (r_o \parallel R_d) = -16.6$$

→ Simulation Results

value of
K is used

$$20 \log \left(\frac{V_2}{V_i} \right) = 15$$

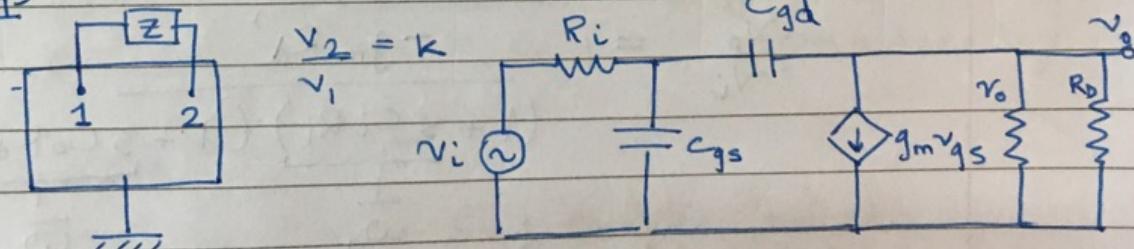
$$\Rightarrow \frac{V_2}{V_i} = 10^{\frac{15}{20}} = 5.6 \rightarrow$$

$$\approx -16.6$$

$$\frac{(1 + s \times 27.6 \times 10^{-9})}{(1 + s \times 1.06 \times 8.3 \times 10^{-9})}$$

→ Our analysis shows that approximation is valid until 3dB Gain \Rightarrow It can be used.

Recap



$$Z_1 = \frac{Z}{1-K}$$

$$Z_2 = \frac{KZ}{(K-1)}$$

$$R_i \parallel C_L \parallel g_m V_{GS} \parallel C_O \parallel R'_D$$

where,

$$C_L = C_{GS} + C_{GD}(1-K)$$

$$C_O = \left(\frac{K-1}{K} \right) C_{GD} = 1.06 \text{ pF}$$

$$= 27.6 \text{ pF}$$

$$R'_D = r_o \parallel R_D \approx R_D = 8.33 \text{ k}\Omega$$

$$\frac{V_o}{V_i} = -g_m \left(\frac{r_o || R_D}{R_i} \right) (g_m - SC_{gd})$$

$$s^2 C_{gs} C_{gd} (r_o || R_D) + \frac{1}{R_i} +$$

$$s [C_{gs} + C_{gd} + C_{gd} (r_o || R_D) (g_m +$$

↓ simplified version, using

$$\frac{V_o}{V_i} = \frac{-g_m R_d'}{(1 + sC_i R_i)(1 + sC_o R_d')}$$

→ Miller's thm

$$I = \frac{V_1 - V_2}{Z} = V_1 \frac{(1 - k)}{Z} = \frac{V_1 (1 - k)}{Z(1 - k)} V,$$

$$\Rightarrow Z' = \left(\frac{Z}{1 - k} \right)^{-1}$$

$$-I = \frac{V_2 - V_1}{Z} = \frac{V_2 (1 - \frac{1}{k})}{Z} \Rightarrow Z' = \left(\frac{kZ}{k - 1} \right)$$

→ Bode plot of Av vs f

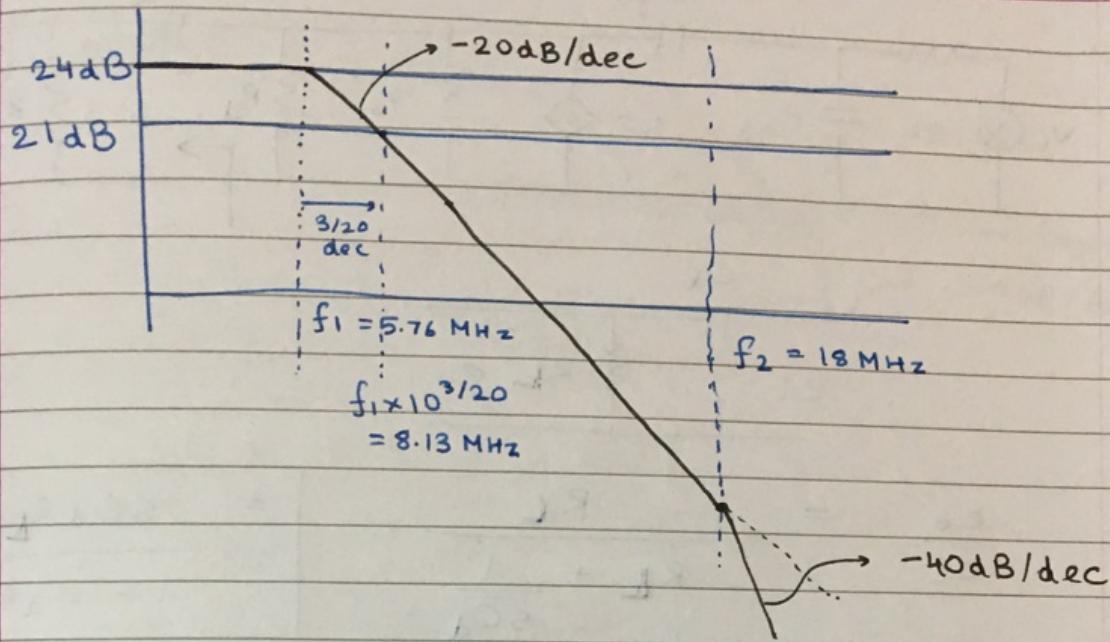
$$\frac{V_o}{V_i} = \frac{-g_m R_d'}{(1 + sC_i R_i)(1 + sC_o R_d')} \frac{\frac{1}{\omega_1}}{\frac{1}{\omega_2}}$$

$$f_1 = \frac{1}{2\pi R_i C_i}, \quad f_2 = \frac{1}{2\pi R_d' C_o}$$

We get, using $k = -16.6$ (last lec.)

$$f_1 = 5.76 \text{ MHz}, \quad f_2 = 18 \text{ MHz}$$

$$20 \log (g_m R_d') = 24 \text{ dB (peak gain)}$$



→ Approximations

$$A_v \Big|_{DC, \omega=0} = -g_m (\nu_o \parallel R_d) \approx k$$

$$f_1 = \frac{1}{2\pi R_i C_i}$$

$$f_1 \approx \frac{1}{2\pi R_i (1-k) C_{gd}} \quad ; \quad k C_{gd} > C_{gs}$$

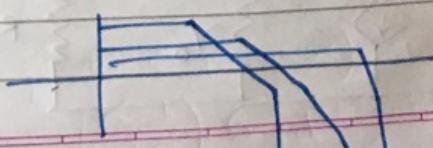
$$f_1 \approx \frac{1}{2\pi R_i |k| C_{gd}}$$

$$|A_v| f_1 = \frac{1}{2\pi R_i C_{gd}} \left(\text{Gain - BW product} \right)$$

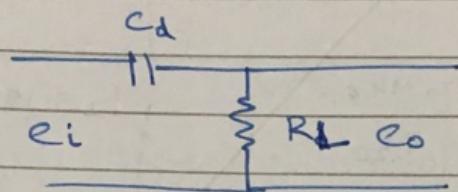
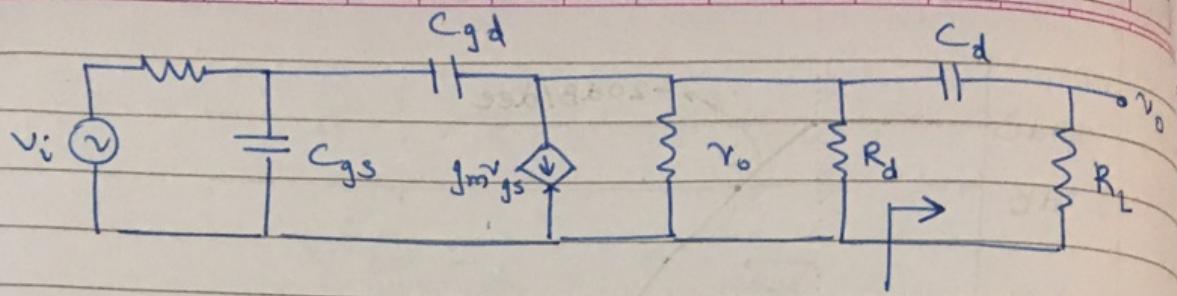
→ Qualitatively :

- High ν across cap. C_{gd} needs to change, hence can't respond at very high frequency. $(1+A_v)\nu_i$ changes period. across cap.

↳ $R_i C_i \approx T$ (ν_o is being changed by thus)

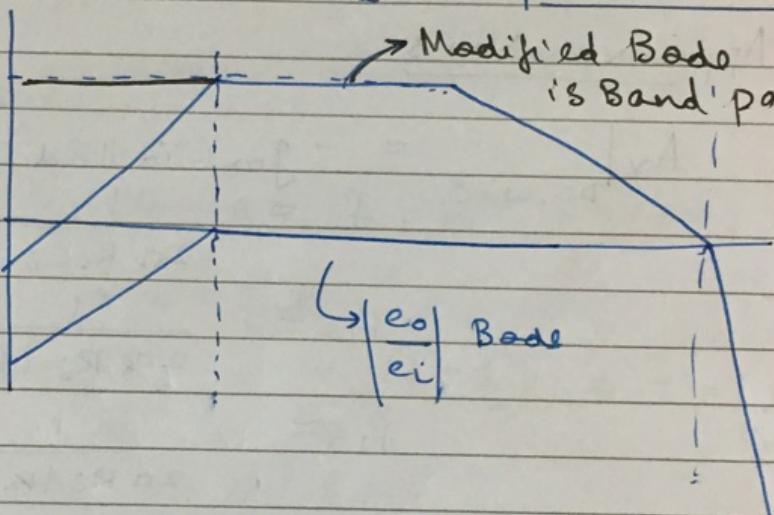


(Must sacrifice Gain for BW)



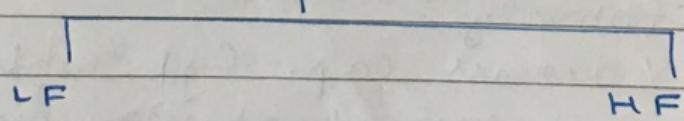
$$\frac{e_o}{e_i} = \frac{R_L}{R_L + \frac{1}{sC_d}} = \frac{sC_d R_L}{1 + sC_d R_L}$$

Gain in dB

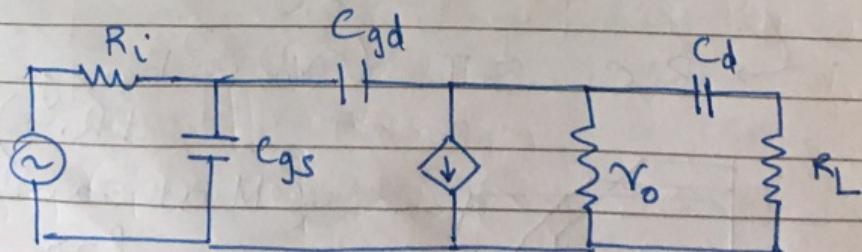


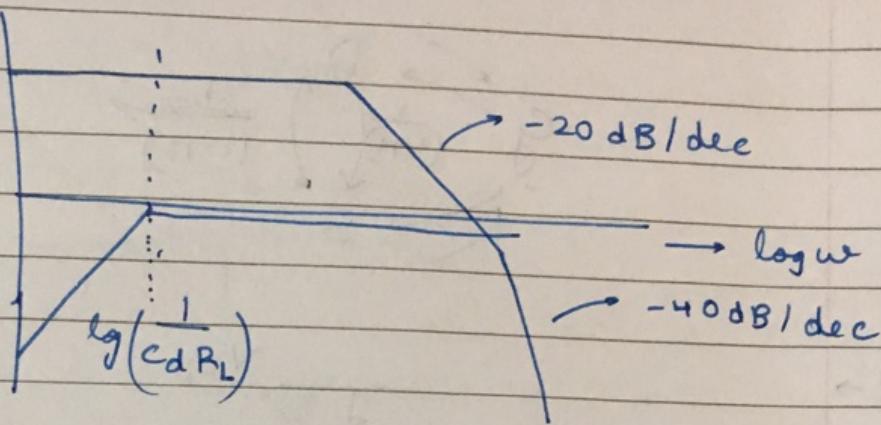
→ Hence we got a Band pass filter.

C_s, C_g, C_D



→ Bode plot :



$20 \log |A_{v1}|$ 

$$\frac{1}{1 + \frac{1}{SC_d R_L}}$$

$$\frac{1}{1 + \frac{1}{j\omega C_d R_L}}$$

$$\omega_c = \frac{1}{C_d R_L}$$

→ No -40 dB line in simulation

$$\frac{V_o}{V_i} = \frac{-g_m (r_o \parallel R_d)}{(1 + SR_i C_i)(1 + SC_o (r_o \parallel R_d) r_i)}$$

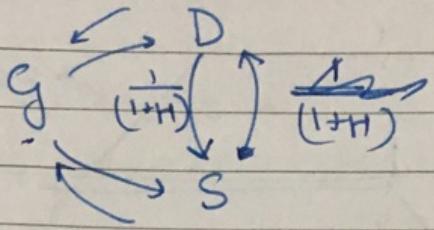
$$\omega \rightarrow \infty \Rightarrow \frac{V_o}{V_i} \propto \frac{1}{\omega^2} \quad (-40 \text{ dB})$$

$$\frac{V_o}{V_i} = \frac{-(g_m - SC_{gd})}{s^2 + \beta s + Y_i}$$

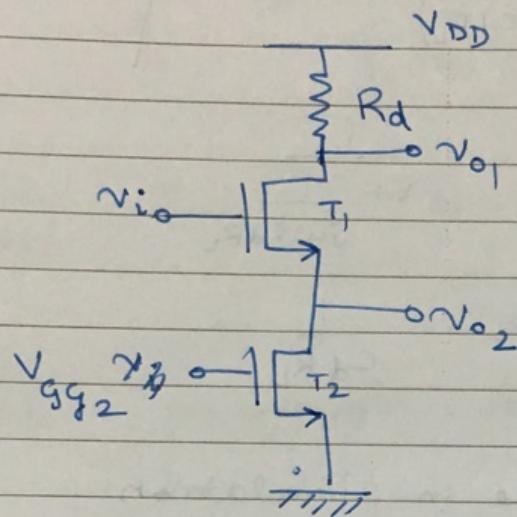
$$\omega \rightarrow \infty \Rightarrow \frac{V_o}{V_i} \propto \frac{1}{\omega} \quad (-20 \text{ dB})$$

→ Voltage signs in referred circuit

$$\begin{aligned}
 V_g \uparrow &\Rightarrow i_{ds} \uparrow \Rightarrow i_{ds} R_d \uparrow \Rightarrow V_{o1} \downarrow \rightarrow CS \\
 V_i \uparrow &\Rightarrow i_{ds} \uparrow \Rightarrow i_{ds} R_S \uparrow \Rightarrow V_{o2} \uparrow \rightarrow CD \\
 V_i \uparrow &\Rightarrow V_g \uparrow \Rightarrow V_{o2} \uparrow \Rightarrow V_{o1} \uparrow
 \end{aligned}$$



$Q \rightarrow$



Given,
 T_1 & T_2 in sat.

CS

$$A_V = \frac{-H R_d}{R_d + (1+H)r_{o2} + r_{o1}}$$

$$Z_i = \infty$$

$$Z_o = R_d \parallel [r_{o1} + (1+H)r_{o2}]$$

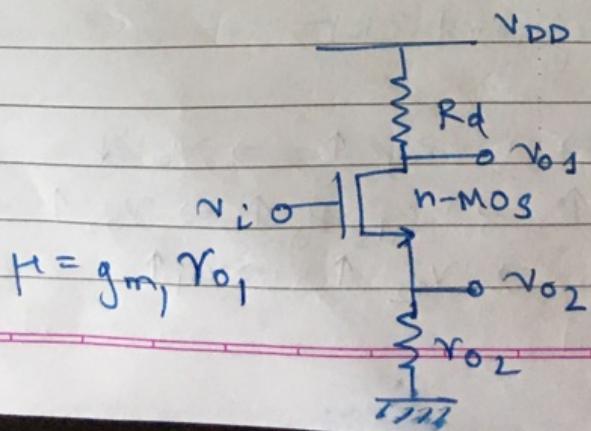
CD

$$A_V = \frac{\left(\frac{H}{1+H}\right) R_s}{\left(\frac{R_d + r_{o1}}{1+H}\right) + r_{o2}}$$

$$Z_i = \infty$$

$$Z_o = R_s \parallel \left[\frac{r_{o1} + R_d}{(1+H)} \right]$$

↓ ac eqvt



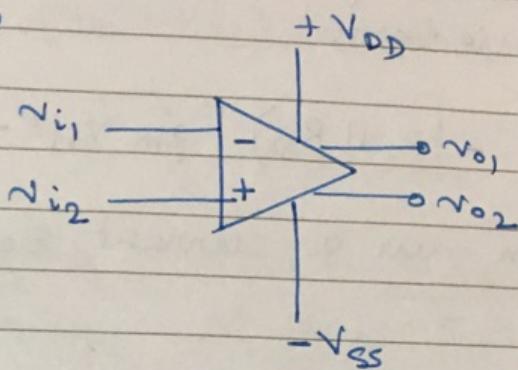
⇒ Replace R_s by r_{o2}

$$H = g_m, r_{o1}$$

Differential Amplifier

Page No.:

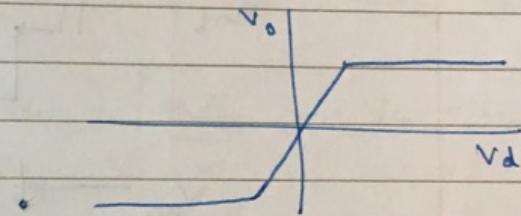
→ Op amp



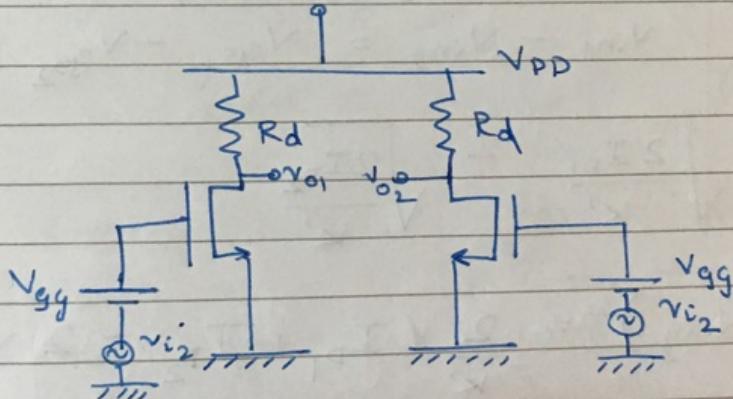
$$v_d = v_{\text{diff,in}} = (v_{i1} - v_{i2})$$

$$v_o = v_{\text{diff,out}} = (v_{o1} - v_{o2})$$

$$\left| \frac{v_o}{v_d} \right| = \text{large}$$



→ primitive difference amplifier



→ Assume completely identical tran.

$$A_{\text{diff}} = \frac{v_{o1} - v_{o2}}{v_{i1} - v_{i2}} = -g_m (r_o \parallel R_d) \frac{(v_{i1} - v_{i2})}{(v_{i1} - v_{i2})}$$

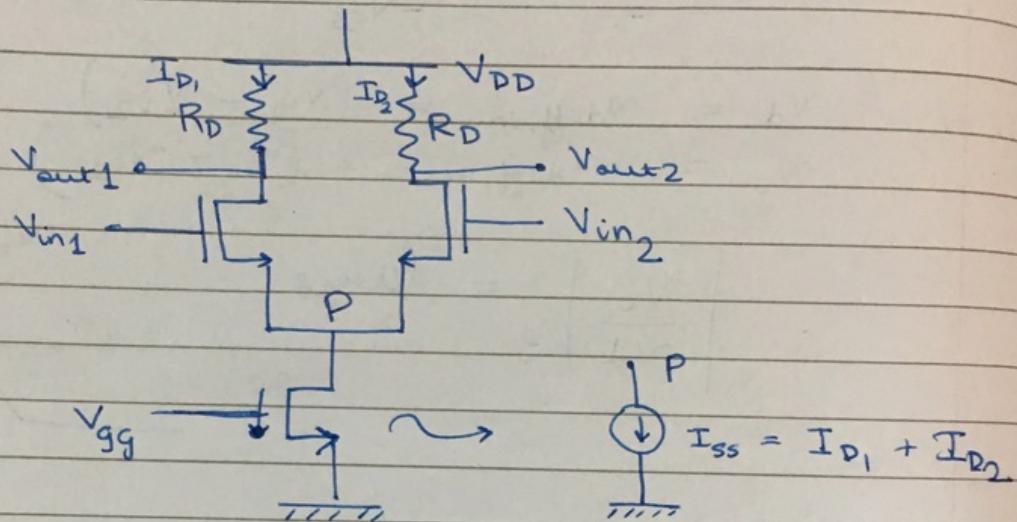
$$A_{\text{diff}} = -g_m (r_o \parallel R_d)$$

→ Major flaw: If there is slight mismatch b/w transistors,

$$A_{dil} = -(\tau_0 \parallel R_D) [g_m v_{i_1} - g_m v_{i_2}]$$

→ Solution use a current source below

~~6.2.18~~



→ Equations: (Large Signal char.)

$$\begin{aligned} v_{in_1} - v_{in_2} &= v_{gs_1} - v_{gs_2} \left(\frac{v_{in_1} - v_p}{k'} - \frac{v_{in_2} - v_p}{k'} \right) \\ &= \sqrt{\frac{2I_{D1}}{k'}} - \sqrt{\frac{2I_{D2}}{k'}} \end{aligned}$$

$$(v_{in_1} - v_{in_2})^2 = \frac{2}{k'} (I_{D1} + I_{D2} - 2\sqrt{I_{D1}I_{D2}})$$

$$I_D = \frac{k'}{2} (v_{gs} - v_T)^2$$

$$\Rightarrow v_{gs} = \sqrt{\frac{2I_D}{k'}} + v_T$$

$$v_{out_1} = v_{DD} - I_{D1}R_D$$

$$v_{out_2} = v_{DD} - I_{D2}R_D$$

→ We want $(V_{o_1} - V_{o_2})$ in terms of $(V_{in_1} - V_{in_2})$

$$V_{o_1} - V_{o_2} = -R_D (I_{D_1} - I_{D_2})$$

$$\frac{4 I_{D_1} I_{D_2}}{\sqrt{I_{D_1} I_{D_2}}} = \frac{(I_{D_1} + I_{D_2})^2 - (I_{D_1} - I_{D_2})^2}{\frac{1}{2} \sqrt{I_{ss}^2 - (I_{D_1} - I_{D_2})^2}}$$

$$I_{D_1} \neq I_{D_2}$$

$$\Rightarrow (V_{in_1} - V_{in_2}) = \frac{2}{k'} (I_{ss} - \sqrt{I_{ss}^2 - (I_{D_1} - I_{D_2})^2})$$

$$I_{D_1} - I_{D_2} = \frac{k'}{2} (V_{in_1} - V_{in_2}) \sqrt{\frac{4 I_{ss}}{k'} - (V_{in_1} - V_{in_2})^2}$$

$$\sqrt{\frac{2}{k'}} (\sqrt{I_{D_1}} - \sqrt{I_{D_2}})$$

→ Transconductance

$$G_m = \frac{8 \Delta I_D}{8 \Delta V_{in}} \quad (\text{Differential amp.})$$

$$G_m = \frac{1}{2} k' \left(\frac{\frac{4 I_{ss}}{k'} - 2 \Delta V_{in}^2}{\sqrt{\frac{4 I_{ss}}{k'} - \Delta V_{in}^2}} \right)$$

$$\Delta V_{in} \ll 1, \quad G_m = \frac{k'}{2} \sqrt{\frac{4 I_{ss}}{k'}} = \sqrt{k' I_{ss}}$$

$$\rightarrow \text{Regular amp. } g_m = \frac{8 I_{DS}}{8 V_{GS}} = k' (V_{GS} - V_T)$$

$$= k' \sqrt{\frac{2 I_D}{k'}} = \sqrt{2 k' I_D}$$

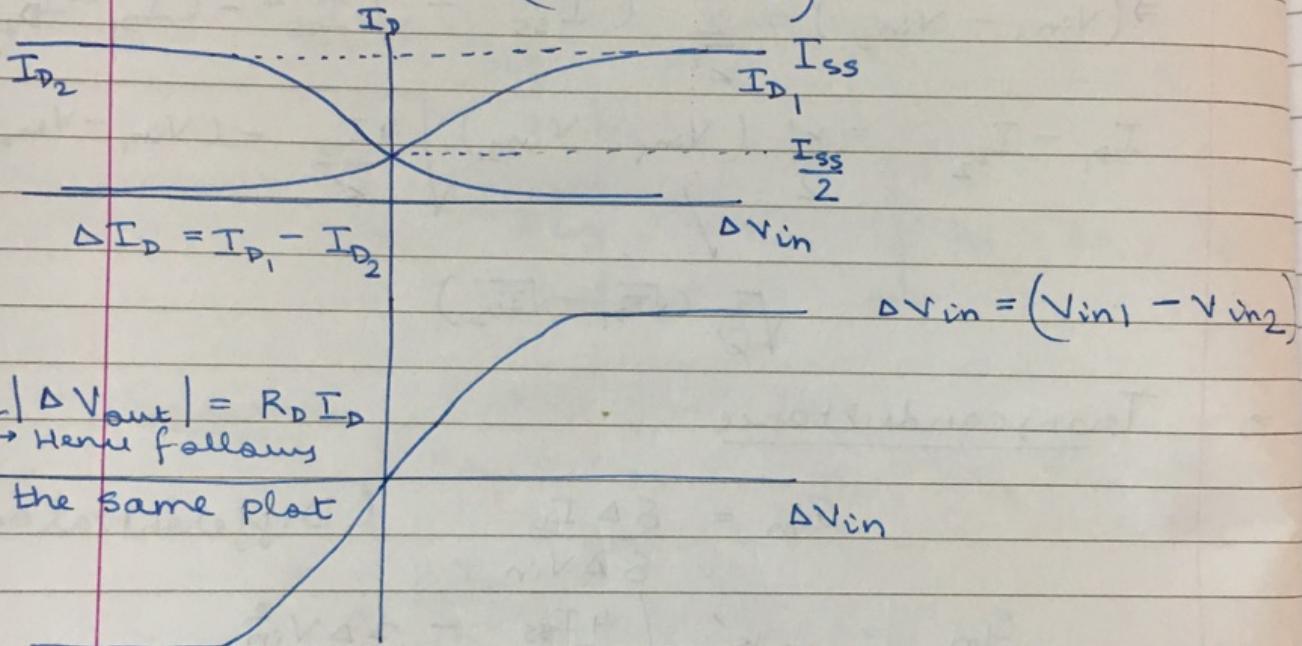
$$\rightarrow \Delta V_{out} = V_{out_1} - V_{out_2} = (V_{DD} - I_{D_1} R_D) - (V_{DD} - I_{D_2} R_D)$$

$$\Delta V_{out} = -(I_{D_1} - I_{D_2}) R_D$$

$$A_v = \frac{\delta \Delta V_{out}}{\delta \Delta V_{in}} = \frac{\delta \Delta V_{out}}{\delta \Delta I_D} \cdot \frac{\delta \Delta I_D}{\delta \Delta V_{in}}$$

$$A_v = -G_m R_D = -\sqrt{k' I_{ss} R_D}$$

$$\Delta V_{in} = (V_{in_1} - V_{in_2})$$



$|\Delta V_{out}| = R_D \Delta I_D$
Hence follows

the same plot

\rightarrow Differential Amp. Key Equations

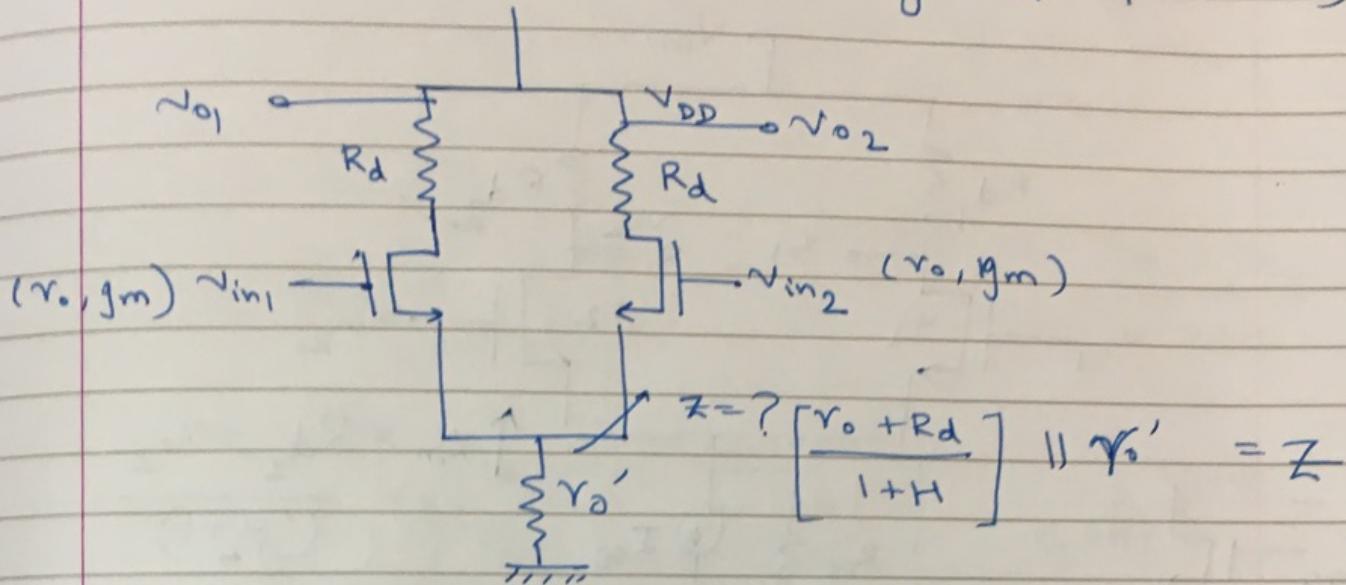
$$I_{D_1} - I_{D_2} = f(V_{in_1} - V_{in_2})$$

$$I_{D_1} + I_{D_2} = I_{ss}$$

$$G_m = \frac{S \Delta T_D}{S \Delta V_{in}}$$

$$A_v = -G_m R_D$$

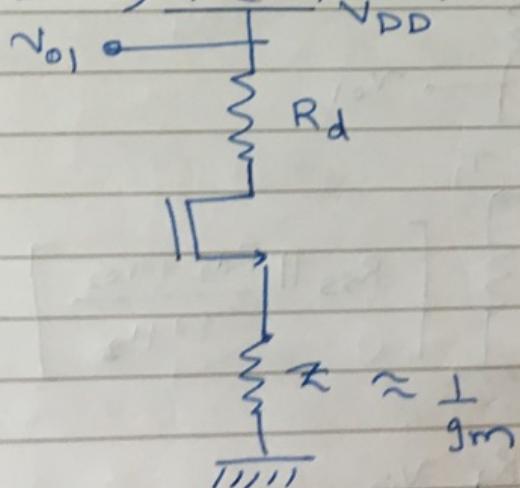
→ Small Signal Char. (Using superposition)



$$\text{By } \frac{v_o + R_d}{1 + H}, \quad v_o \gg R_d = \text{---}, \quad H = g_m r_o \\ \Rightarrow \approx \frac{1}{g_m}$$

$$g_m \rightarrow 2 \text{ mS} \rightarrow \frac{1}{g_m} \approx 500 \Omega$$

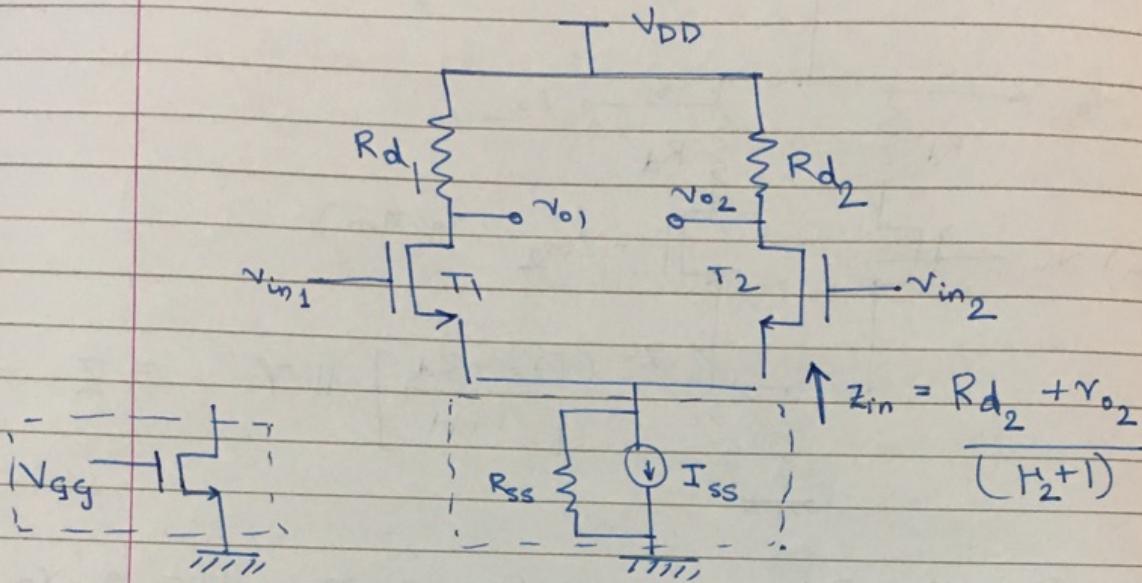
$v_{in1} \neq 0, v_{in2} = 0$ → Superposition



$$A_d = \frac{v_{o1} - v_{o2}}{v_{in1} - v_{in2}}$$

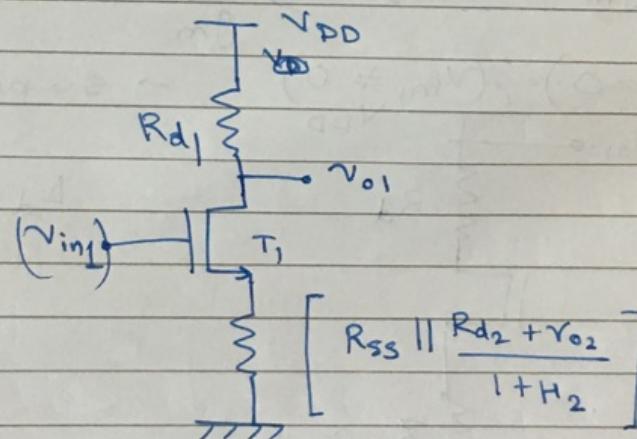
- Bode Plots Revision → Done EE225 notes.

→ Differential Amplifier



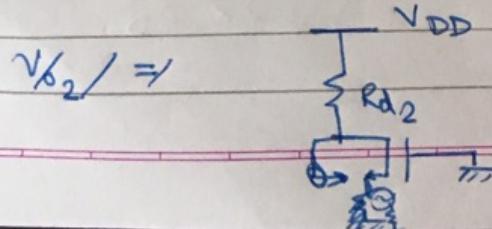
→ Small Signal Analysis
 $v_{in1} \neq 0, v_{in2} = 0$

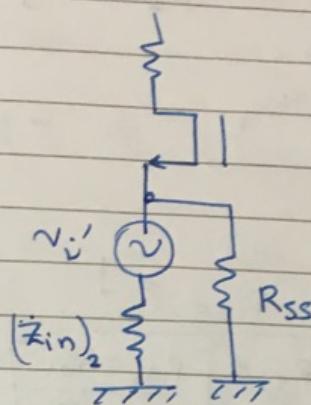
Gain, $A_{dm} = -g_m$



$$v_{o1} = -H_1 R_{d1} v_{in1}$$

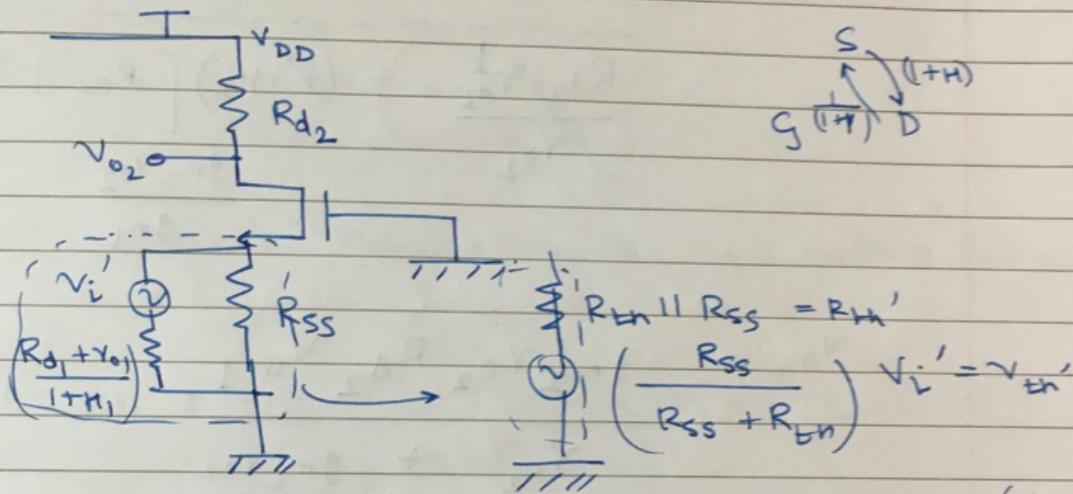
$$R_{d1} + r_{o1} + (1 + H_1) \left[R_{ss} \parallel \frac{R_{d2} + r_{o2}}{1 + H_2} \right]$$





$$V_{i1}' = \left(\frac{H_1}{1+H_1} \right) V_{in1}$$

$$R_{th} = \frac{R_{d1} + r_o}{1+H_1}$$



$G \xrightarrow{\frac{1}{H}} D$

$$R_{th} \parallel R_{SS} = R_{th}'$$

$$V_{i1}' = V_{th}' \left(\frac{R_{SS}}{R_{SS} + R_{th}} \right)$$

$$V_{o2} = \frac{(1+H_2) V_{th}' R_{d2}}{(1+H_2) R_{th}' + r_{o2} + R_{d2}} = \frac{(1+H_2) R_{d2} \frac{R_{SS}}{R_{SS} + R_{th}} \left(\frac{H_1}{1+H_1} \right) V_{in1}}{R_{d2} + r_{o2} + (1+H_2)} \times \left[\frac{R_{SS} \parallel R_{d1} + r_{o1}}{1+H_1} \right]$$

$$= (1+H_2) R_{d2} \times \frac{R_{SS}}{R_{SS} + \left(\frac{R_{d1} + r_{o1}}{1+H_1} \right)} \left(\frac{H_1}{1+H_1} \right) V_{in1}$$

$$R_{d2} + r_{o2} + (1+H_2) \left[\frac{R_{SS} \parallel R_{d1} + r_{o1}}{1+H_1} \right]$$

→ Approximations

$$V_{o1} = - g_{m1} V_{o1} V_{in1} R_{d1} \frac{\frac{r_{o2}}{g_{m2} r_{o2}} = 1/g_{m2}}{V_{o1} + \frac{(g_{m1} V_{o1})}{g_{m2}}}$$

$$g_m = g_{m_1} = g_{m_2}, R_{d_1} = R_{d_2} = R_D \text{ in always}$$

$$\Rightarrow v_{o_1} = -\frac{g_m R_d v_{in_1}}{1+1} = -\frac{g_m R_D}{2} v_{in_1}$$

$$v_{o_2} = \frac{(V+H_2) R_{d_2} \frac{R_{SS} \rightarrow 1}{R_{SS} + \left[\frac{R_{d_1} + r_{o_1}}{1+H_1} \right]} \frac{H_2 \uparrow 1}{Y \uparrow H_1} v_{in_2}}{\frac{R_{d_2} + r_{o_2}^1}{r_{o_2}} + (V+H_2) \left[R_{SS} \parallel \frac{R_{d_1} + r_{o_1}}{1+H_1} \right] \frac{g_{m_2} v_{o_2}}{g_{m_1}}}$$

$$v_{o_2} = \frac{g_{m_2} r_{o_2} R_{d_2} v_{in_1}}{r_{o_2} + \frac{g_{m_2} r_{o_2}}{g_{m_1}}} = \frac{g_m r_o R_d v_{in}}{r_o + r_o} \\ = \frac{g_m R_d v_{in}}{2}$$

Summary

$$v_{in_1} \neq 0, v_{in_2} = 0, v_{o_1} = -\frac{g_m R_D}{2} v_{in_1}$$

$$v_{o_2} = \frac{g_m R_d}{2} v_{in_1}$$

$$v_{in_1} = 0, v_{in_2} \neq 0, v_{o_1} = \frac{g_m R_D}{2} v_{in_2}$$

$$v_{o_2} = -\frac{g_m R_D}{2} v_{in_2}$$

→ Suppose,

$$v_{o_1} = -\frac{g_m R_D}{2} (v_{in_1} - v_{in_2})$$

$$v_{o_2} = \frac{g_m R_D}{2} (v_{in_1} - v_{in_2})$$

$$A_{dm} = \frac{v_{o_1} - v_{o_2}}{v_{in_1} - v_{in_2}} = -g_m R_D$$

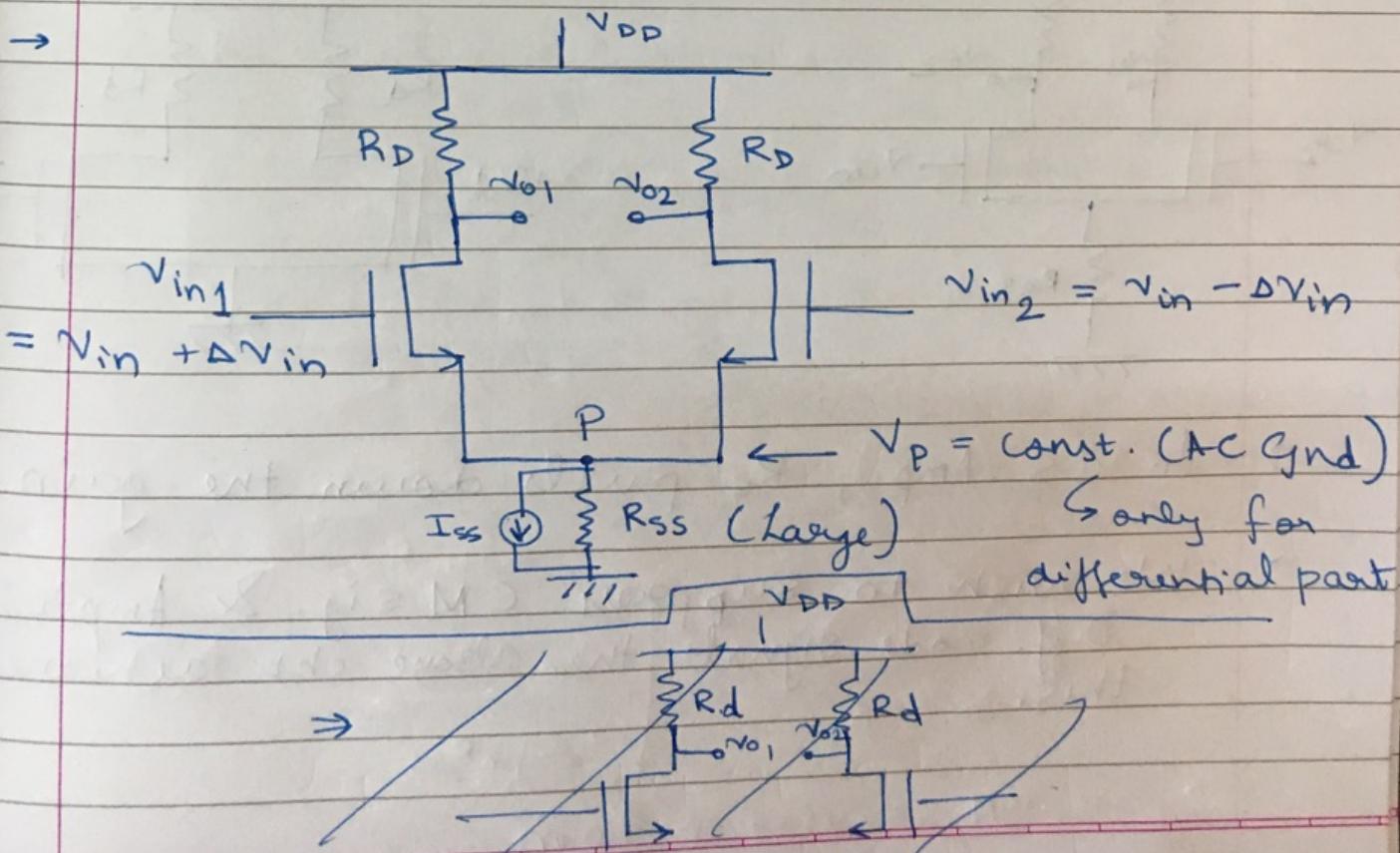
$$\boxed{A_{dm} = -g_m R_D}$$

→ We connected transistors to equivalent linear model.

$$i = f(v)$$

$$i_1 + i_2 = f(v_1 + v_2) = f(v_1) + f(v_2)$$

$$ai = f(av)$$



$$\rightarrow v_{in1} = v_{cm} + v_{dm}$$

$$v_{in2} = v_{cm} - v_{dm}$$

$$v_{cm} = \frac{v_{in1} + v_{in2}}{2}, v_{dm} = \frac{v_{in1} - v_{in2}}{2}$$

\$v_{in1}\$ \$v_{in2}\$

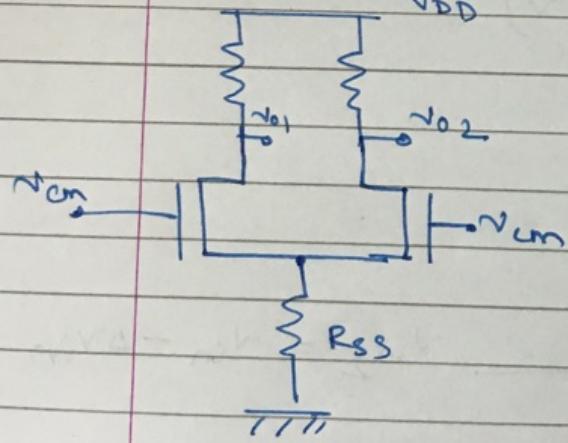
$$hence \quad v_{in1} = \left(\frac{v_{in1} + v_{in2}}{2} \right) + \left(\frac{v_{in1} - v_{in2}}{2} \right)$$

$$v_{in2} = \left(\frac{v_{in1} + v_{in2}}{2} \right) - \left(\frac{v_{in1} - v_{in2}}{2} \right)$$

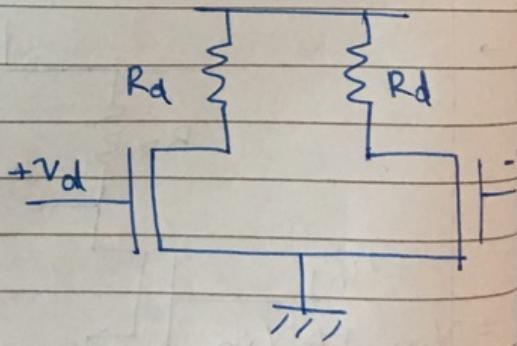
may
be
written in above form.

\rightarrow P is small signal AC ground for differential signal mode only.

CM Ckt



Differential Ckt



\rightarrow In CS Amp., R_S pulls down the gain.

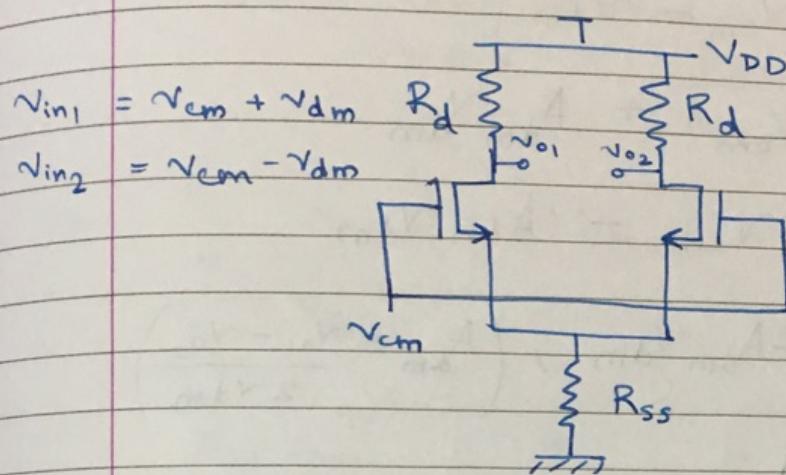
\rightarrow We wish to suppress CM sig. & amplify Diff. mode signal, the above ckt achieves this.

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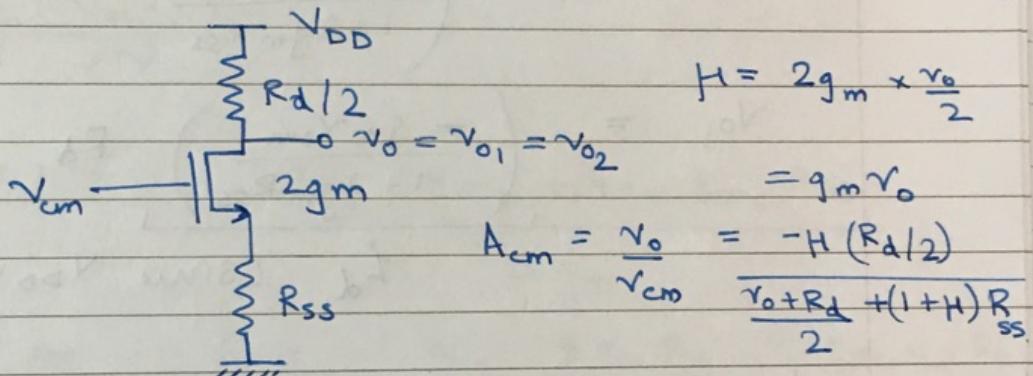
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→ CM Gain continued



→ Small signal v_{cm} applied to gates of both T_1 & T_2

→ Two Identical transistors in parallel, $v_o = v_{o1} = v_{o2}$



$$\frac{v_o}{v_{cm}} \approx \frac{-HR_d}{(v_o + R_d) + 2(1+H)R_{ss}} \approx \frac{-HR_d}{v_o + 2HR_{ss}}$$

$$\approx \frac{-g_m v_o R_d}{v_o + 2g_m v_o R_{ss}}$$

$$\frac{v_o}{v_{cm}} = \left[\frac{-g_m R_d}{1 + 2g_m R_{ss}} = A_{cm} \right] \rightarrow \text{Defined this way}$$

$$A_{DM} = -g_m R_D$$

$$\begin{aligned} V_{in1} &= V_{cm} + V_{dm} \\ V_{in2} &= V_{cm} - V_{dm} \end{aligned} \quad \left\{ \text{Diff} = 2V_{dm} \right.$$

$$V_{o1} = A_{cm} V_{cm} + A_{dm} V_{dm}$$

$$V_{o2} = A_{cm} V_{cm} - A_{dm} V_{dm}$$

$$V_{o1} - V_{o2} = 2A_{dm} V_{dm}, \quad \left(A_{dm} = \frac{V_{o1} - V_{o2}}{2V_{dm}} \right)$$

→ Mismatch in R_d

$$V_{o1} \quad A_{cm} = \left(\frac{-g_m R_d \times V_{cm}}{1 + 2g_m R_{ss}} \right)$$

$$V_{o1} = \left(\frac{-g_m V_{cm}}{1 + 2g_m R_{ss}} \right) R_d$$

I_d (Since $V_{DD} \equiv AC Gnd$)

→ Let us assume mismatch in R_D , and ↑ in V_o
 $= \Delta V_o$

$$\Delta V_{o1} = \frac{-g_m \Delta V_{cm}}{1 + 2g_m R_{ss}} R_d$$

$$\Delta V_{o2} = \frac{-g_m \Delta V_{cm} (R_d + \Delta R_d)}{1 + 2g_m R_{ss}}$$

$$\Delta (V_{o1} - V_{o2}) = \frac{g_m \Delta V_{cm} \Delta R_d}{1 + 2g_m R_{ss}}$$

$$\frac{\Delta (V_{o1} - V_{o2})}{\Delta V_{cm}} = \frac{g_m \Delta R_d}{1 + 2g_m R_{ss}} = A_{CM-DM}$$

→ CM-DM conversion term

→ Common Mode Rejection Ratio,

$$CMRR = \frac{A_{DM}}{A_{CM-DM}}$$

$$A_{CM-DM}$$

$$\rightarrow \frac{V_{o1} - V_{o2}}{2V_{dm}} = |A_{DM}| = \frac{R_D}{2} \frac{g_{m_1} + g_{m_2} + 4g_{m_1}g_{m_2}R_{SS}}{1 + (g_{m_1} + g_{m_2})R_{SS}}$$

$$A_{CM-DM} = -\frac{\Delta g_m R_D}{(g_{m_1} + g_{m_2})R_{SS} + 1}$$

$$= \frac{\Delta (V_{o1} - V_{o2})}{V_{cm}}$$

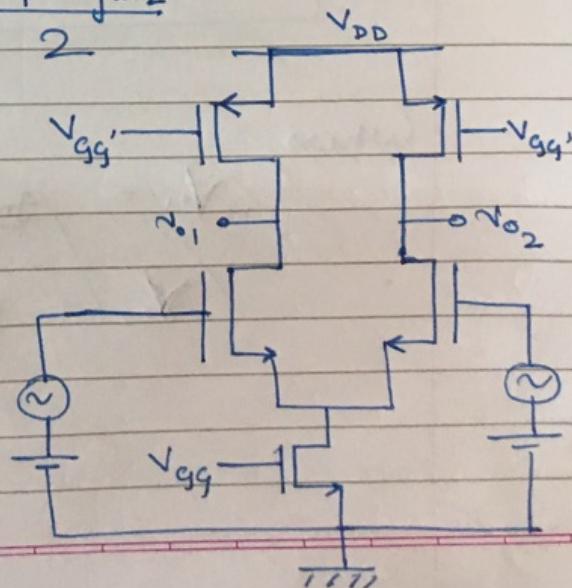
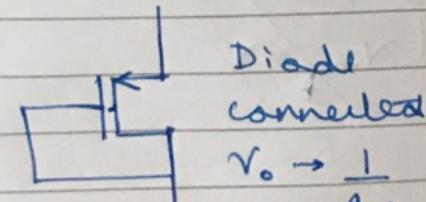
$$|CMRR| = \frac{1}{\Delta g_m} \frac{g_{m_1} + g_{m_2} + 4g_{m_1}g_{m_2}R_{SS}}{\Delta g_m}$$

$$\rightarrow CMRR \underset{\Delta R_D}{\approx} \frac{A_{DM}}{A_{CM-DM}} \approx \frac{\Delta g_m R_D}{\frac{\Delta R}{2R_{SS}}} = 2g_m R_{SS} \left(\frac{R_D}{\Delta R} \right)$$

→ If $\Delta g_m, \Delta R_D \neq 0$, Add the responses and take $g_m = \frac{g_{m_1} + g_{m_2}}{2}$.

→ Replace R_D with PMOS

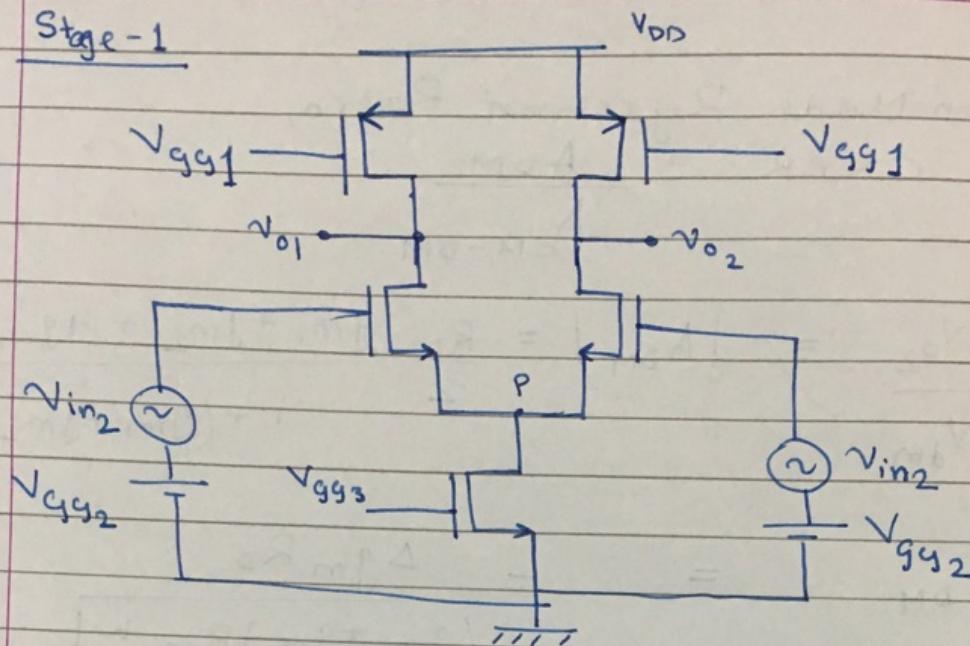
Replace PMOS by,



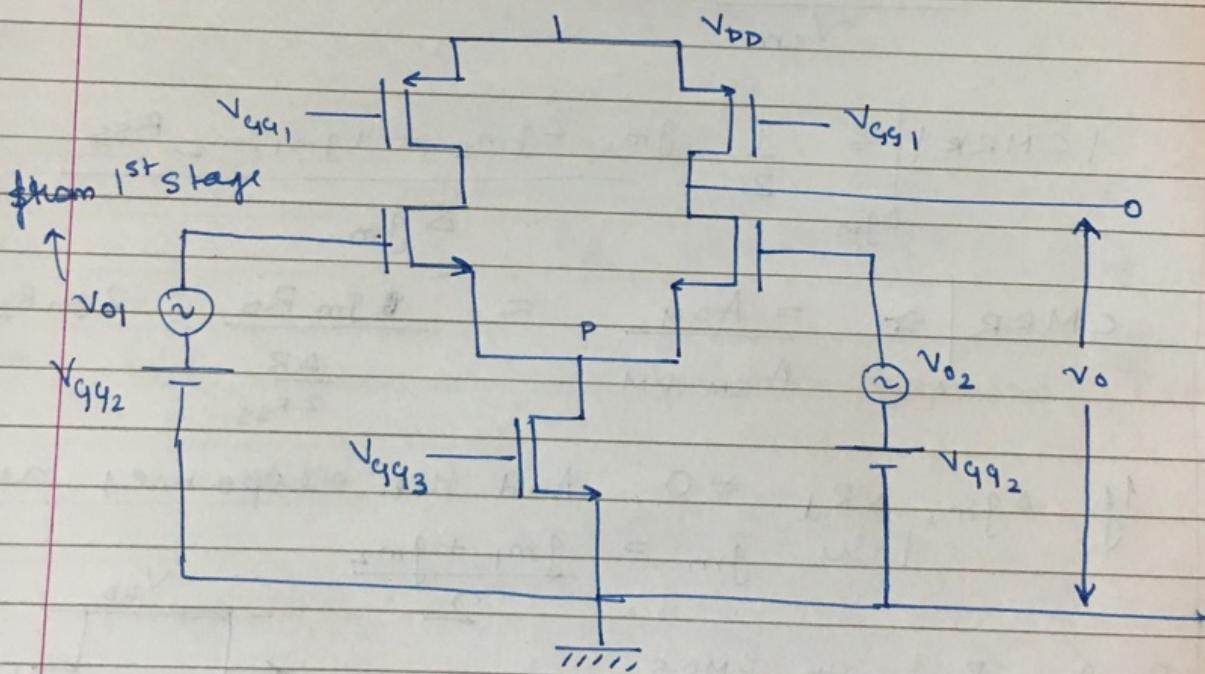
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Stage - 1



Stage 2



Where,

$$V_{o1} = \frac{g_m R_D}{2} (V_{in1} - V_{in2})$$

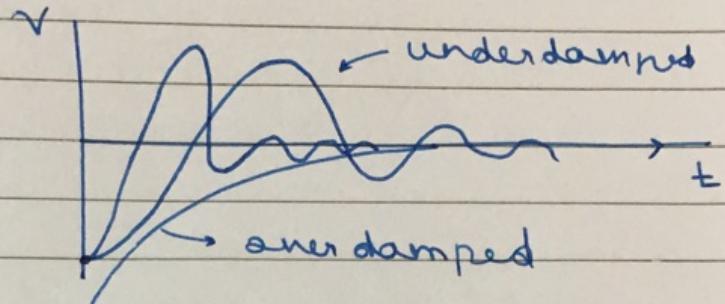
$$V_{o2} = \frac{g_m R_D}{2} (V_{in1} - V_{in2})$$

→ Symbolic Python lib. (sympy)

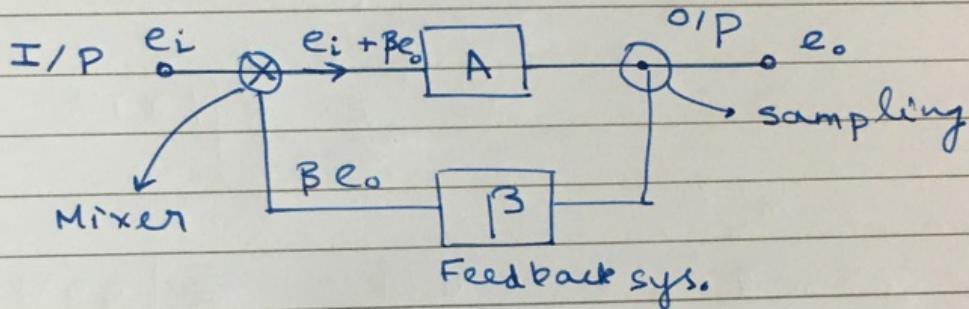
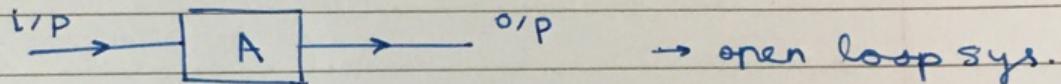
Methods:

`Symbol('x')`, `series(sin^2('x') + cos('x'))`
jupyter → sh -h ≡ shift + tab

→ Feedback



- No feedback = open loop system



→ OL sys : → A

$$e_o = (e_i + \beta e_o) A$$

$$\Rightarrow e_o [1 - A\beta] = A e_i$$

$$\frac{e_o}{e_i} = \boxed{\frac{A}{1 - A\beta}}$$

closed
loop sys.