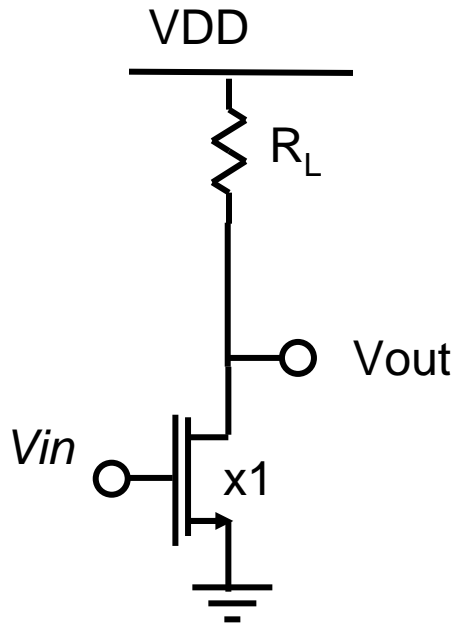


Lectures 11+12

Much ado about current mirrors

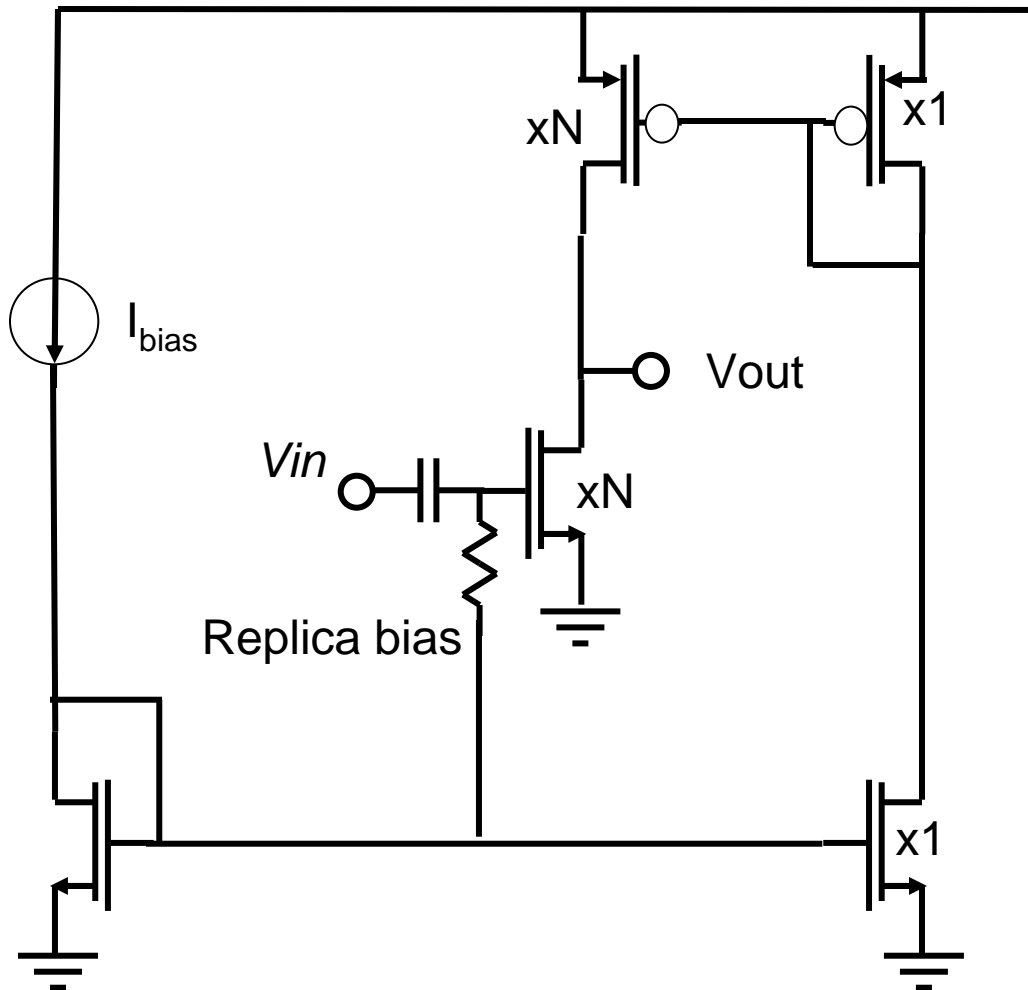
Biasing limits gain for resistive loads



- R sets gain:
 - $A_v = g_m R_L$
 - $g_m = 2I_D / V_{od}$
 - $A_v = 2I_D R_L / V_{od}$
- But for saturation:
 - $I_D R_L < V_{DD} - V_{od}$
 - So $A_v < 2V_{DD} / V_{od} - 2$
- Example:
 - $V_{DD} = 2.4V$
 - $V_{od} = 0.2V$
 - $A_v < 22$

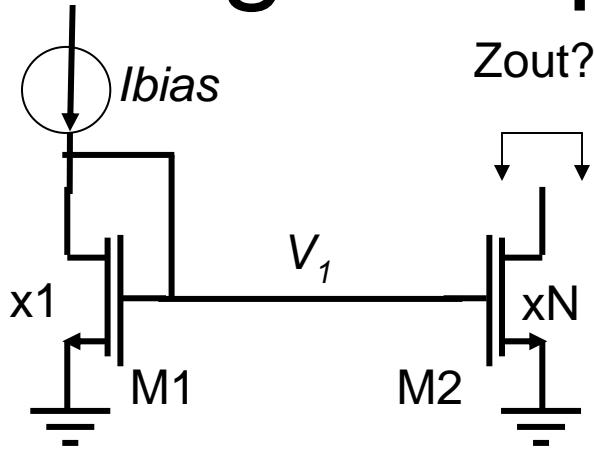
Using Current mirrors as active sources

VDD



- Replica bias sets $I_d = N \cdot I_{\text{bias}}$
- PMOS mirror set load
- $A_v = g_{m_n}(r_{\text{on}} || r_{\text{op}})$
- Bias: $V_{\text{odn}} + V_{\text{odp}} < V_{\text{DD}}$
- This is much easier to make high gain!
- Implies that mirror output impedance matters.

High-frequency analysis: Zout



- Start small signal

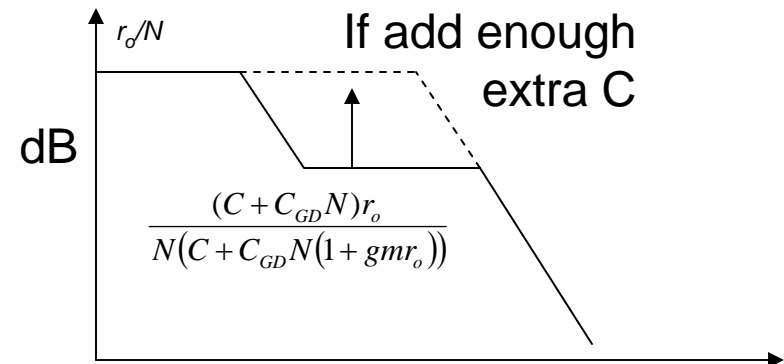
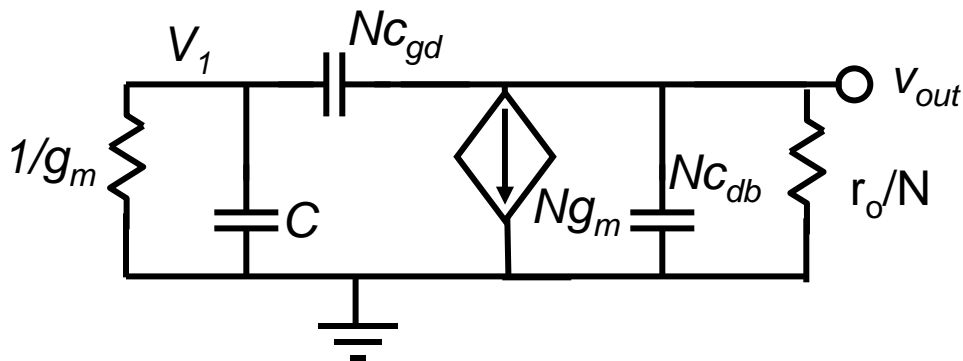
$$C = (1 + N)C_{GS} + C_{DB}$$

$$i_{out} = Nv_{out} \left(\frac{1}{r_o} + j\omega C_{DB} \right) + j\omega C_{GD} N(v_{out} - v_1) + v_1 N g_m$$

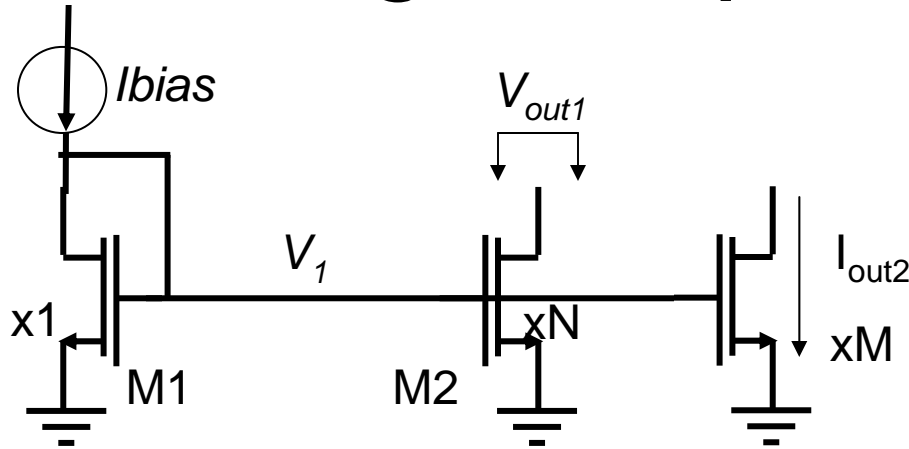
$$v_1 = v_{out} \frac{j\omega C_{GD} N}{g_m + j\omega(C + C_{GD} N)}$$

$$i_{out} = Nv_{out} \left(\frac{1}{r_o} + j\omega C_{DB} + j\omega C_{GD} + \frac{j\omega C_{GD} N(gm - j\omega C_{GD})}{gm + j\omega(C + C_{GD} N)} \right)$$

$$Z_{out} \approx \frac{gmr_o + j\omega(C + C_{GD} N)r_o}{N(gm + j\omega(C + C_{GD} N(1 + gmr_o))) - \omega^2(C + C_{GD} N)(C_{DB} + C_{GD})r_o}$$



High-frequency analysis

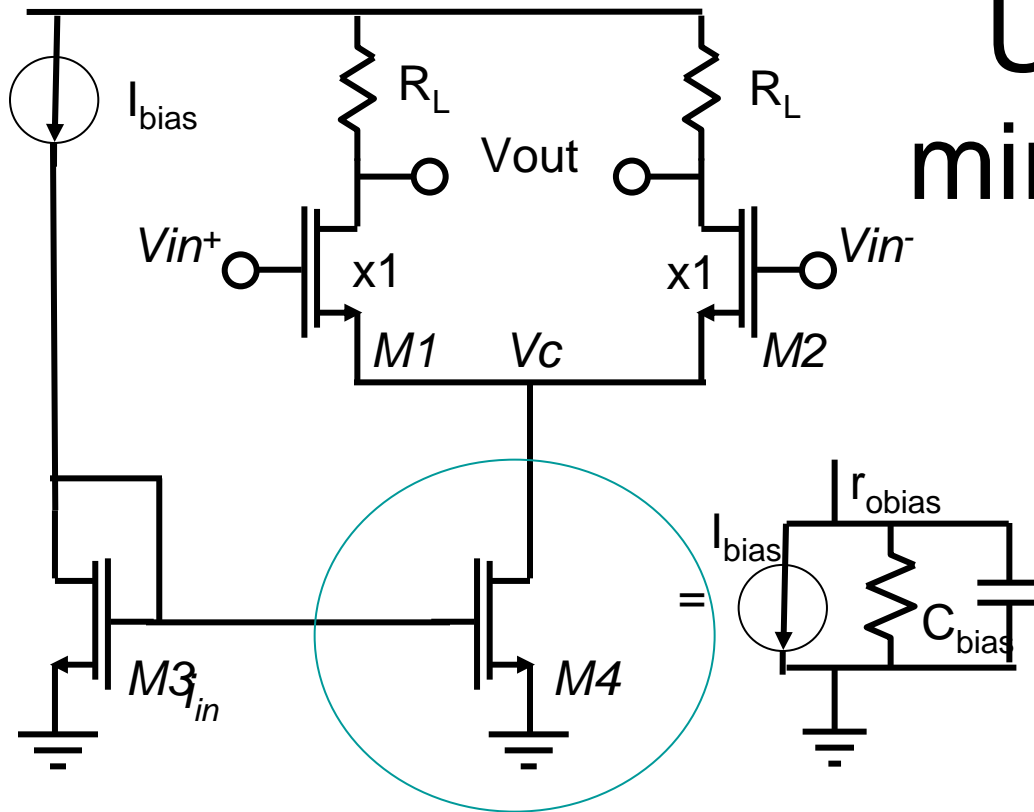


- Start small signal
- Can also get cross-talk
- All much better if add explicit Capacitor on gate
- Or if cascode everything (now need capacitor on cascode node)

$$v_1 = v_{out1} \frac{j\omega C_{GD} N}{gm + j\omega(C + C_{GD} N)}$$

$$I_{out2} = Mgm \cdot v_1 = v_{out1} \frac{j\omega C_{GD} NM}{gm + j\omega(C + C_{GD} N)}$$

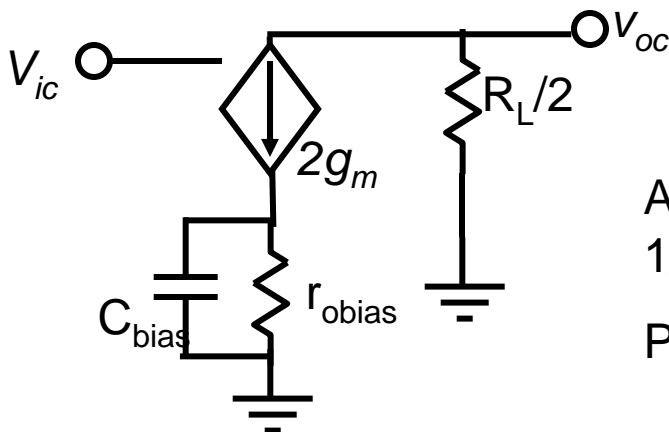
VDD



Using Current mirrors as biases

- This implies $V_c > V_{OD4}$
 - In general need to maintain headroom
- No effect on differential behavior:
 - Same half-circuit
- Affects common mode, however
 - Small signal
 - Low F:

$$V_{oc} = V_{ic}(g_m R_L / (1 + 2g_m r_{obias}))$$
 - $A_{VCM} = V_{oc}/V_{ic} \sim R_L / 2r_{obias}$
 - High F: replace r_{obias} with general Z_c including r_o , C_{db} , and C_{sb} of differential pair.



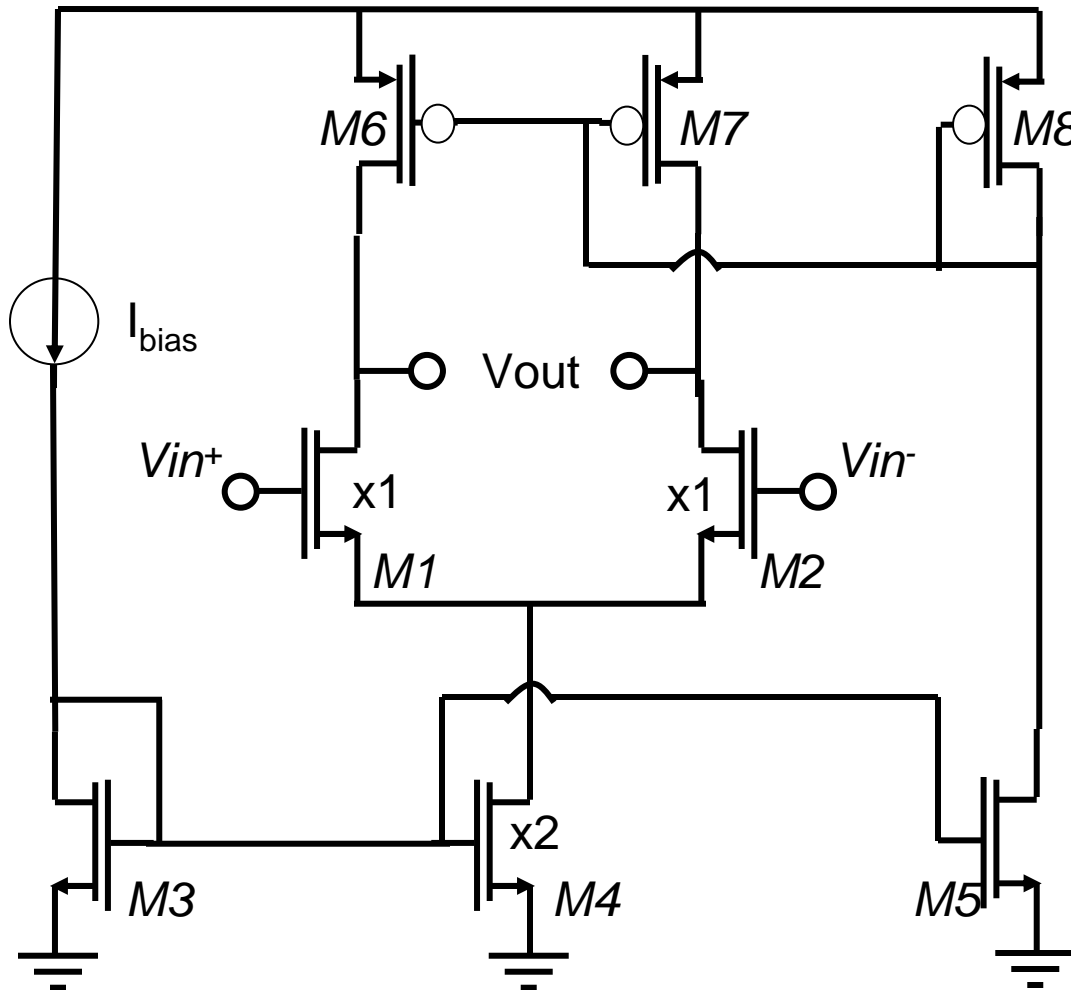
ACM: zero at $1/(r_{obias} C_{bias})$
 Pole at $2g_m/C_{bias}$

$$\frac{V_{oc}}{V_{ic}} = \frac{g_m R_L (1 + j\omega r_{obias} C_{bias})}{1 + 2g_m r_{obias} + j\omega r_{obias} C_{bias}}$$

$$C_{bias} = C_{db4} + C_{sb1} + C_{sb2}$$

Using Current mirrors as active sources

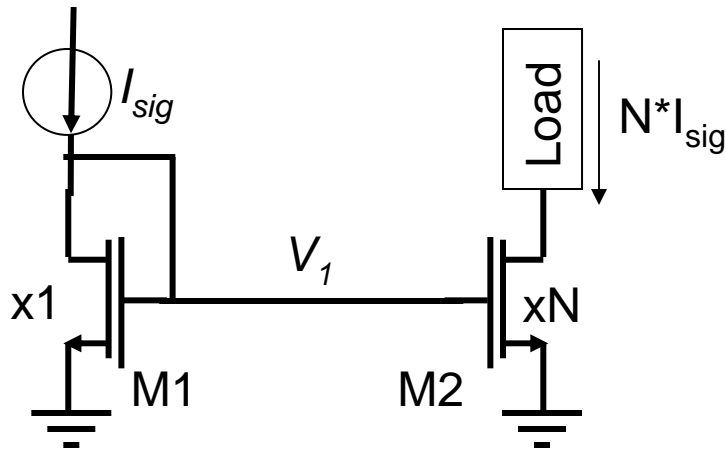
VDD



- Use extra mirror (M6-M8) to source the current sunk by the diff. pair
- M6 and M7 act as load resistors ($R_L = r_{on} || r_{op}$)
- $A_{vdm} = g_{m_n}(r_{on} || r_{op})$
- $A_{vcm} \sim (r_{on} || r_{op})/r_{obias}$

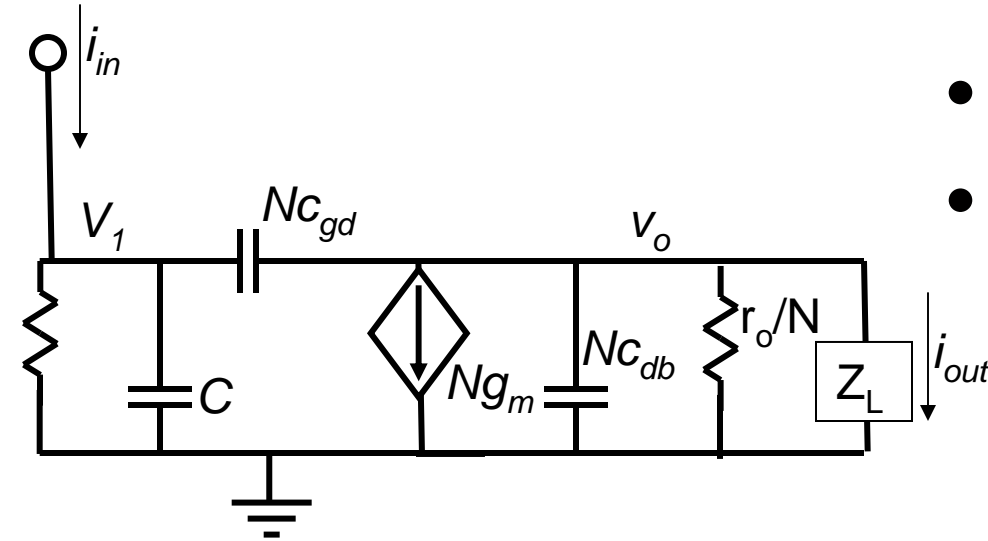
This is a sort of differential op amp (high gain)

Active mirror



- Mirror acts as current amplifier
 - Excellent dynamic range
 - Well defined gain (N)
 - Requires input always have one sign
- Can think of as:
 - a Common source amp
 - preceded by a pre-distorting load (the diode-connected device)

Active mirror: HF



- Small signal model

- Model CS:

$$\frac{V_o}{V_1} = -Ng_m \left(Z_L \parallel \frac{r_o}{N} \right) \frac{1 - j\omega/z_{trans}}{1 + j\omega/p_{out}}$$

– where

$$p_{out} = \frac{1}{\left(R_L \parallel \frac{r_o}{N} \right) (C_L + Nc_{db} + Nc_{gd})} \quad z_{trans} = \frac{gm}{C_{gd}}$$

- Model input impedance

$$\frac{V_1}{i_{in}} = \frac{1}{g_m + j\omega C + j\omega Nc_{gd}(1-A)} = \frac{1}{g_m} \frac{1}{1 + j\omega/p_{in}}$$

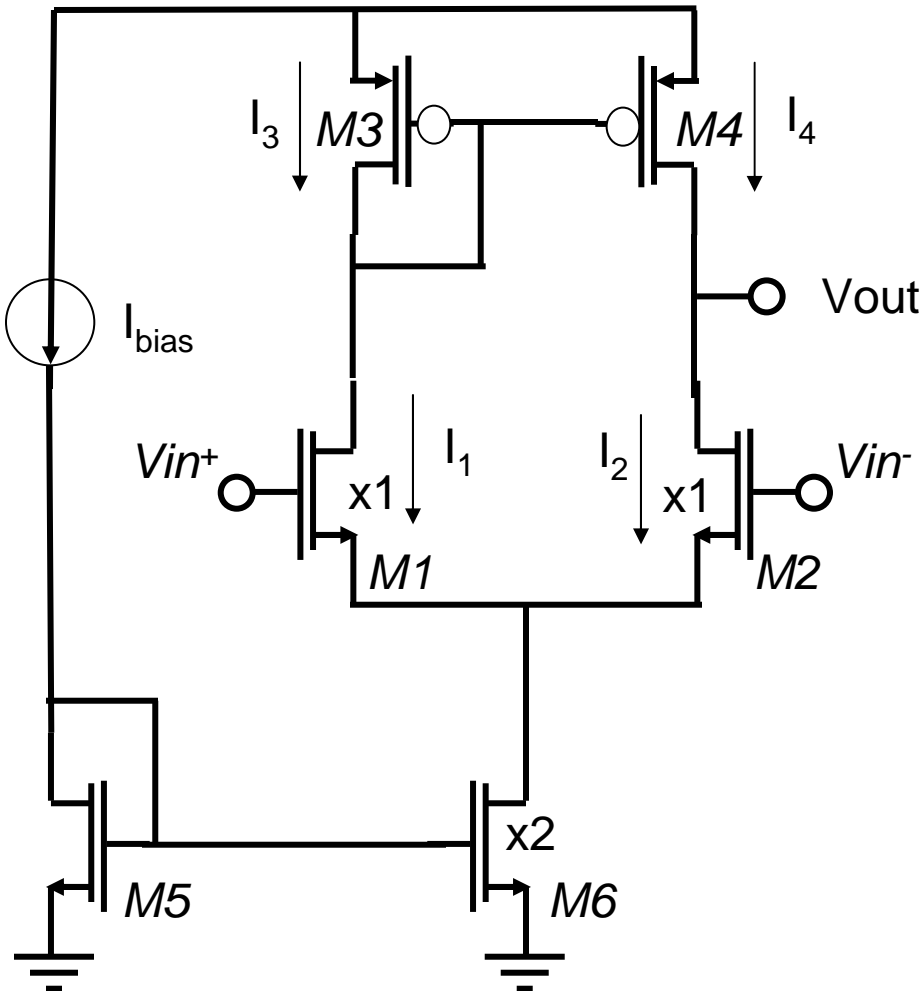
$$p_{in} = \frac{g_m}{C + Nc_{gd}(1-A)}$$

- Finally: $\frac{i_{out}}{V_o} = \frac{1}{Z_L}$

$$\frac{i_{out}}{i_{in}} = \frac{i_{out}}{V_o} \frac{V_o}{V_1} \frac{V_1}{i_{in}} = \frac{1}{Z_L} \left(-Ng_m \left(Z_L \parallel \frac{r_o}{N} \right) \frac{1 - j\omega/z_{trans}}{1 + j\omega/p_{out}} \right) \frac{1}{g_m} \frac{1}{1 + j\omega/p_{in}} = -N \frac{1 - j\omega/z_{trans}}{(1 + j\omega/p_{out})(1 + j\omega/p_{in})} \frac{r_o}{NZ_L + r_o}$$

Simple op-amp

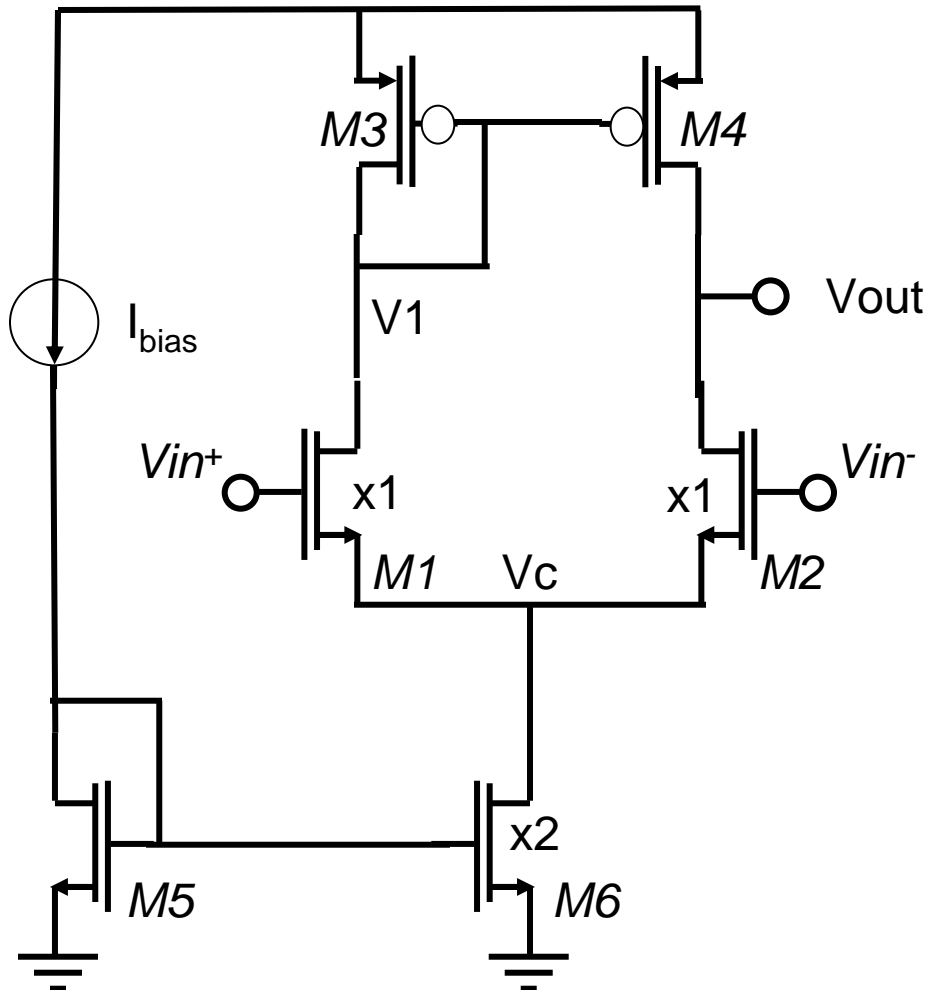
VDD



- Uses PFET mirror to:
 - combine differential outputs as single-ended
 - $I_1 = I_{\text{bias}} + g_{\text{mn}}(V_{\text{in}}^+ - V_{\text{in}}^-)/2$
 - $I_2 = I_{\text{bias}} - g_{\text{mn}}(V_{\text{in}}^+ - V_{\text{in}}^-)/2$
 - $I_3 = I_1$ (KCL)
 - $I_4 = I_3$ (mirror)
- $I_{\text{out}} = I_4 - I_2$
 - $I_{\text{out}} = g_{\text{mn}}(V_{\text{in}}^+ - V_{\text{in}}^-)$
- $R_{\text{out}} = (r_{\text{on}} \parallel r_{\text{op}})$
- $A_{\text{vdm}} = g_{\text{mn}}(r_{\text{on}} \parallel r_{\text{op}})$

Simple op-amp

VDD



- Claim:
 - $R_{out} = (r_{on} || r_{op})$

- Check:
 - Vary V_{out} ,
 - set $V_{in}^+ - V_{in}^- = 0$,
 - check I_{out}

$$I_2 = -gmV_c + \frac{V_{out} - V_c}{r_{on}}$$

$$I_1 = -gmV_c + \frac{V_1 - V_c}{r_{on}} = \frac{V_c}{r_{ob}} - I_2$$

$$|V_{out}| \gg |V_1|, gm \gg \frac{1}{r_{ob}}, \frac{1}{r_{on}}$$

$$V_c \approx \frac{V_{out}}{2gm r_{on}}, I_2 = \frac{V_{out}}{2r_{on}} = -I_1 = -I_3$$

$$I_4 = -\frac{V_{out}}{r_{op}} + I_3 = -\frac{V_{out}}{r_{op}} - \frac{V_{out}}{2r_{on}}$$

$$I_{out} = I_4 - I_2 = -\frac{V_{out}}{r_{op}} - \frac{V_{out}}{r_{on}} = -\frac{V_{out}}{r_{on} || r_{op}}$$