

Wadhwani Electronics Lab Dept. of Electrical Engineering

Analog Circuits Lab (Code: EE-230)

IIT Bombay February 20, 2018

Design Exercise

<u>Name:</u> Roll Number: Time: 30 min.

— Part-1: Integrator Design Exercise —

1 Basic Switch Capacitor Circuit

- 1. The transfer function $(\frac{V_{out}}{V_{in}})$ of the circuit shown in Fig.(1) is
- 2. The cut-off frequency of the transfer function derived in Q1 is
- 4. For the circuit in Fig.(2), the ratio $\frac{C_1}{C_2}$ of 1 kHz cut-off frequency is
- 5. Choosing C_2 to be 0.1 μF , the value of C_1 based on the answer of Q4 will be
- 6. Based on the answers of Q1-5 the value of discrete resistor R will be

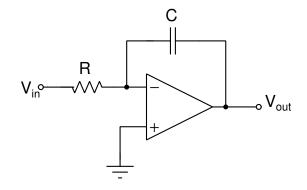


Figure 1: RC Integrator

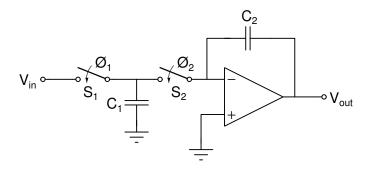


Figure 2: Switch Cap Integrator

2 Design Specifications

- Filter Type: Butterworth Low Pass Filter
- Center Frequency (3-dB Frequency for LPF): $f_0 = 1 \text{ kHz}$
- Stop Band Frequency: $f_s = 3.5 \text{ kHz}$
- Pass Band Gain: $H_0 = 1 \text{ dB}$
- Maximum Allowable Pass Band Rillple: $A_{max} = 1 \text{ dB}$
- Stop Band Attenuation: $A_{min} = 40 \text{ dB}$
- Clock Frequency: $f_{clk} = 100 \text{ kHz}$
- Mode of Operation for LMF100CCN = 3

3 Questions

Refer to the datasheet of the IC LMF100CCN (click on the IC name) used in Mode-3 of Low Pass Filter configuration.

- 7. Using the method explained in Section: 2, filter order for the specifications given in Section: 3 is
- 8. Assuming that the desired dc-gain is to be obtained from the first stage only, answer the following:
 - Q for the two stages (Stage-A and Stage-B) are: $Q_A = \dots$ and $Q_B = \dots$

 - \bullet Normalized DC Gain value of the two stages (Stage-A and Stage-B) are $H_{0_A}=......$ and $H_{0_B}=.......$
- 9. The DC Gain (H_{0_A}) of stage -A (or B) in terms of resistances R_{1_A} and R_{4_A} is (*Hint*: Refer to the datasheet)

If we choose R_1 for both the stages to be 18 k Ω . Then:

- $R_{2_A} = \dots$ and $R_{2_B} = \dots$
- $R_{3_A} = \dots$ and $R_{3_B} = \dots$
- $R_{4_A} = \dots$ and $R_{4_B} = \dots$

4 Using Nomographs and Tables

The nomographs and tables are the most helpful tools that can be used for the hand design of filters. Nomographs are used to find the order of the filter based on desired specifications and the table is used to find the necessary parameters such as center frequencies and Quality Factor(Q) of a low-pass equivalent. The nomographs and tables are given in Fig.(4) and Fig.(5)

To use a nomograph A_{max} , A_{min} and the ratio f_s/f_c for low-pass must defined by the designer. We can calculate the filter order required by drawing a straight line(1) joining the desired value of A_{max} and A_{min} to the y-axis of the nomograph. Then draw a line vertical (2) from the desired f_s/f_c , cutting the order curves at point A and B. Finally, draw a horizontal line (3) from the intersection of line-1 and y-axis points. The intersection of line(1) and line(3) on the curves will give the filter order based on the desired specifications. This process is elaborated in Fig.(3) where the order of the filter turns out to be n_2 .

The table is given in Fig.(5) is specified for a normalized frequency of 1Hz. To obtain the necessary filter design data with a different cut-off/center frequency, simply multiply all of center frequency values given in the table by the desired f_c .

Remark: To read nomographs either you can use adobe reader or bring a printout of it, no printout of the nomographs will be provided during the lab.

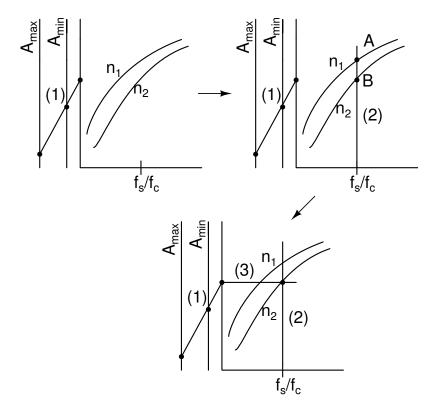


Figure 3: Example of Nomograph Use

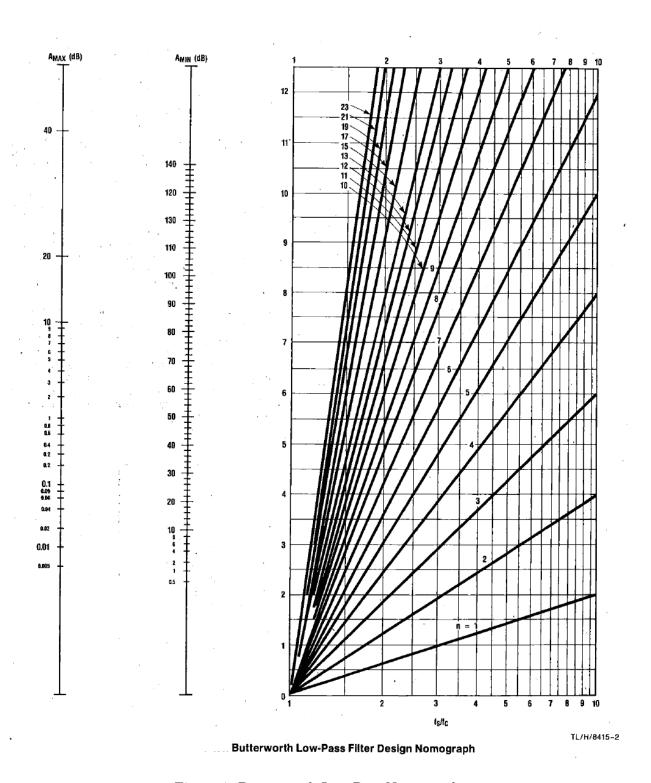


Figure 4: Butterworth Low Pass Nomograph

Butterworth Design Table for 1 Hz -3 dB Frequency

Order N	$f_{-3dB}=1Hz$										Att (dB)
	F1	Q1	- F2	Q2	F3	Q3,	F4	Q4	F5	Q5	2Fc
. 1	1									1	9
2	1 1	0.707				'	ŀ				15
3	1	1.000	1 1						! .		21
4 ·	1	0.541	1 1	1.306							27
5	. 1	0.618	1	1.620	· 1	!					33
6	1	0.518	1	0.707	1 1	1.932			,		39
7	1	0.555	1	0.802	1	2.247	1 1				45
8	1	0.510	1	0.601	1	0.900	l i	2.563		1	51
9	1	0.532	1	0.653	1	1.000	1	2.879	1 1		57
10	1	0.506	ì	0.561	1	0.707	1	1.101	1	3.196	63

Figure 5: Normalised Butterworth Low Pass Design Table