Sample circuit schematic

Length displayed
(by default it will not be shown)

l = 0.18

V0
vdc=0

V18
vdc=1.8

gnd

1.8

M8

900m

0

w=10

0

V17
vdc=Vg

gnd

gnd

Sample plot

Name   Vis
Id

1.05184V 57.6653u

761.631mV 26.8022u

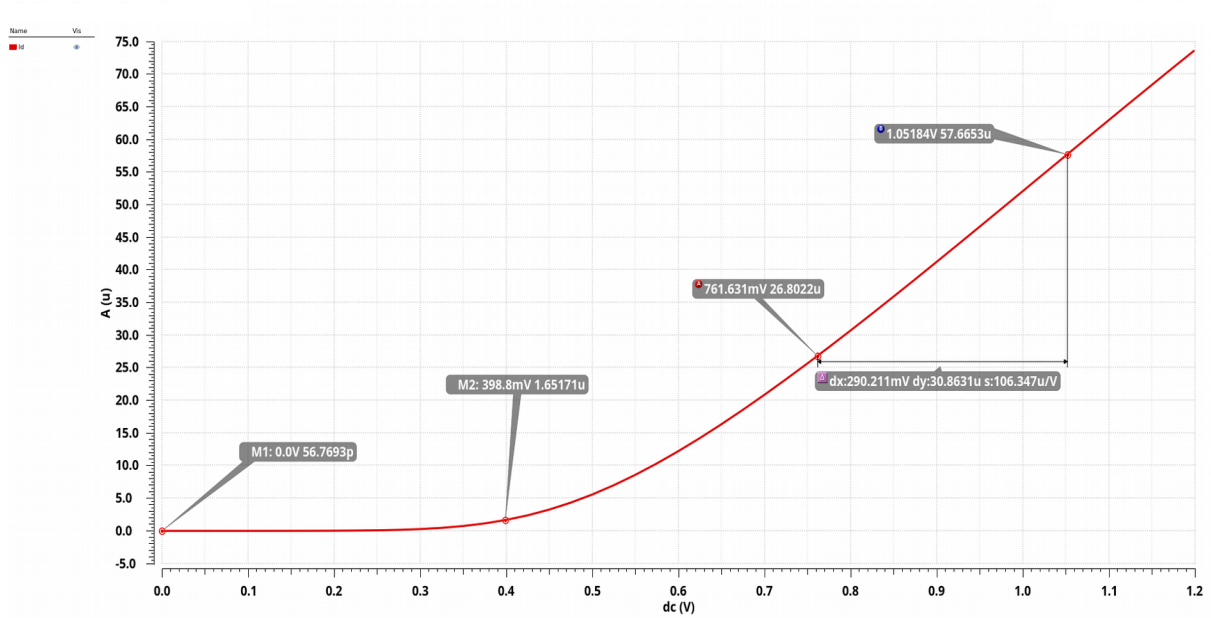M2: 398.8mV 1.65171u

dx:290.211mV dy:30.8631u s:106.347u/V

M1: 0.0V 56.7693p

A (u)

dc (V)

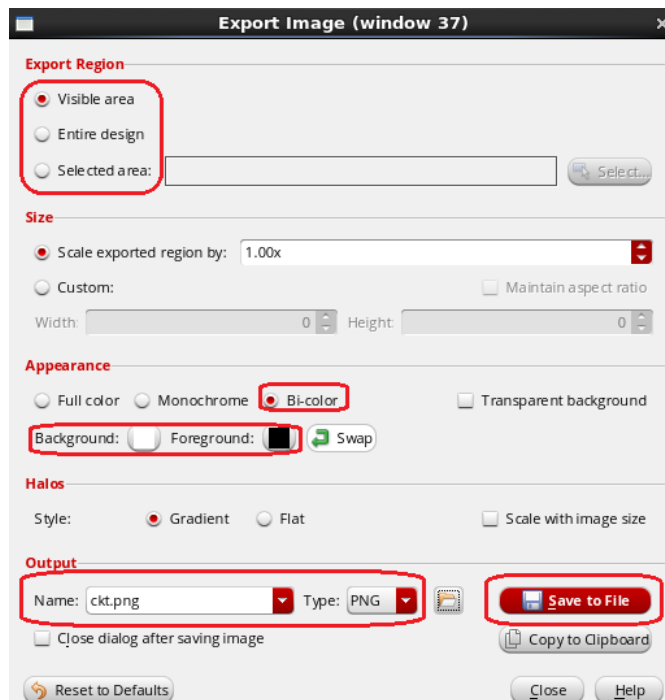**Save your schematics and plots in the same manner as shown above. So that it looks in an understandable way. Instructions to save them are given below.**

For saving the schematic:

In the **circuit schematic window**, **File -> Export Image**. An "Export Image" dialog box opens as shown below.


Fill out/choose the options circled in **"red colour"**. Before saving the circuit, zoom in to fit the circuit within the visible area. Please note that gate length doesn't get displayed by default in the schematic. Be sure to display it, by choosing it in the transistor properties dialog box (shown below).
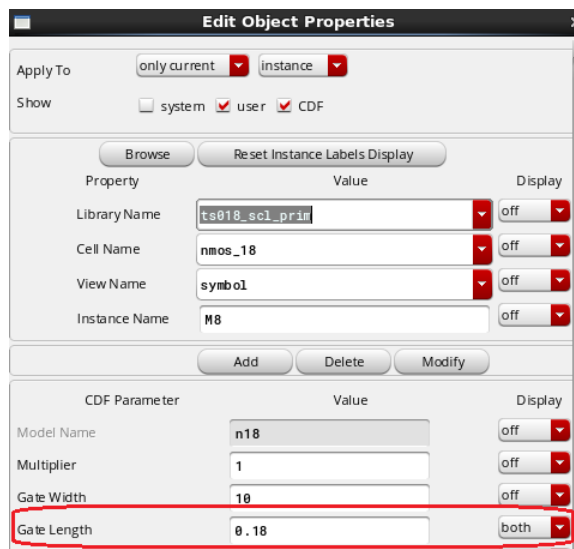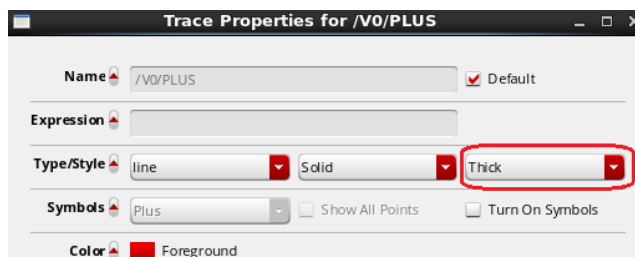
For saving the plot:

When the **plot window** opens after simulation, go to **Edit -> Properties**. "Graph properties" dialog box will be shown as below.
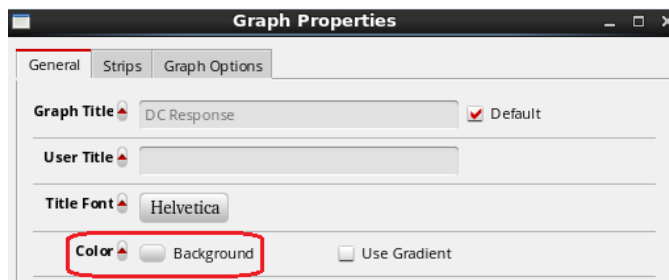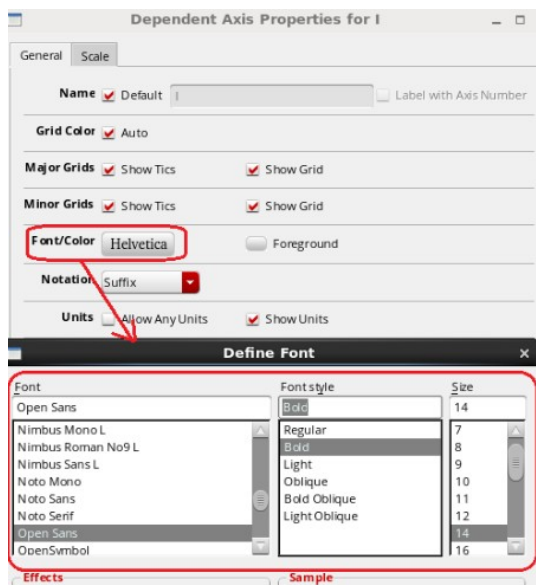

Change the colour of the background to white.

To change the axis/waveform properties double click on them. Their corresponding properties dialog box opens up.

Sample waveform properties dialog box

Sample axis properties dialog box

For saving the plot go to **File -> Save Image** in the waveform window. Choose the folder where you want to save.