

- Above is the formal procedure for combining 2 graphs (compared to the graph combining we did by hand calculation).
- Reachable states till now =

$\textcircled{00} \rightarrow \textcircled{11}$   
i.e.  $\overline{s_2} \overline{s_1} \overline{s_0} + \overline{s_2} s_1 \overline{s_0} = \text{new state}$   
generated the same states  
generated new states

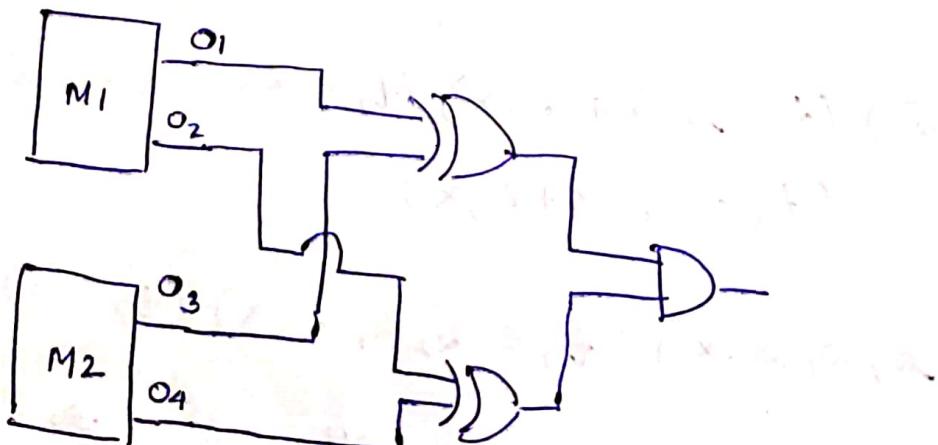
$$\begin{aligned} T &= (\overline{s_2} \overline{s_1} \overline{s_0} + \overline{s_2} s_1 \overline{s_0}) R(x, s, t) \\ &= (t_1 = \overline{s_2} s_1 \overline{s_0} \bar{x} + \dots) (t_2 = \overline{s_2} s_1 \overline{s_0} \bar{x} + \dots) \\ &\quad (t_3 = \overline{s_2} s_1 \overline{s_0} x) \end{aligned}$$

$$\begin{aligned} \text{at } x=0 : T &= (t_1 = 1) (t_2 = 1) (t_3 = 1) \Rightarrow 11 \\ x=1 : T &= (t_1 = 0) (t_2 = 0) (t_3 = 1) \Rightarrow 02 \end{aligned}$$

## SA/SD LOGIC SYNTHESIS

### Representation

- Truth table
  - Minterms
  - Maxterms
  - ROBDD
- } Cube pat



- Need to op
  - area
  - perform
  - power
  - testable

- Scriby
  - SAT
  - BDD
- Test

- Toolbox
  - req

- K-N

- Need to optimize for
  - area
  - performance
  - power
  - testability
- Using RUBDD
  - min# of nodes req. to implement logic
  - less max's
  - { Need to find correct order of variables? }
- Specify using
  - SAT
  - BDD
- Test (how to do that?)
- Biffling:
  - requires multilevel circuit
  - ↓
  - may not be performance optimal
- K-Map: good for human visualisation  
however inefficient for computers
  - textual way better

a	b	c	/ z
0	0	0	1
0	0	1	1
0	1	0	1
0	1	1	1
1	0	0	
1	0	1	
1	1	0	
1	1	1	

doesn't affect the output  
(automatically comes adjacent in K-Map and hence combined)  
(similar thing happens in BDD where 2 edges going to same output are combined)

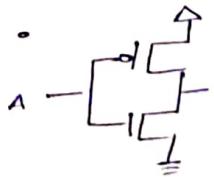
→ cube/implicants

→ variables = literal  
(incomplement or uncomplement form)

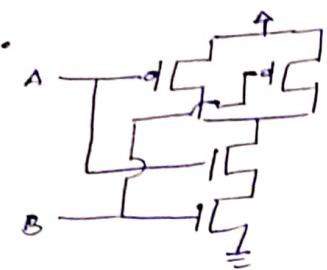
$$\text{Ex: } \overline{a}\overline{b}\overline{c} + \overline{a}\overline{b}c + \overline{a}b\overline{c} + \overline{a}bc = F$$

3 AND

AND



1 input = 2 gates  
= 2x area



2 input = 4 gates  
= 2x area

• 111<sup>st</sup> 3 input NOR requires 6x area

⇒ Area is proportional to number of literals  
⇒ to minimize area need to minimize literals

- Performance ⇒ minimise delay of path
  - ⇒ no. of gates in critical path
  - determine delay of ckt
  - ⇒ delay of gate depends on no. of inputs

- If  $s$  is delay of 1 inverter then delay of

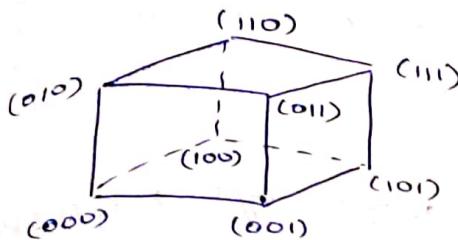
$$F(3 \text{ stages}) = S + 3S + 4S$$

$\hookrightarrow$  scaled up by the amount of fanin of gate having max<sup>m</sup> literal, in that stage

→ Thus, to improve performance and reduce area we need to reduce the number of literals

- Bigger gates
- K-Map
- Quine-

bigger gates dissipate more power

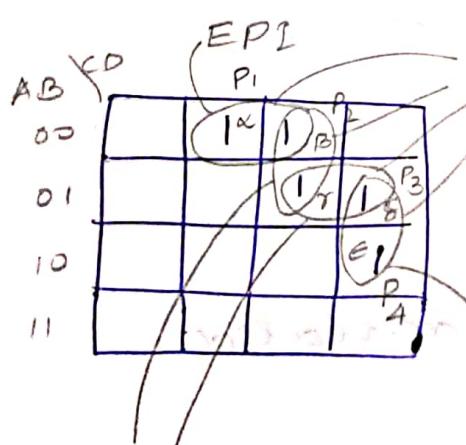


- K-Map is not scalable
- Quinn-McCluskey method is scalable

a	b	c	
0	0	0	
0	0	1	} 00-
0	1	0	} 01-
0	1	1	} 01-
1	0	0	} 10-
1	0	1	} 10-
1	1	0	} 11-
1	1	1	} 11-

→ Tabular Method

The largest size implicant we can get by merging implicants = prime implicant



→ helps to minimize ckt  
Prime Implicants (PI)  
- however there are redundant PI's

Essential Prime Implicant (EPI)  
(Since it is the only PI that covers  $A\bar{B}CD$ )

Selective Prime Implicant (SPI)

- From selective prime Implicants we need to choose one
  - do so by forming a set covering problem
  - or use logical solution

	$P_1$	$P_2$	$P_3$	$P_4$	
$\alpha$	✓	✗			→ covered by $L \Rightarrow P_1$ is EP <sub>1</sub>
$\beta$	✓	✓	✗		
$\gamma$		✓	✓		
$\delta$			✓	✓	
$\epsilon$				✓	→ covered by $L \Rightarrow P_4$ is EP <sub>1</sub>

- Patrick Method (Logical Method)

$$\alpha = P_1$$

$$\beta = P_1 + P_2$$

$$\gamma = P_2 + P_3$$

$$\delta = P_3 + P_4$$

$$\epsilon = P_4$$

⇒ each minterm should be covered

$$P_1 \cdot (P_1 + P_2) \cdot (P_2 + P_3) \cdot (P_3 + P_4) \cdot P_4 = L$$

logical solution that gives  $P_1 = P_2 = P_3 = P_4 = 1$

however one solution of this is  $P_1 = P_2 = P_3 = P_4 = 1$

hence convert it into SOP and choose the term which has least number of literals

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- This problem can be translated in matrix problem

$$Ax = b$$

Minterms:  $\alpha, \beta, \gamma, \delta, \epsilon$

up we need

covering

$P_1 \rightarrow P_1 \text{ is } EP_1$

$$P_1 \quad P_2 \quad P_3 \quad P_4$$

$$\begin{matrix} x \\ p \\ r \\ s \\ t \\ e \end{matrix} \left[ \begin{array}{cccc} 1 & 0 & 0 & 0 \\ 1 & 1 & 0 & 0 \\ 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 1 \\ 0 & 0 & 0 & 1 \end{array} \right] \begin{matrix} x_1 \\ x_2 \\ x_3 \\ x_4 \end{matrix} = \begin{matrix} b_1 \\ b_2 \\ b_3 \\ b_4 \end{matrix}$$

$A$

$$b_i \geq 1$$

if suppose one solution is  
then  $b = [1101]$

$$\begin{bmatrix} 1 \\ 2 \\ 1 \\ 1 \end{bmatrix} \rightarrow \text{covered twice}$$

Need to find  $x$  such that we get  
minimum no. of ones.

- If we minimize for

Area = no. of literals

Perf = no. of literals + product terms

Use ROBDD to find  $x$

↳ gives n-level ckt

→ to get a two level ckt

(path1) + (path2) + ...

↳ a path that evaluates to 1

- Shannon's Theorem

$$f(u_1, u_2, \dots, u_n) = u_i f_{x_i} + \bar{u}_i f_{\bar{x}_i}$$

$$= u_i f_{x_i} \oplus \bar{u}_i f_{\bar{x}_i}$$

Boolean Difference

$$\frac{\partial f}{\partial x} = f_{x_i} \oplus f_{\bar{x}_i}$$

- if they are different then evaluate to s  
↳ that  $f$  is dependent on  $x_i$
- if they come out to be same then  $f$  is independent of  $x_i$

$$i) \frac{\partial f}{\partial x_i} = \frac{\partial f}{\partial \bar{x}_i}$$

$$ii) \frac{\partial f}{\partial x_i} = \frac{\partial f}{\partial \bar{x}_i}$$

$$iii) \frac{\partial}{\partial x_i} (f \oplus g) = \frac{\partial f}{\partial x_i} \oplus \frac{\partial g}{\partial x_i}$$

$$iv) \frac{\partial}{\partial x_i} (f + g) = \bar{f} \frac{\partial g}{\partial x_i} + \bar{g} \frac{\partial f}{\partial x_i} + \frac{\partial f}{\partial x_i} \frac{\partial g}{\partial x_i}$$

$$v) \frac{\partial}{\partial x_i} (f \cdot g) = f \cdot \frac{\partial g}{\partial x_i} + g \frac{\partial f}{\partial x_i} + \frac{\partial f}{\partial x_i} \frac{\partial g}{\partial x_i}$$

- Consensus

$$= f_{x_i} \cdot f_{\bar{x}_i}$$

Smoothing

$$= f_{x_i} + f_{\bar{x}_i} \rightarrow \text{minterms independent of } x_i$$

- we can decompose a function w.r.t. orthogonal functions rather than the literals (literals are always orthogonal)

For  $\Phi_i$  &  $\Phi_j$ ,  $\Phi_i \cdot \Phi_j = 0$

$$\Rightarrow f = \Phi_i f_{\Phi_i} + \Phi_j f_{\Phi_j}$$

- for  $f = ab + bc + ac$

$$\Phi_1 = ab$$

$$\Phi_2 = \bar{a} + \bar{b} \quad \left. \right\} \Phi_1 \cdot \Phi_2 = 0$$

$$f = ab \cdot f_{ab} + (\bar{a} + b) f_{(\bar{a} + b)}$$

↓

1

need to make this  
possible for computer to compute

Positional vector: any function can be  
expressed using these vectors

- any variable can be represented in 2 bits:

$$\begin{array}{l} 00 \rightarrow \phi \\ 01 \rightarrow 1 \\ 10 \rightarrow 0 \\ 11 \rightarrow x \end{array}$$

don't care

a

b

c

①	01	01	01	→ for ab
②	11	01	11	→ for bc
③	01	11	01	→ for ca
④	10	11	11	→ $\bar{a}$

bit-wise operation

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## LOGIC OPTIMISATION

exact optimisation

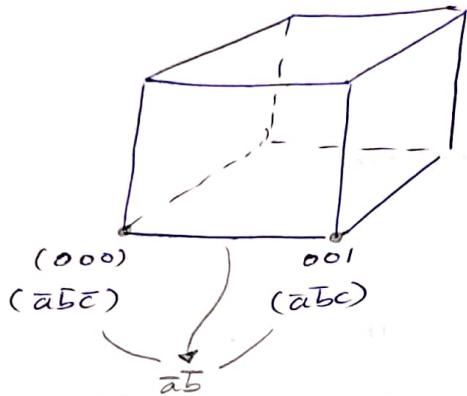
i) Enumerate PI → use QM

ii) Solve a set covering problem to find  
the minimum PI

Containment:  $P_1$  shouldn't be covered in  $P_2$

Heuristic way to optimize  
→ don't explicitly enumerate  
Espresso, Espresso-exact, Espresso-heuristic

- i) Expansion
- ii) Reduction
- iii) Reshape
- iv) Redundancy check



$$\bar{a}\bar{b}\bar{c} + \bar{a}\bar{b}c = \bar{a}\bar{b}(\bar{c} + c) = \bar{a}\bar{b} : \text{Expansion}$$

(= reduction of literals)

expansion of area covered

Expansion: reduce implicants to prime implicants

$$\bar{a}\bar{b} = \bar{a}\bar{b}(\bar{c} + c) = \bar{a}\bar{b}\bar{c} + \bar{a}\bar{b}c : \text{Reduction}$$

Reduction: size of cube reduced  
(no. of literals reduced)  
Increase

→ reduction is important if we want  
to explore other possibility of expansion  
↳ 'Reshape'

- heuristic

while doing redeveloping recursively, need to check for redundancy.

positional representation

$$ab + \bar{a}c + \bar{a} = f(a, b, c)$$

a	b	c
01	01	11
10	11	01
10	11	11

$$\begin{aligned}f(a, b, c) &= af_a + \bar{a}f_{\bar{a}} \\&= ab + \bar{a} \\f_a &= b, \quad f_{\bar{a}} = 1\end{aligned}$$

- to find  $f_a$  we intersect  $\bar{a}$  (10 11 11) with each term of  $f$ . If it comes out to be  $\emptyset$ , then that term not present in  $f_a$ .

⇒ with  $ab$ :  $\begin{matrix} 00 \\ 01 \end{matrix}$  10 11 11  $\Rightarrow \emptyset \Rightarrow ab$  not present in  $f_a$

$$\bar{a}c : 10 \quad 11 \quad 01$$

⇒  $\bar{a}c$  is a valid term, to get the term to be added we OR the term with ~~its~~  $\bar{a}$ 's complement

$$\begin{array}{r} 10 \quad 11 \quad 01 \\ 001 \quad 00 \quad 00 \\ \hline 101 \quad 11 \quad 01 \end{array} \Rightarrow C$$

$$\bar{a} : 10 \quad 11 \quad 11$$

$$\begin{array}{r} 10 \quad 11 \quad 11 \\ 01 \quad 00 \quad 00 \\ \hline 11 \quad 11 \quad 11 \end{array} \Rightarrow 1$$

Thus,  $f_a = c + 1 = 1 \rightarrow$  Tautology  
of evaluation by

$$f_a: a = 01 \quad 11 \quad 11$$

$$\Rightarrow ab = 01 \quad 01 \quad 11$$

$$\begin{array}{r} 01 & 01 & 11 \\ 10 & 00 & 00 \\ \hline 11 & 01 & 11 \end{array} \Rightarrow b$$

$$\bar{a}c = \begin{array}{c} 00 \\ \textcircled{00} \end{array} \quad 11 \quad 01 \\ \phi$$

$$\bar{a} = \begin{array}{c} 00 \\ \textcircled{00} \end{array} \quad 11 \quad 11 \\ \phi$$

$$\Rightarrow f_a = b$$

→ Finding Partial function w.r.t. a term

$$bc = 11 \quad 01 \quad 01$$

$$ab = 01 \quad 01 \quad 01$$

$$\begin{array}{r} 01 & 01 & 01 \\ 00 & 10 & 10 \\ \hline 01 & 11 & 11 \end{array} \Rightarrow a$$

$$\bar{a}c = 10 \quad 01 \quad 01$$

$$\begin{array}{r} 10 & 01 & 01 \\ 00 & 10 & 10 \\ \hline 10 & 11 & 11 \end{array} \Rightarrow \bar{a}$$

$$\bar{a} = 10 \quad 01 \quad 01$$

$$\begin{array}{cccc}
 & 10 & 01 & \\
 & 00 & 10 & 01 \\
 10 & & 11 & 11 \\
 \hline
 & 11 & 11 &
 \end{array}$$

$\Rightarrow f_{bc} = 1 \Rightarrow \bar{a}$   
 $\Rightarrow bc$  is contained in  $f$ .  
 $\Rightarrow$  containment  
 $f(a, bc) = ab + ac + \bar{a} = ab + ac + abc + \bar{a}$   
 $\text{due to De Morgan's theorem}$   
 $\Rightarrow$  Redundancy check

Tautology check tells us if there are redundancies present  
 $\Rightarrow$  Redundancy check

- ✓/10
- Heuristic Based minimization
- i) expand
  - ii) reduce
  - iii) reshape
  - iv) redundancy check

### Testability



- i) Single-stuck-at fault model

$\rightarrow$  only fault occurs at a time  
 $\rightarrow$  linear  $\Rightarrow$  no. of faults  $\propto$  # gates  
 $\#$  nets

### Test generation:

find a vector which can give distinguishable faulty or fault-free behaviour

- $f(x_1, x_2, \dots, x_n)$

TV for a fault S.A.O. at  $x_i$

i) algebraic method

ii) algorithmic

$f_x(x_1, x_2, \dots, x_n)$ : f having fault

$\Rightarrow f \oplus f_x$  should be distinguishable

i.e.  $f \oplus f_x = 1$

If  $\alpha = \text{S.A.O}$

$$\Rightarrow f_\alpha = f_{\bar{x}_i}$$

Now we want

$$f \oplus f_{\bar{x}_i} = 1$$

$$x_i f_{x_i} \oplus \bar{x}_i f_{\bar{x}_i} \oplus f_{\bar{x}_i} = 1$$

$$x_i f_{x_i} \oplus f_{\bar{x}_i} (\bar{x}_i \oplus 1) = 1$$

$$x_i f_{x_i} \oplus f_{\bar{x}_i} x_i = 1$$

$$x_i [f_{x_i} \oplus f_{\bar{x}_i}] = 1$$

$$\Rightarrow x_i = 1 \rightarrow \text{fault sensitizat'n}$$

$$f_{x_i} \oplus f_{\bar{x}_i} = 1$$

$\frac{\partial f}{\partial x_i} = 1$

fault excitation  
(have opposite value  
as the fault value)

fault Propagation

(whether the given input  
propagates to the P.O. and  
helps us know if fault  
indeed occurred)

Redundant fault

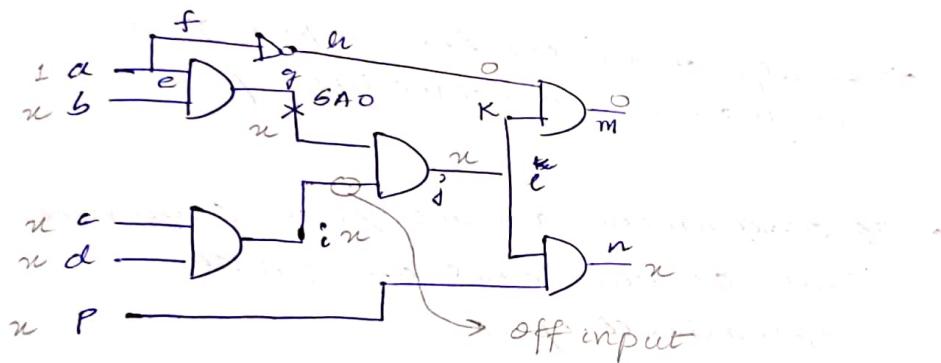
$$\begin{array}{c} \text{Fault} \\ \text{SAL} \\ \text{---} \\ \text{D}_m \\ \text{---} \\ \text{D}_m = \text{D}_o \end{array}$$

Redundant fault      Redundancy elimination

i) Regularity

ex: for a RGA : need 1 H1 & 3 FA  
but we use 4 FA for regularity

ii) Timing optimization



i) fault excitation [objective :  $g=1$ ]

ii) fault propagation

→ to excite the fault, we need to make give input at P.I.

→ traverse a path from fault to PI

→ assign a value to PI :

(Objective value)  $\oplus$  (inversion parity)  
of path

→ Next do the forward simulation

→ check if objective satisfied

→ If not, go back to the PI's  
take another path

- [make  $b=1 \rightarrow$  objective satisfied]
- check if value propagated to primary output
    - if yes, terminate
    - else, choose one of the gates from the fanout and propagate (→ objective now changes to the other input of the gate)
  - OFF INPUTS
    - ↳ give non controlling value
    - then repeat the above steps
  - finally, choose the the output gate which has a non-controlling input
  - If assignment doesn't give required result, go back and assign the complementary input  
so maintain a stack

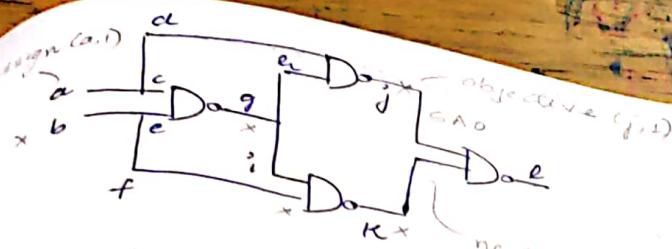
0	$e=1$
0	$d=1$
0	$c=1$
0	$b=1$
0	$a=1$

$e=0 \quad e=x$

$c=0$

## 24/10 Test Generation

- i) PODEM (Path oriented decision making)

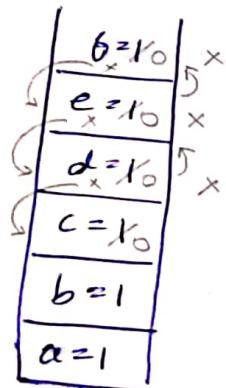


logic simulation

$y_p: (0, 1, x)$

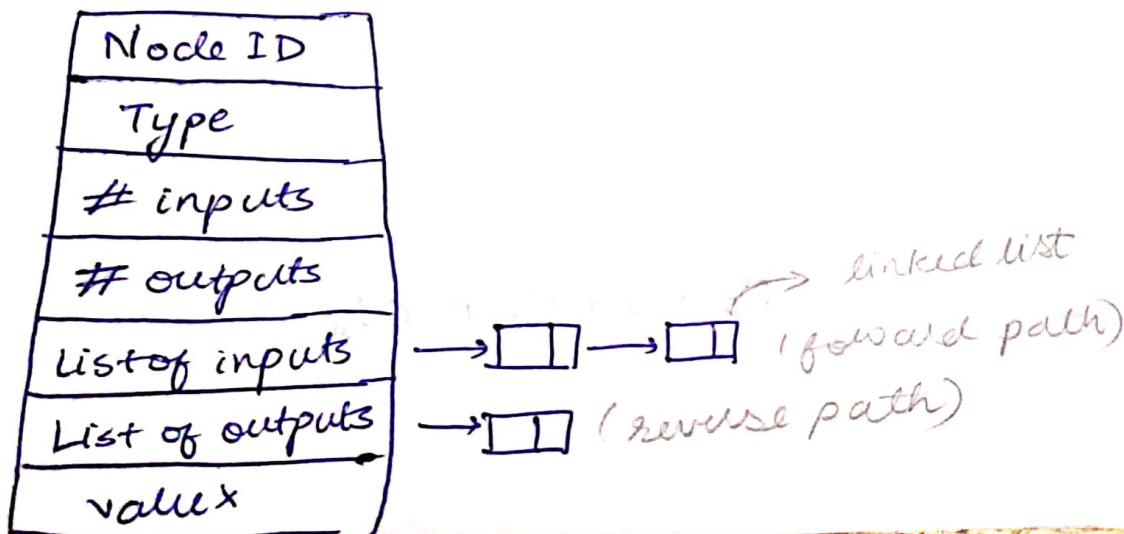
- satisfies (propagate to  $p_0$ )
- doesn't satisfy  
(go back and assign another  
S/P)
- conflict (assignment gives  
wrong objective prev value,  
hence go back and flip value)
  - if objective not satisfied  
then go back and make it  
 $x$  and then flip previous  
assignment)

so we need to maintain a stack

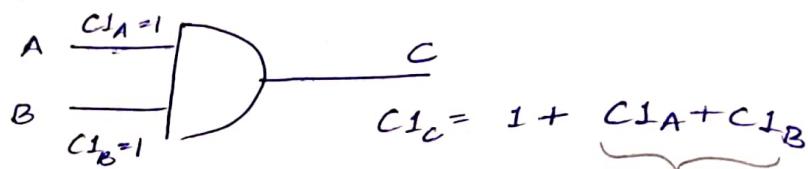
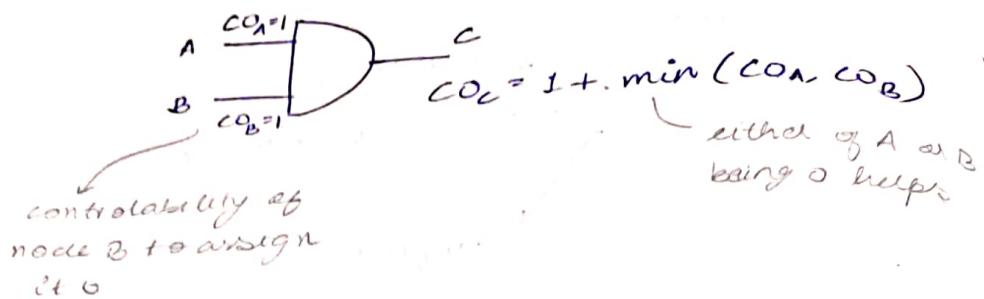


Assignment  
stack

## • Graph



- Take the path which is easier to control or observe
- controllability of a node



we want to take the easier path.

- observability of a node

- need to go backwards here
- For the AND example

$$O_C = 0$$

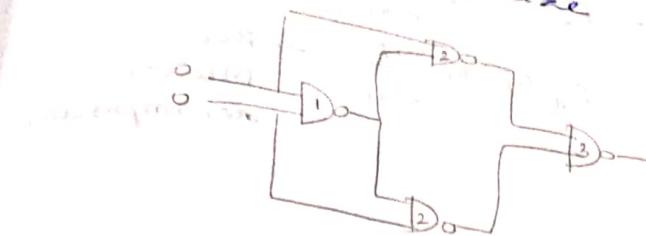
$$O_A = 1 + O_C + C_1 B$$

controllability of non-controlling value  
(can see output when B=1)

$$O_B = 1 + O_C + C_1 A$$

to concise

- level of a gate =  $1 + \max(\text{level of inputs})$   
evaluate all the gates at one level  
→ compile-code simulation  
- easy to parallelize

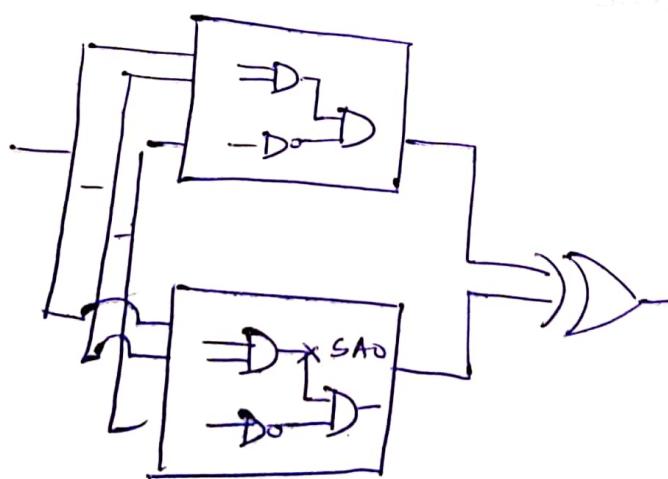


### Event-driven simulation

- 10-15% gates switch when we change
- not great for parallelization

### SAT for test simulation

- fault should be excited
- faulty vs. fault-free behaviour should be distinguished



generate inputs such that output of XOR is 1

**NOTE:** when we traverse a ckt back and forth, we know which variable affects output more and hence helps to determine order of variable

- Syntaxis

$$f(x_1, x_2, \dots, x_n) = x_i f_{x_i} \oplus \bar{x}_i f_{\bar{x}_i}$$

$$= f_{x_i} \oplus \bar{x}_i \frac{\partial f}{\partial x_i}$$

$$= f_{\bar{x}_i} \oplus x_i \frac{\partial f}{\partial x_i}$$

→ Read-Muller decomposition

$$\rightarrow x_i f_{x_i} \oplus \bar{x}_i f_{\bar{x}_i}$$

$$= x_i f_{x_i} \oplus (1+x_i) f_{\bar{x}_i}$$

$$= f_{\bar{x}_i} \oplus x_i (f_{x_i} \oplus f_{\bar{x}_i})$$

$$= f_{\bar{x}_i} \oplus x_i \frac{\partial f}{\partial x_i}$$

- Irredundant terms = no overlap

→ when we have non-overlapping OR & XOR are same

