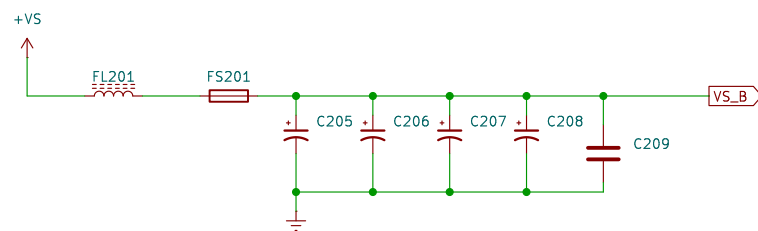


- Add voltage pins
- Add capacitor values

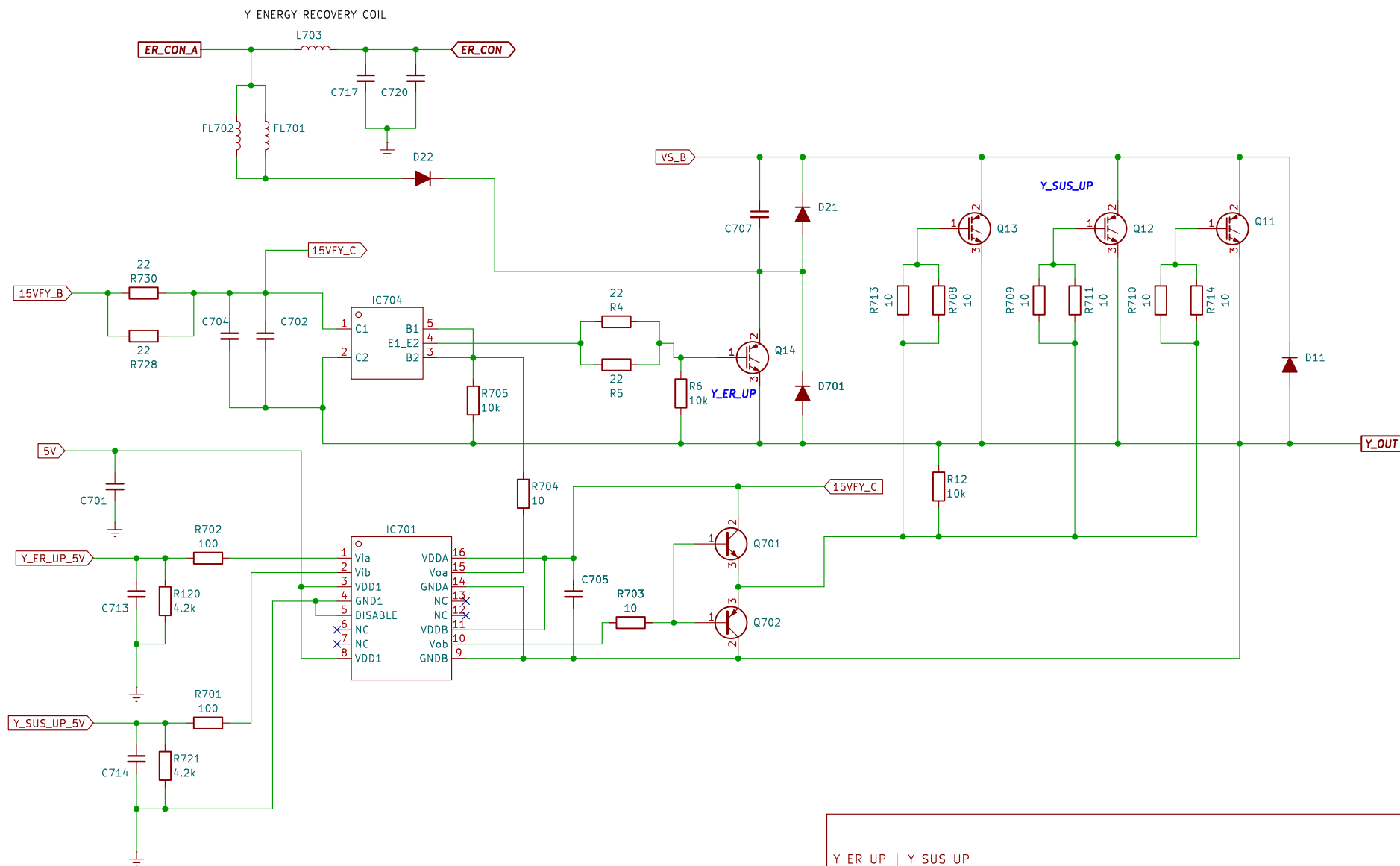


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File: y\_board.kicad\_sch

Size: A4	Date: 2025-03-24	Rev: 1.0
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Y ER UP | Y SUS UP  
Author: Fábio Pereira da Silva

Sheet: /y\_er\_up\_and\_sus\_up/  
File: y\_er\_up\_and\_sus\_up.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

Size: A4 Date: 2025-03-24

KiCad E.D.A. 8.0.9

**Rev: 1.0**

Id: 4/4