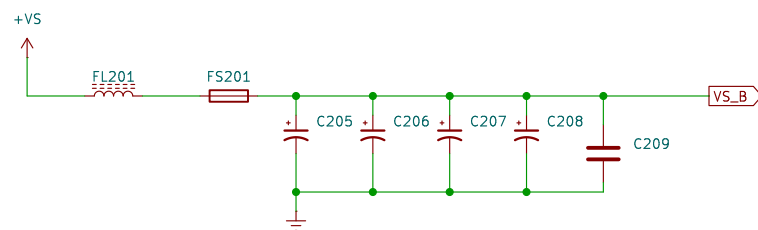


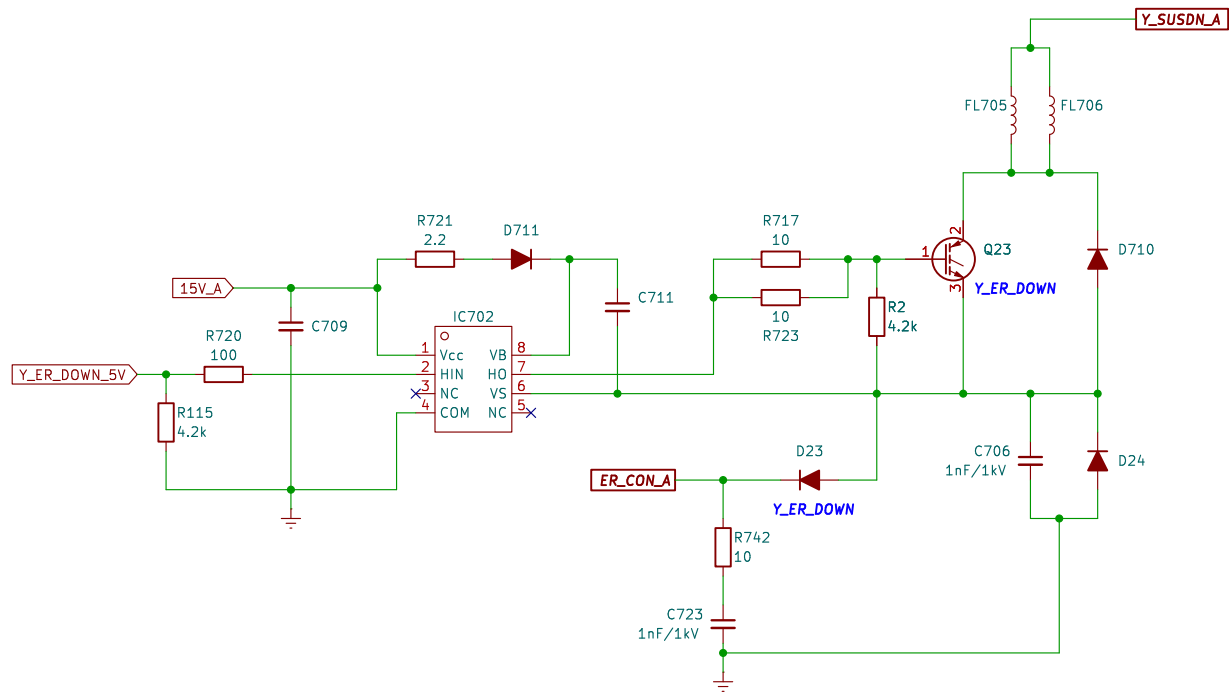
- Add voltage pins
- Add capacitor values



Sheet: /
File: y_board.kicad_sch

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9		Id: 1/6



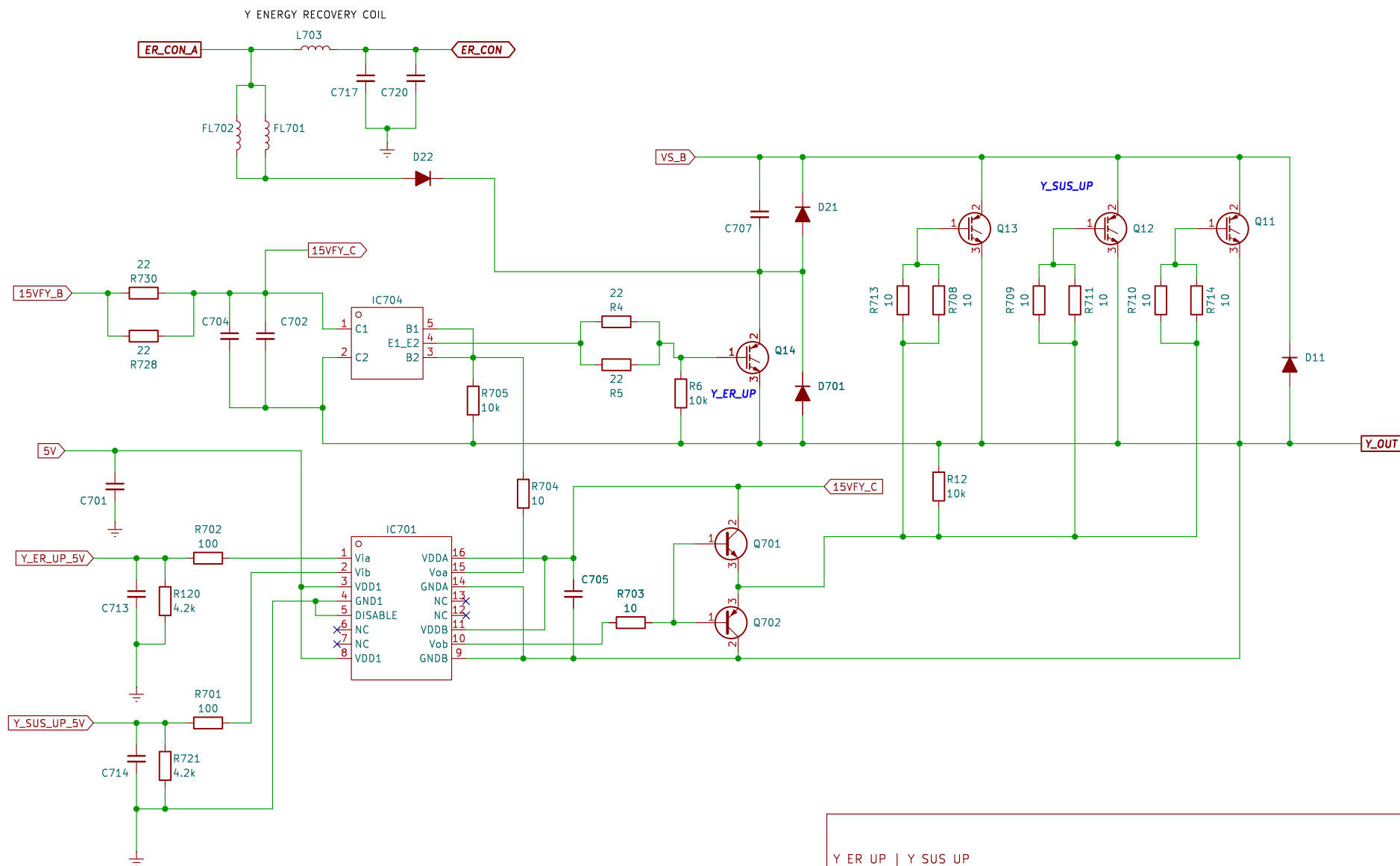


Y ER DOWN
 Author: Fábio Pereira da Silva

Sheet: /y_er_down/
 File: y_er_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9		Id: 3/6



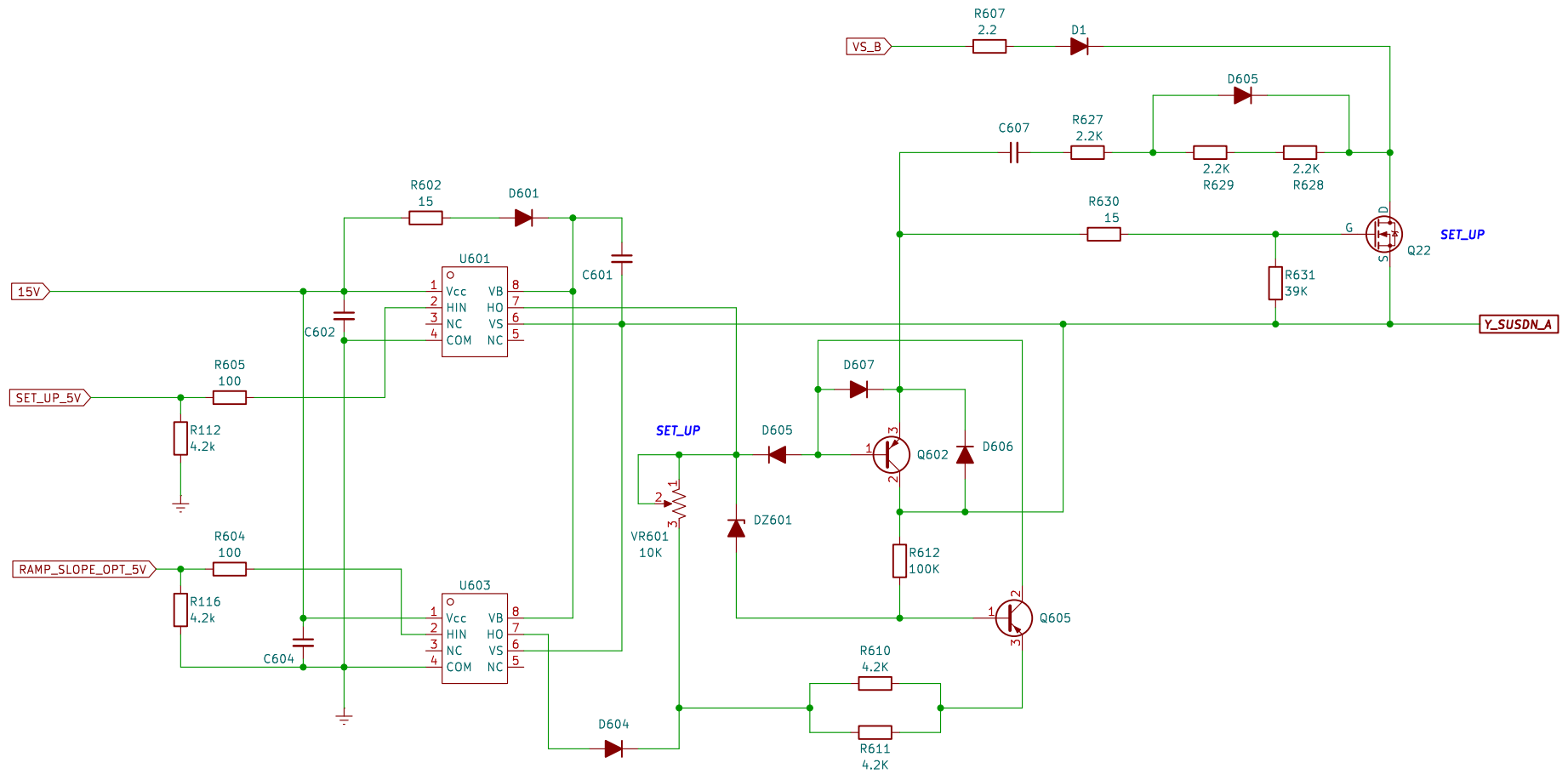
Y ER UP | Y SUS UP
 Author: Fábio Pereira da Silva

Sheet: /y_er_up_and_sus_up/
 File: y_er_up_and_sus_up.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24
 KiCad E.D.A. 8.0.9

Rev: 1.0
 Id: 4/6



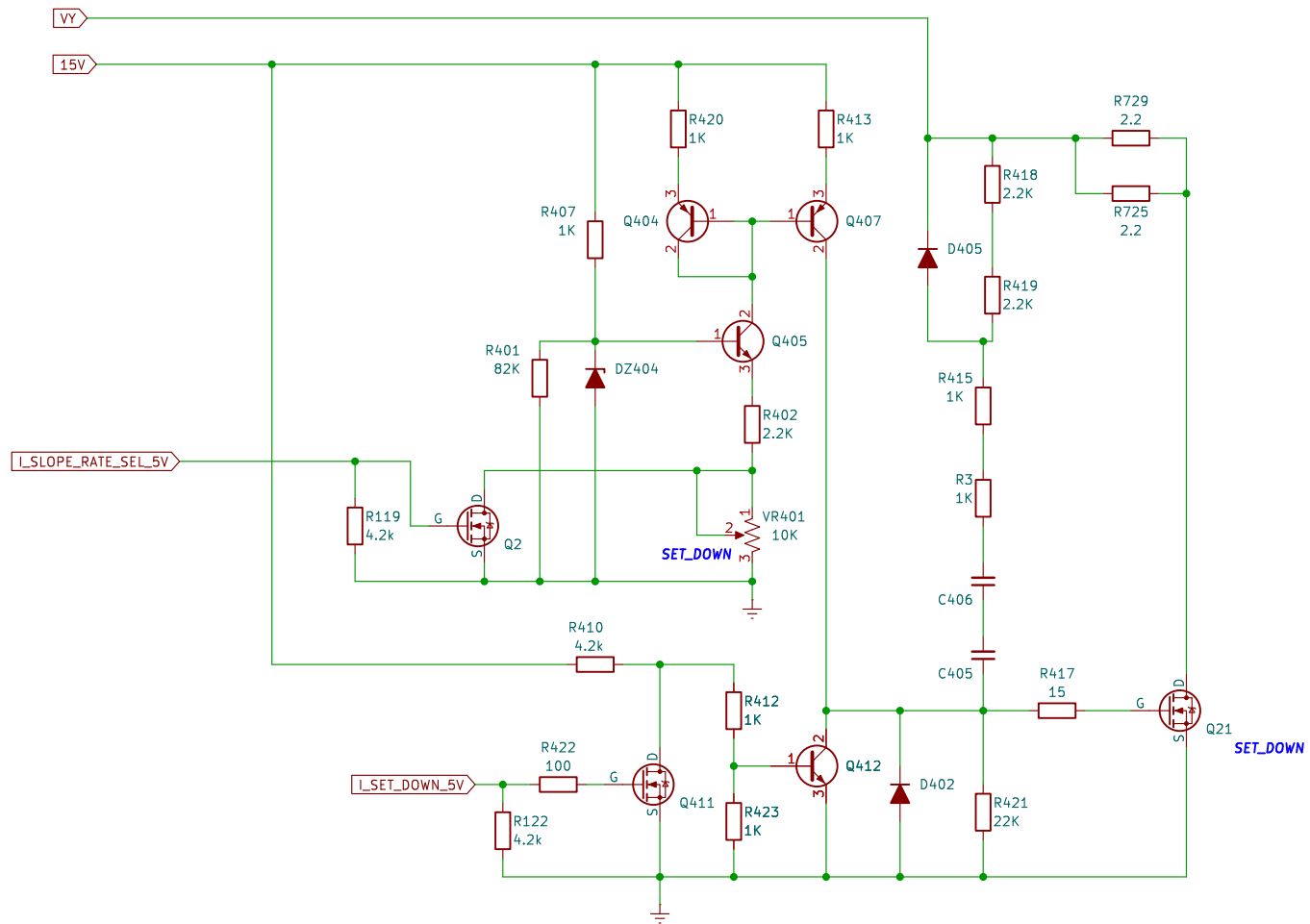
Set Up
Author: Fábio Pereira da Silva

Sheet: /SetUp/
File: set_up.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4
Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 5/6



Author: Fábio Pereira da Silva

Sheet: /SetDown/
File: set_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 6/6