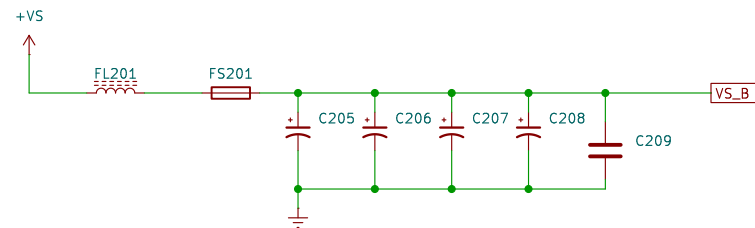


VOLTAGES SUPPLY

TODO:

- Add voltage pins
- Add capacitor values



VS Voltage supply
Author: Fábio Pereira da Silva

Sheet: /
File: y_board.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

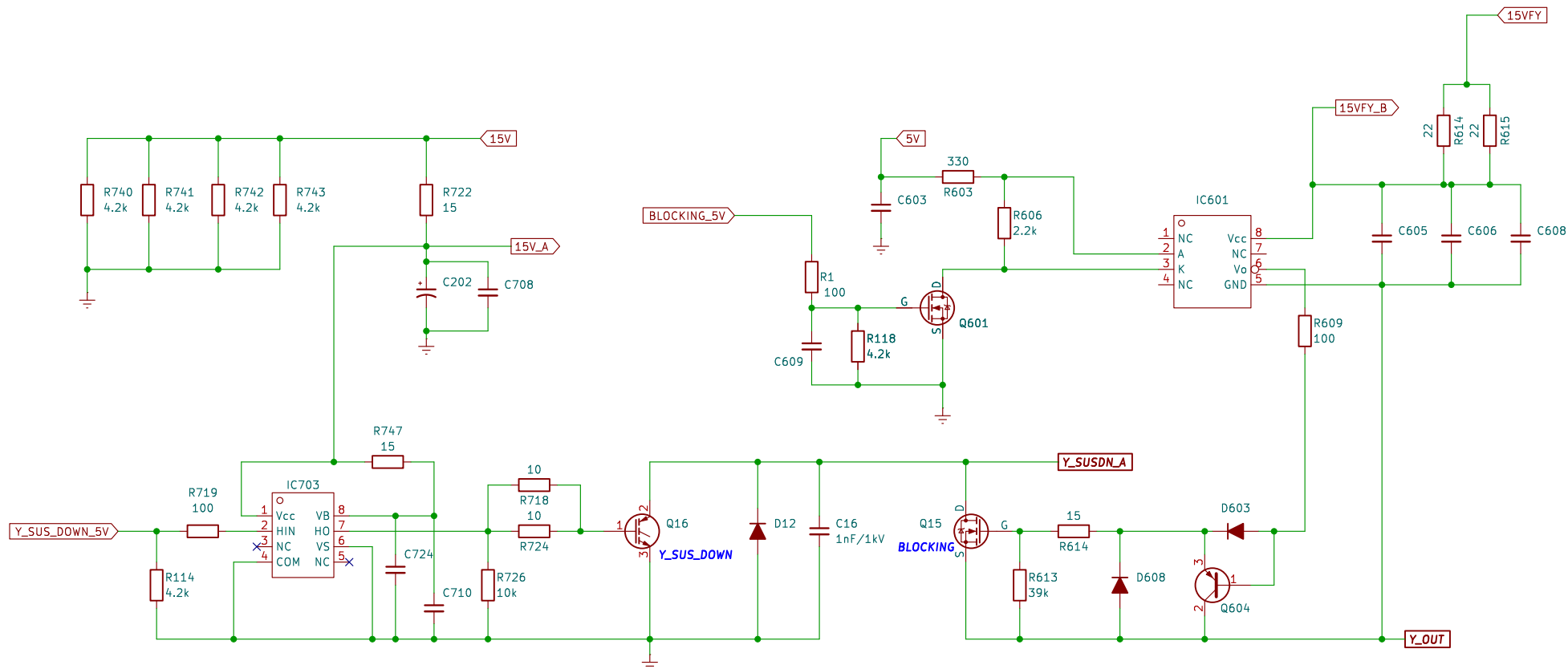
Size: A4 Date: 2025-03-24

KiCad E.D.A. 8.0.9

Rev: 1.0

Id: 1/6

Y BLOCKING AND Y SUS DOWN



Y_BLOCKING | Y_SUS_DOWN
Author: Fábio Pereira da Silva

Sheet: /Y_SUS_DOWN_AND_Y_BLOCKING/
File: y_sus_dn_and_blocking.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4

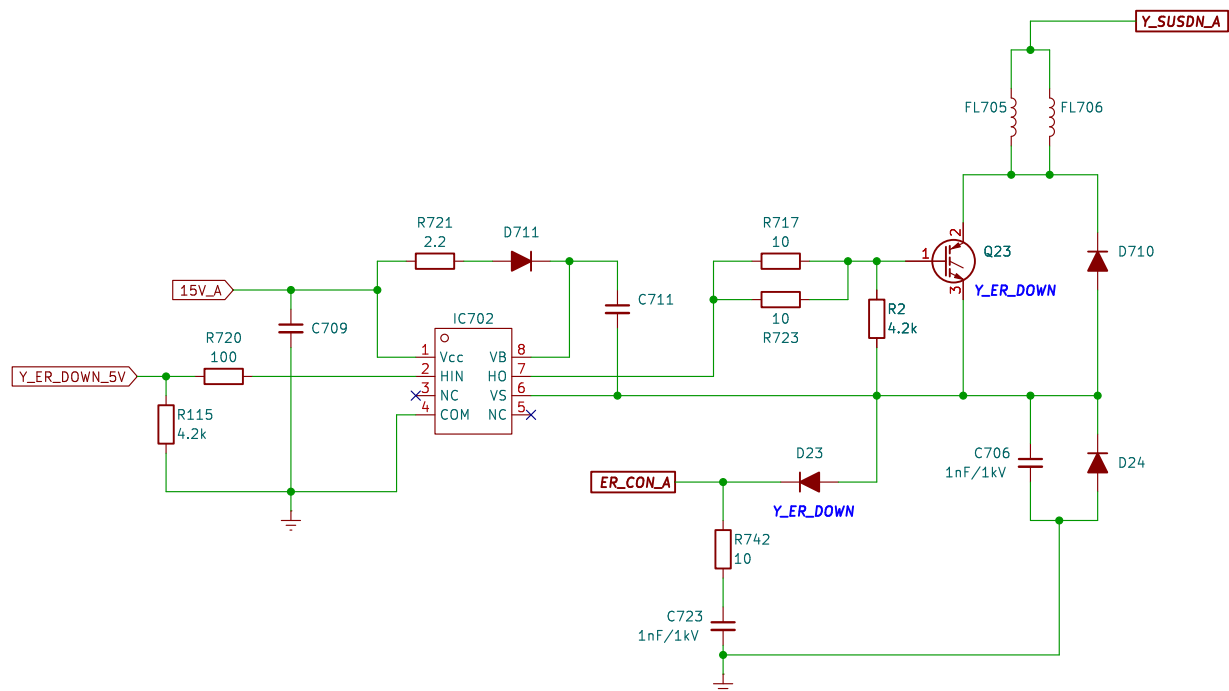
Date: 2025-03-24

Rev: 1.0

KiCad E.D.A. 8.0.9

Id: 2/6

Y ENERGY RECOVERY DOWN



Y ER DOWN
Author: Fábio Pereira da Silva

Sheet: /y_er_down/
File: y_er_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 3/6

Y ENERGY RECOVERY UP AND Y SUS UP

Y ENERGY RECOVERY COIL

ER_CON_A

L703

ER_CON

FL702

FL701

C717

C720

D22

VS_B

C707

D21

Y_SUS_UP

Q13

Q12

Q11

R713

R708

R709

R711

R710

R714

D11

Y_OUT

15V_FY_C

22 R730

22 R728

C704

C702

IC704

B1

E1_E2

B2

R705

10k

R4

22

R5

22

R6

10k

Y_ER_UP

D701

5V

C701

Y_ER_UP_5V

R702

100

C713

4.2k

R120

IC701

Via

Vib

VDD1

GND1

DISABLE

NC

NC

NC

VDD1

VDDA

Voa

GNDA

NC

NC

VDDB

Vob

GNDB

C705

R703

10

Q701

Q702

15V_FY_C

R12

10k

Y_SUS_UP_5V

R701

100

C714

4.2k

R721

4.2k

Y ER UP | Y SUS UP

Author: Fábio Pereira da Silva

Sheet: /y_er_up_and_sus_up/
File: y_er_up_and_sus_up.kicad_sch

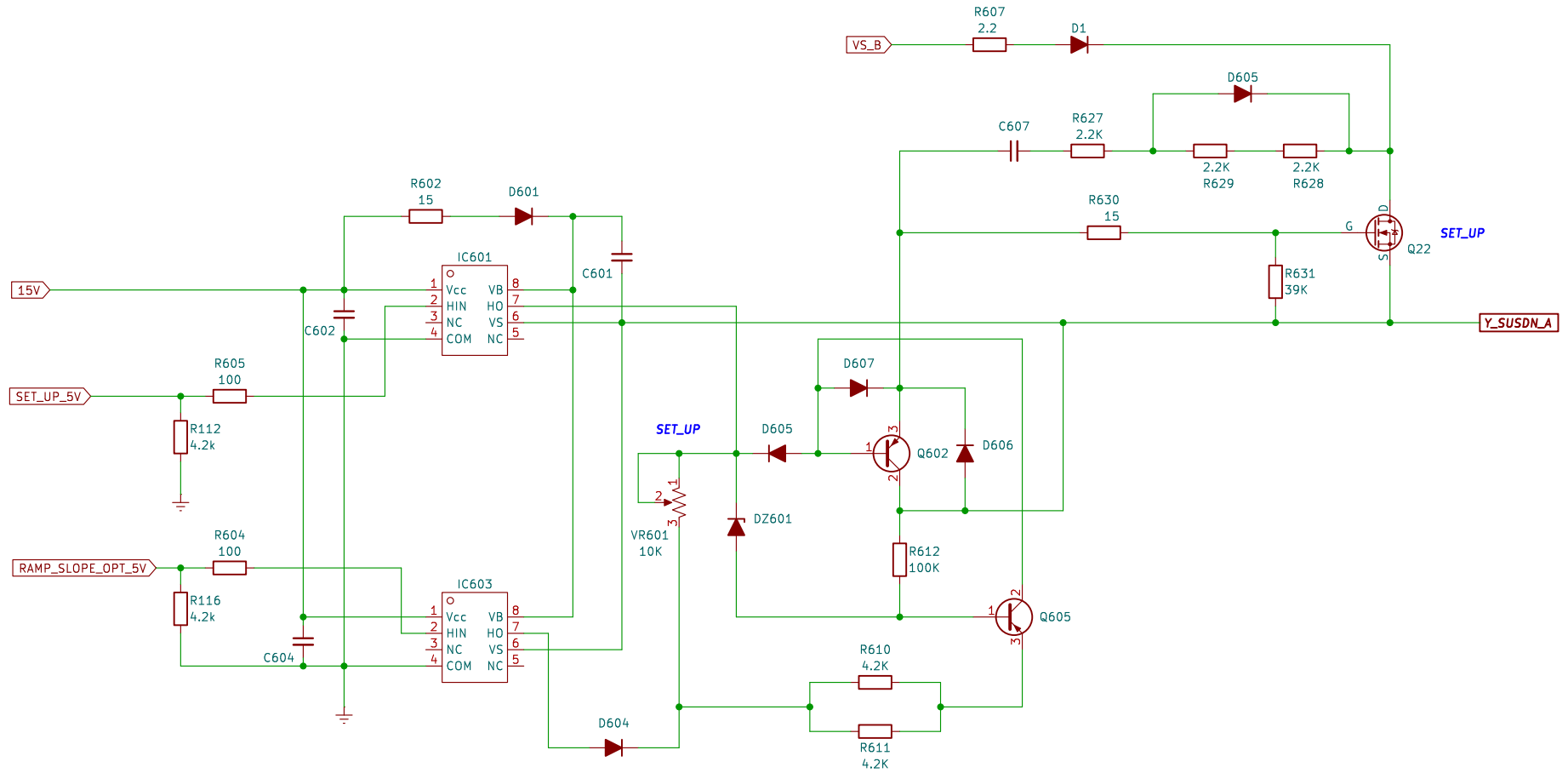
Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0

KiCad E.D.A. 8.0.9 Id: 4/6

Rev: 1.0
Id: 4/6

SET UP



Set Up
Author: Fábio Pereira da Silva

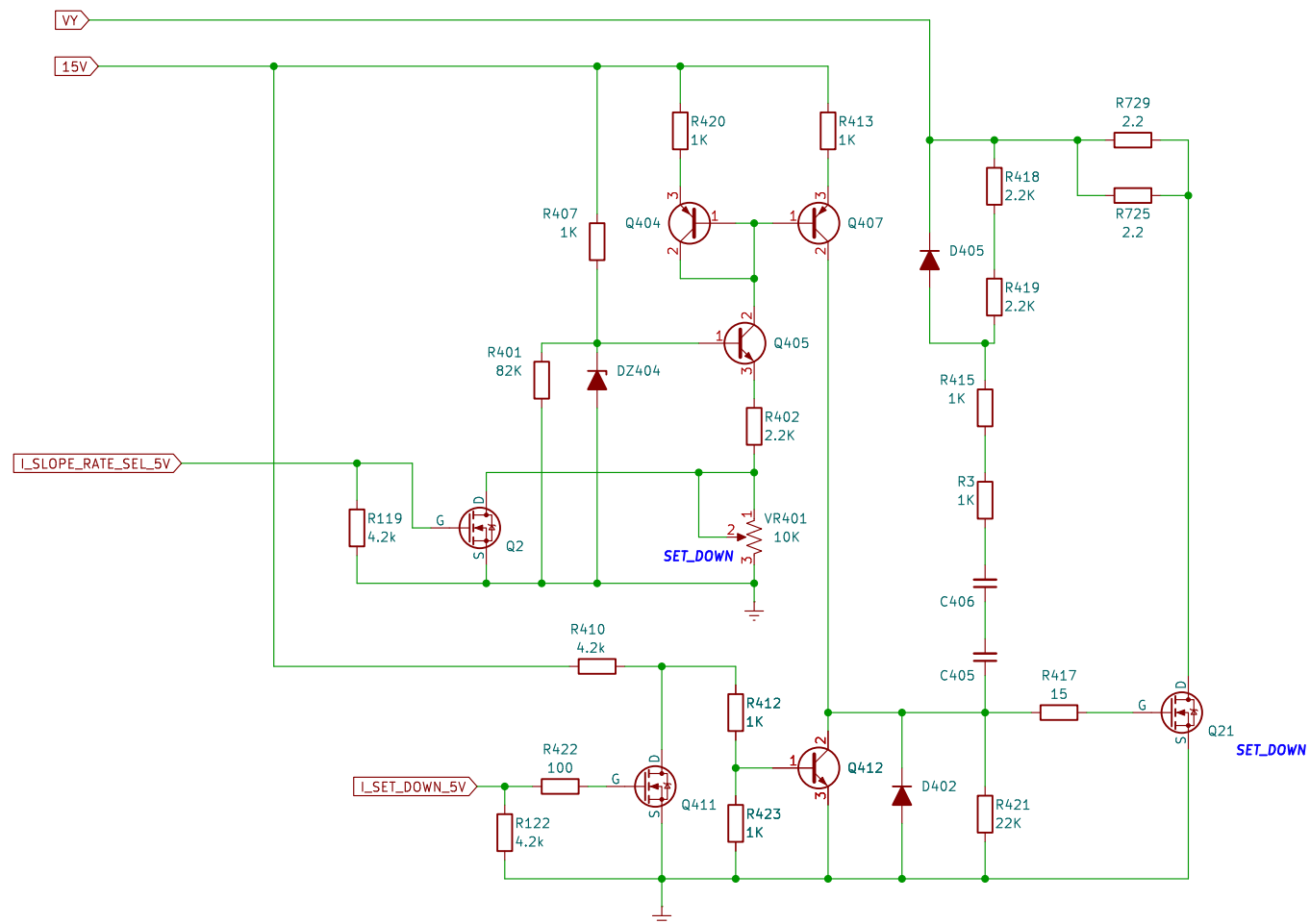
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File: set_up.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4
Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 5/6

SET DOWN



Author: Fábio Pereira da Silva

Sheet: /SetDown/
File: set_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4	Date: 2025-03-24
KiCad E.D.A. 8.0.9	

Rev: 1.0
Id: 6/6