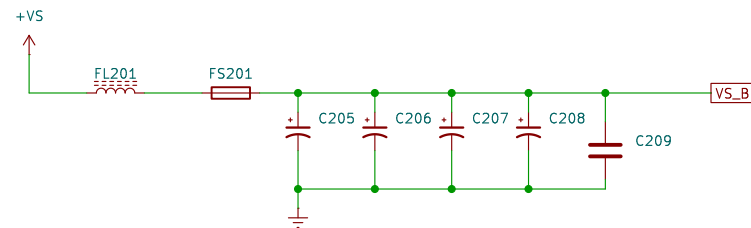


# VOLTAGES SUPPLY

TODO:

- Add voltage pins
- Add capacitor values



VS Voltage supply  
Author: Fábio Pereira da Silva

Sheet: /  
File: y\_board.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

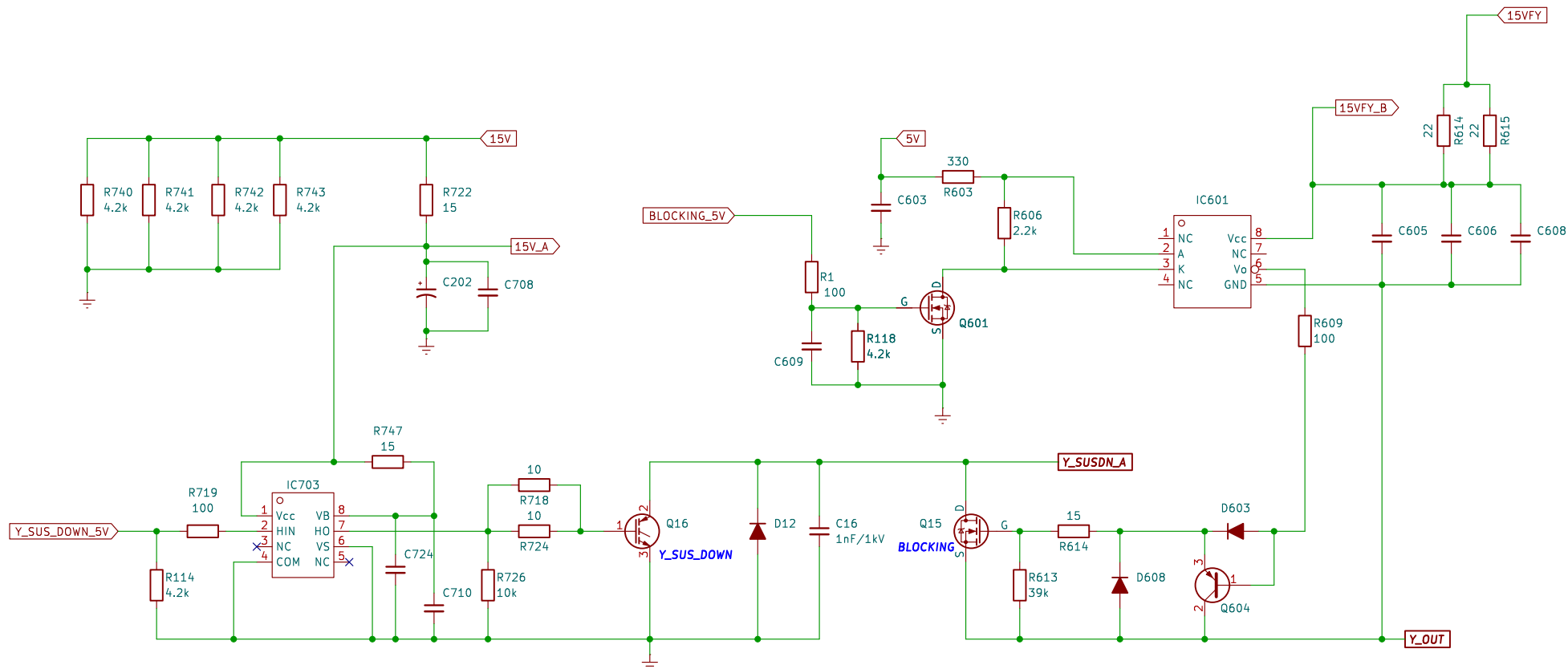
Size: A4 Date: 2025-03-24

KiCad E.D.A. 8.0.9

**Rev: 1.0**

Id: 1/6

## Y BLOCKING AND Y SUS DOWN



Y\_BLOCKING | Y\_SUS\_DOWN  
Author: Fábio Pereira da Silva

Sheet: /Y\_SUS\_DOWN\_AND\_Y\_BLOCKING/  
File: y\_sus\_dn\_and\_blocking.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

Size: A4

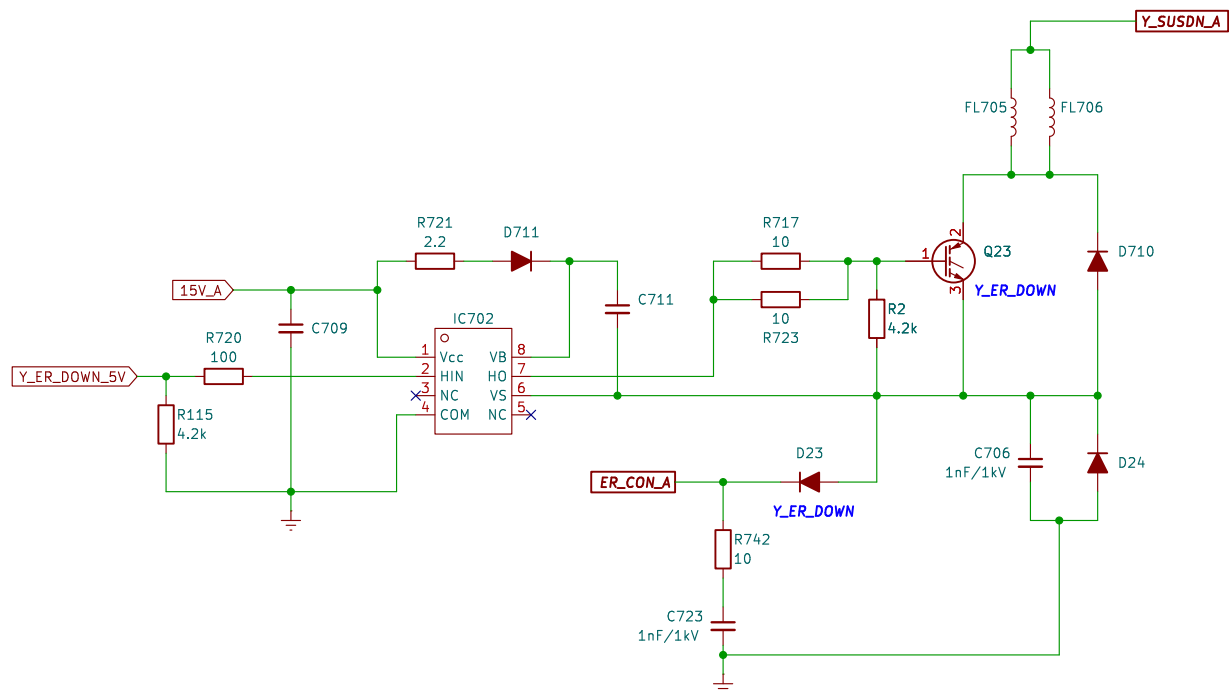
Date: 2025-03-24

Rev: 1.0

KiCad E.D.A. 8.0.9

Id: 2/6

# Y ENERGY RECOVERY DOWN



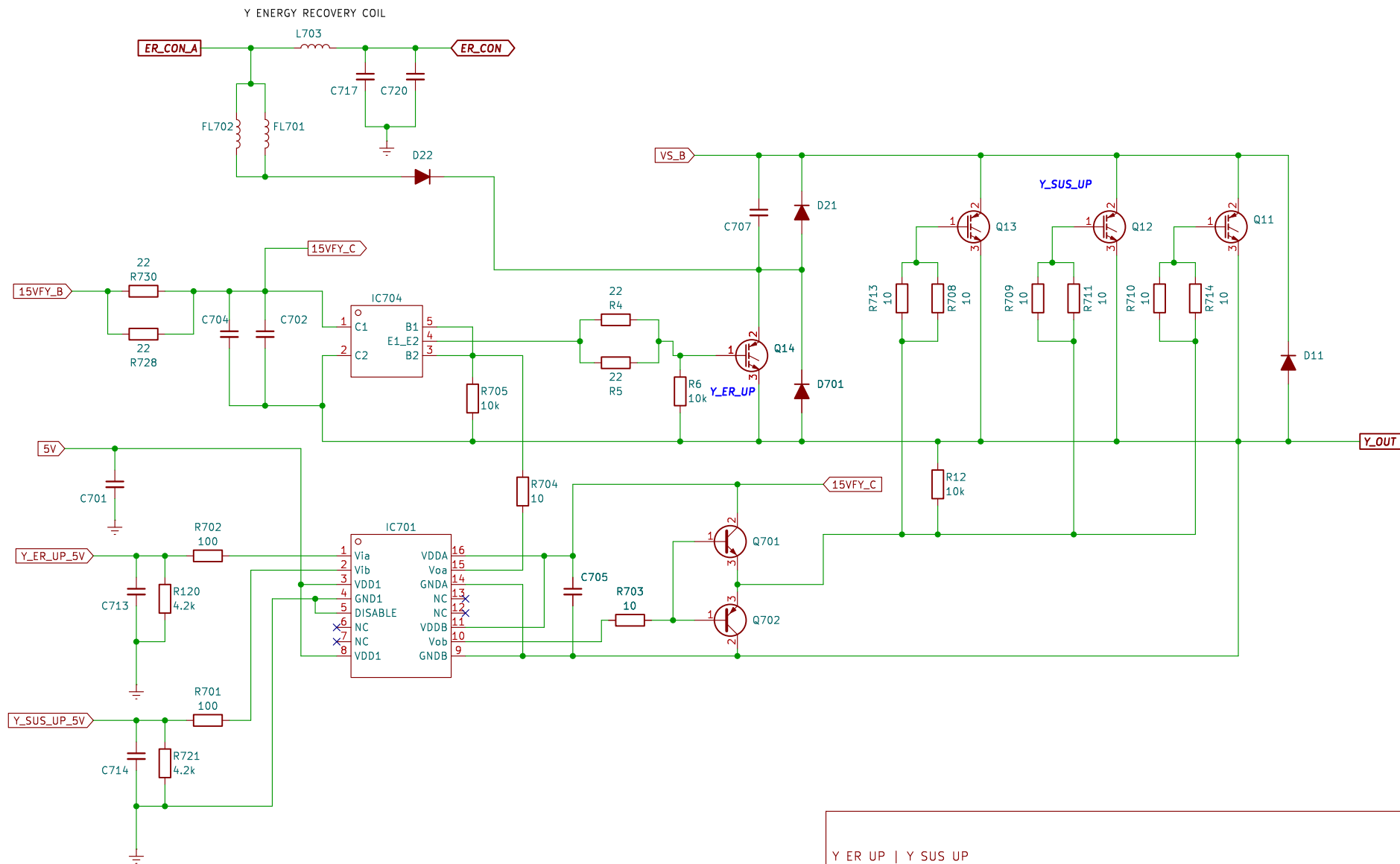
Y ER DOWN  
Author: Fábio Pereira da Silva

Sheet: /y\_er\_down/  
File: y\_er\_down.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9		Id: 3/6

# Y ENERGY RECOVERY UP AND Y SUS UP



Y ER UP | Y SUS UP  
Author: Fábio Pereira da Silva

Sheet: /y\_er\_up\_and\_sus\_up/  
File: y\_er\_up\_and\_sus\_up.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

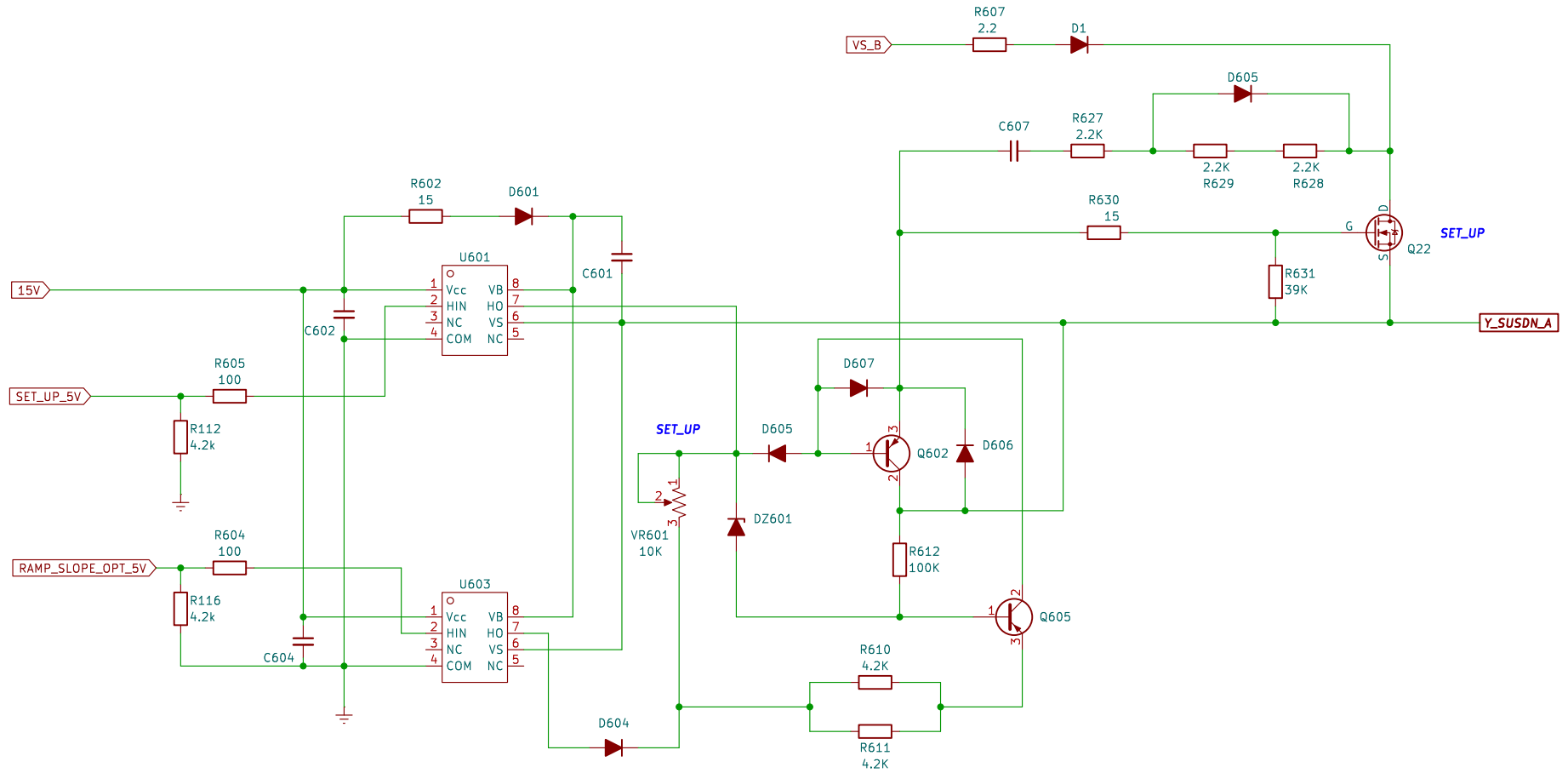
Size: A4  
KiCad E.D.A. 8.0.9

Date: 2025-03-24

Rev: 1.0

Id: 4/6

# SET UP



Set Up  
Author: Fábio Pereira da Silva

Sheet: /SetUp/  
File: set\_up.kicad\_sch

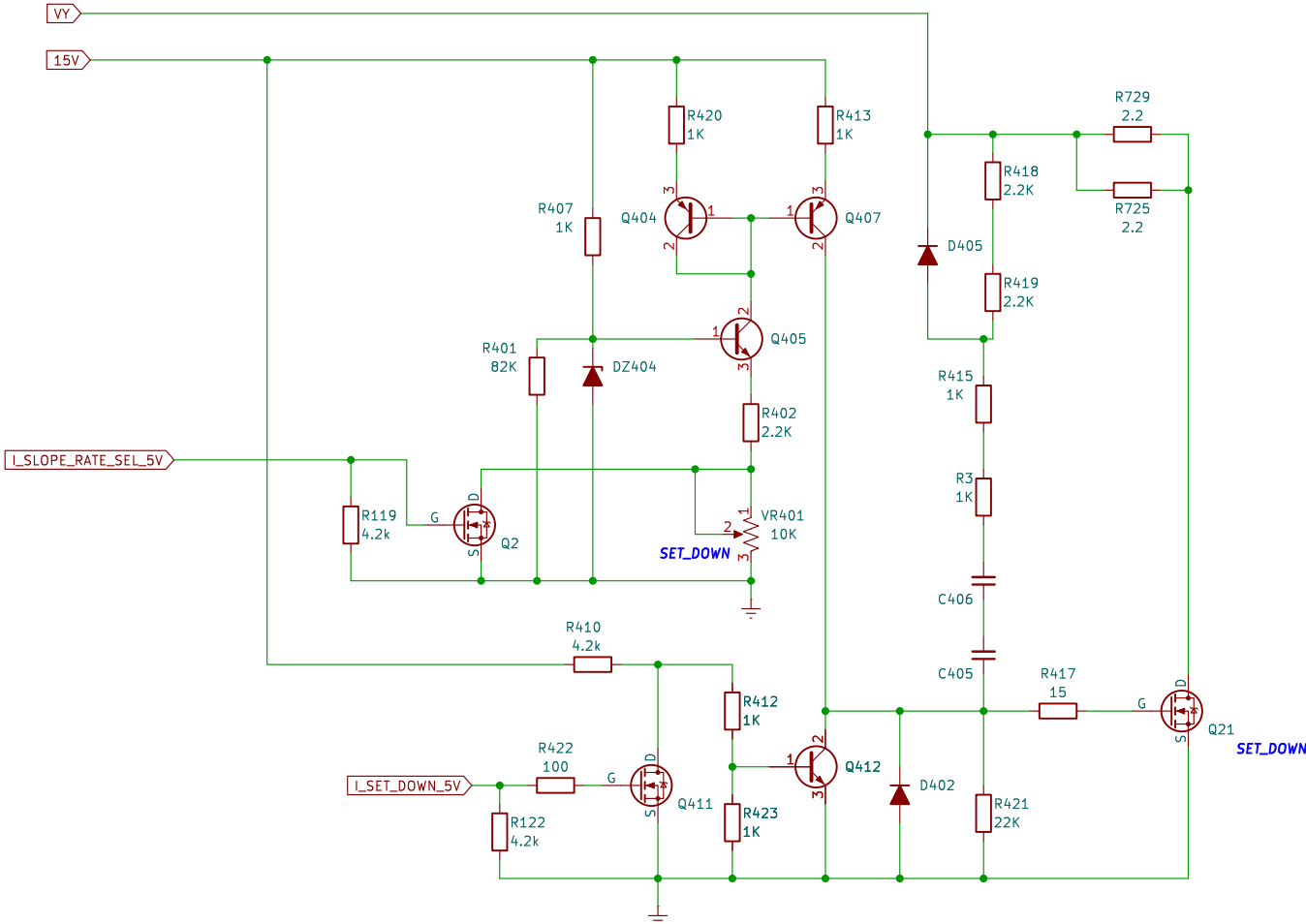
**Title: Y BOARD LG PLASMA TV PANEL**

Size: A4  
Date: 2025-03-24

KiCad E.D.A. 8.0.9

**Rev: 1.0**  
Id: 5/6

SET DOWN



Author: Fábio Pereira da Silva

Sheet: /SetDown/  
File: set\_down.kicad\_sch

**Title: Y BOARD LG PLASMA TV PANEL**

Size: A4	Date: 2025-03-24
KiCad E.D.A. 8.0.9	

Rev: 1.0  
Id: 6/6