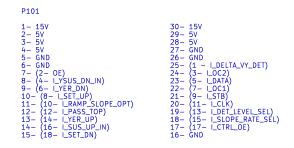
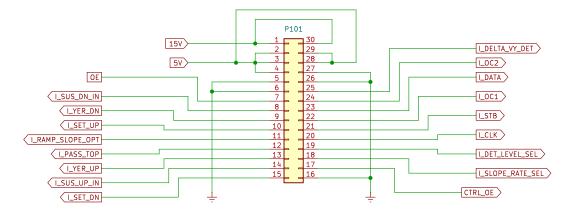


## Input Control (Part 1 of 3)





Input Control (Part 1 of 3) Author: Fábio Pereira da Silva

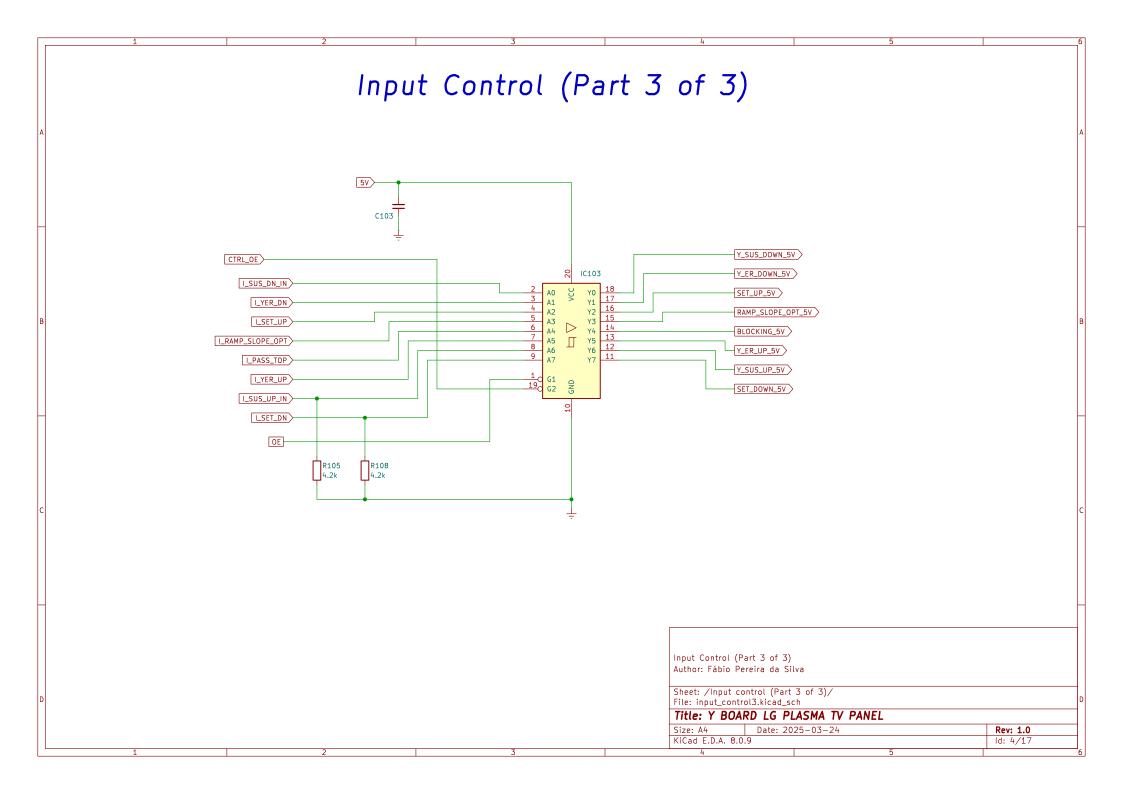
Sheet: /Input control (Part 1 of 3)/
File: input\_control.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

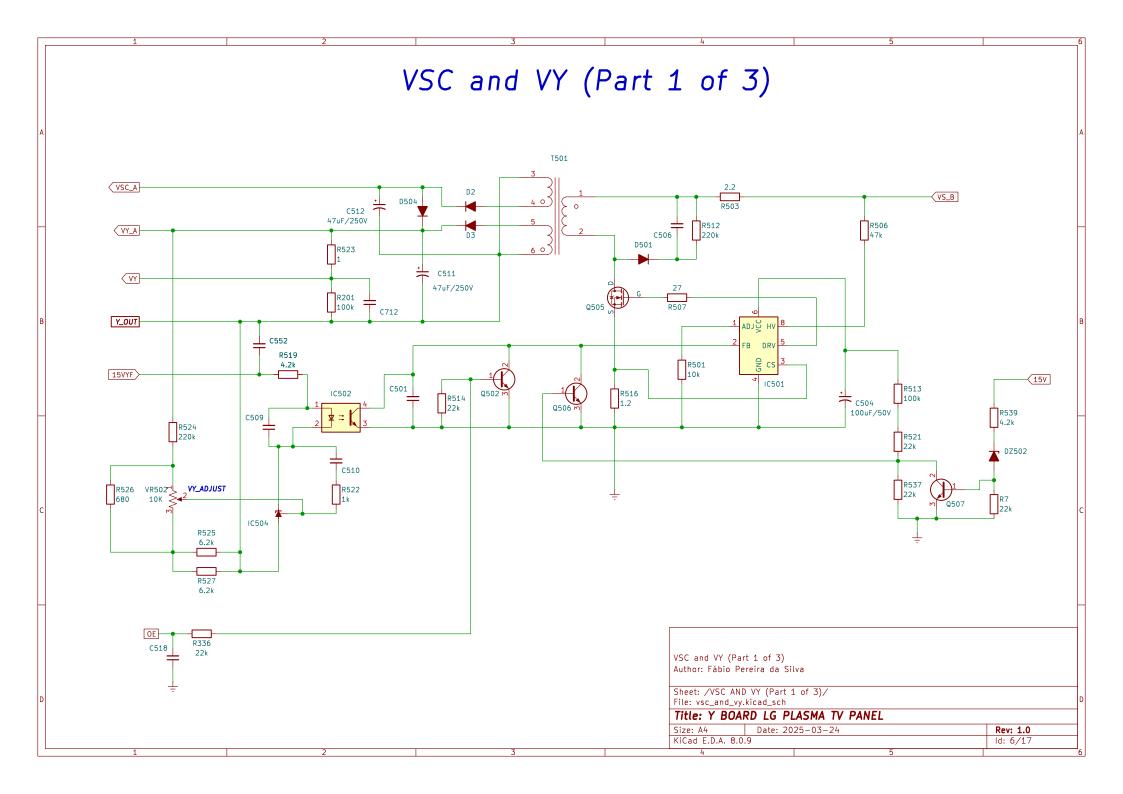
 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 2/17

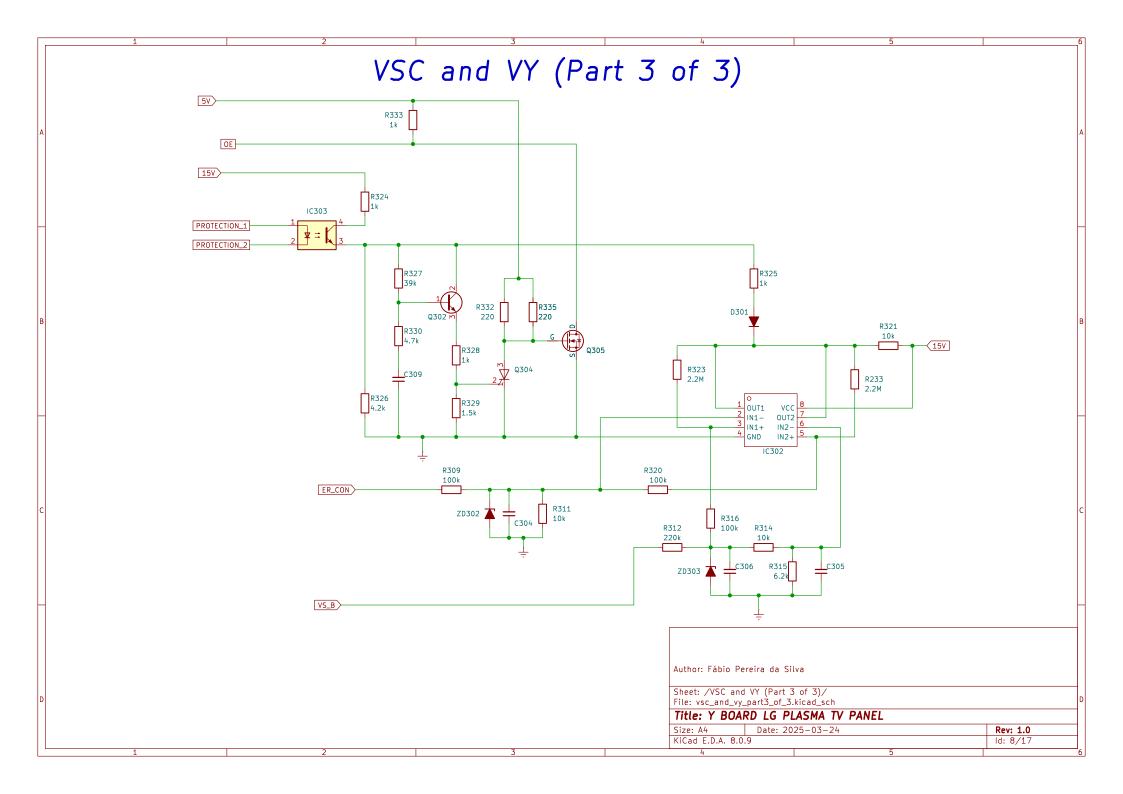
### Input Control (Part 2 of 3) R129 C102 ── C104 I\_SLOPE\_RATE\_SEL\_5V I\_DET\_LEVEL\_SEL\_A R403 CTRL\_OE R12315k 4.2k I\_CLK\_5V > IC102 I\_SLOPE\_RATE\_SEL Y0 Y1 Y2 Y3 Y4 Y5 Y6 I\_DET\_LEVEL\_SEL I\_CLK > R124 4.2k I\_0C1\_5V > I\_STB R109 4.2k I\_STB\_5V I\_OC1 I\_0C2\_5V > R125 4.2k I\_DATA > 19 G2 R143 4.2k I\_OC2 I\_DELTA\_VY\_DET OE R111 4.2k R117 4.2k I\_DATA\_5V I\_DELTA\_VY\_DET\_5V Input Control (Part 2 of 3) Author: Fábio Pereira da Silva Sheet: /Input control (Part 2 of 3)/ File: input\_control2.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 3/17



#### 5VF, 15VF and 15V Supply 5٧ IC505 150 OUT D511 IC508 D509 T502 SVF-C520 330uF/25V C219 D510 D508 C524 R535 1000uF/50V D506 15k C519 IC507 100uF/50V 60) (15VF) C516 1000uF/25V C521 ± C307 IC503 D507 3300uF/10V 330uF/25V Y\_OUT R529 D505 10 F R528 1 k C719 1nF/1kV C722 1nF/1kV R531 IC506 1 VCC 82k C517 2 OUT CTL/SS 3 GND R532 4 VREF INV 5 OE 22k 22k R534 22k R530 R536 C513 ± C514 2.2M 1000 5VF, 15VF and 15V Supply Author: Fábio Pereira da Silva Sheet: /5VF, 15VF 15v/ File: 5VF\_15VF\_15V.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 5/17

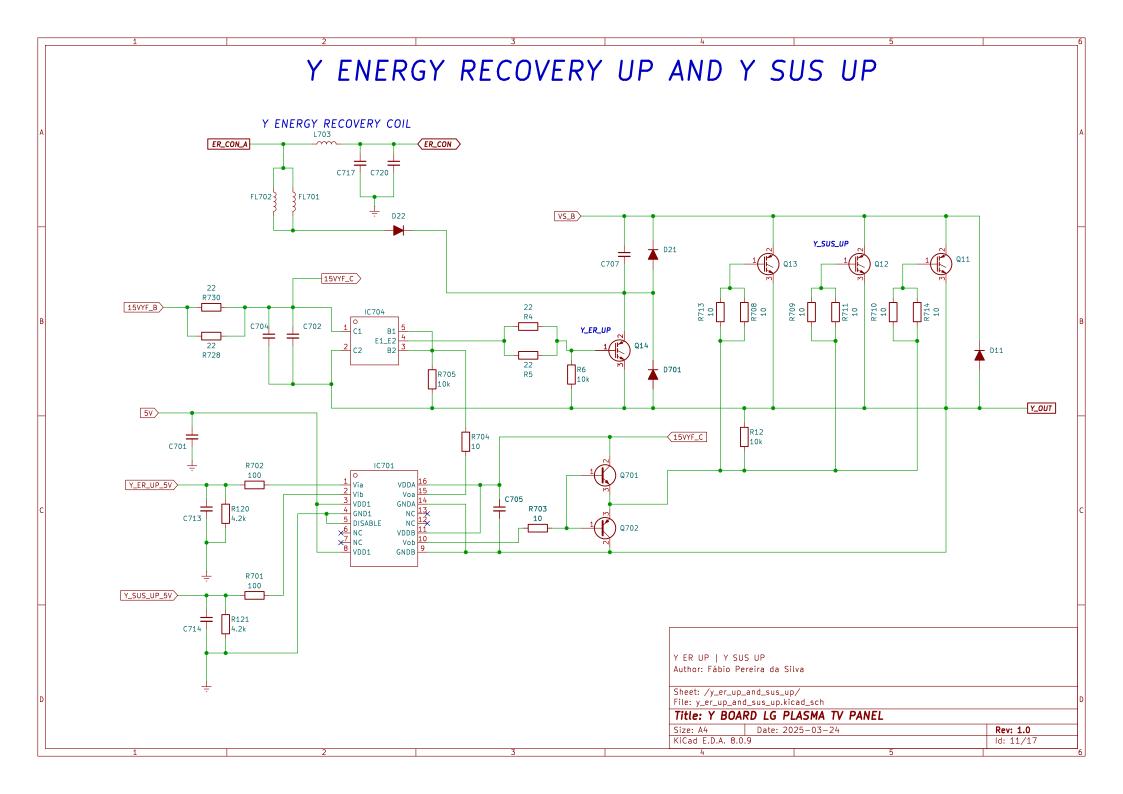


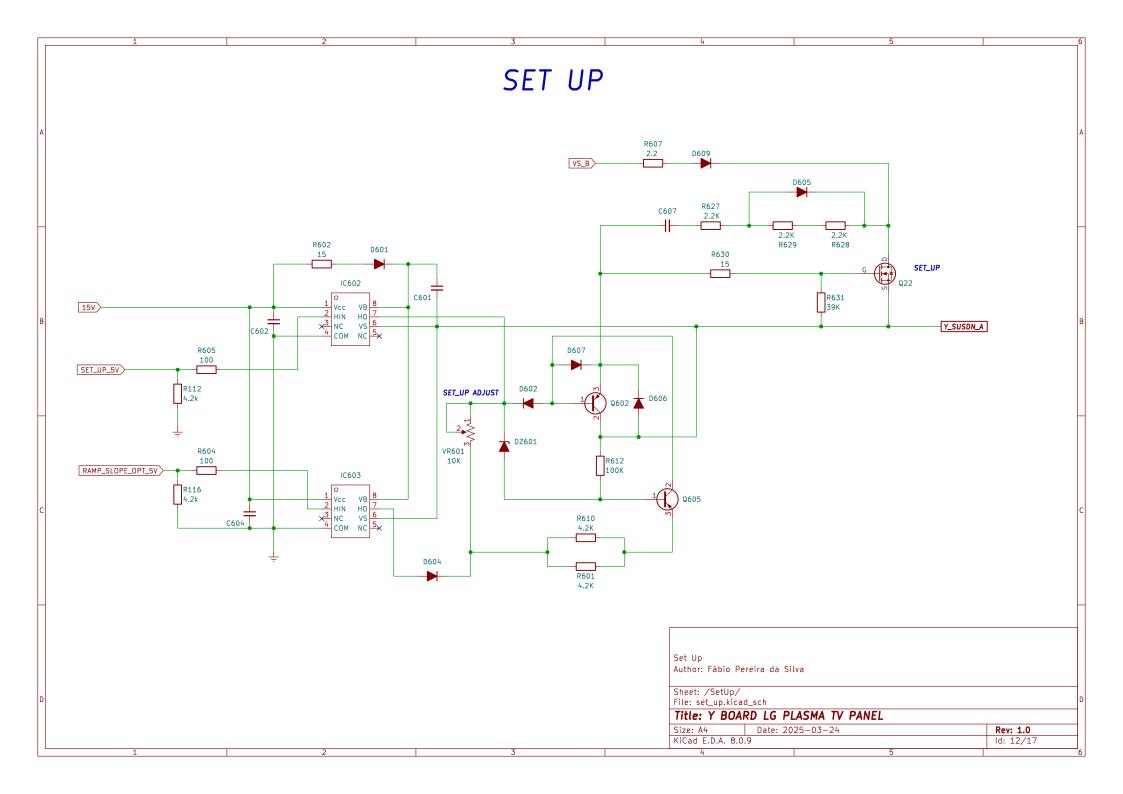
#### VSC and VY (Part 2 of 3) VY\_A C525 VSC\_A R504 10 R544 6.8k R505 470 R301 100k 2→VR501 10K R510 R545 47k 10k VSC\_ADJUST<sup>™</sup> ZD501 15VYF C507 R511 47k R304 100k R317 4.7k R319 1k R520 C508 47uF/250V R310 R517 47k 2.2M PROTECTION\_1 VSC R313 2.2M 0 0 UT1 VCC 2 IN1 - OUT2 3 IN1 + IN2 -GND IN2 + R202 47k ZD304 <u></u>\_C301 \_\_\_\_C213 R518 100k R303 3.9k 100nF PROTECTION\_2 ZD301 Y\_OUT IC301 R305 R308 100k 15k C220 R307 10k \_\_\_C303 R318 10k VSC and VY (Part 2 of 3) Author: Fábio Pereira da Silva Sheet: /VSC and VY (Part 2 of 3)/ File: vcs\_and\_vy\_part2\_of\_3.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 Id: 7/17 KiCad E.D.A. 8.0.9

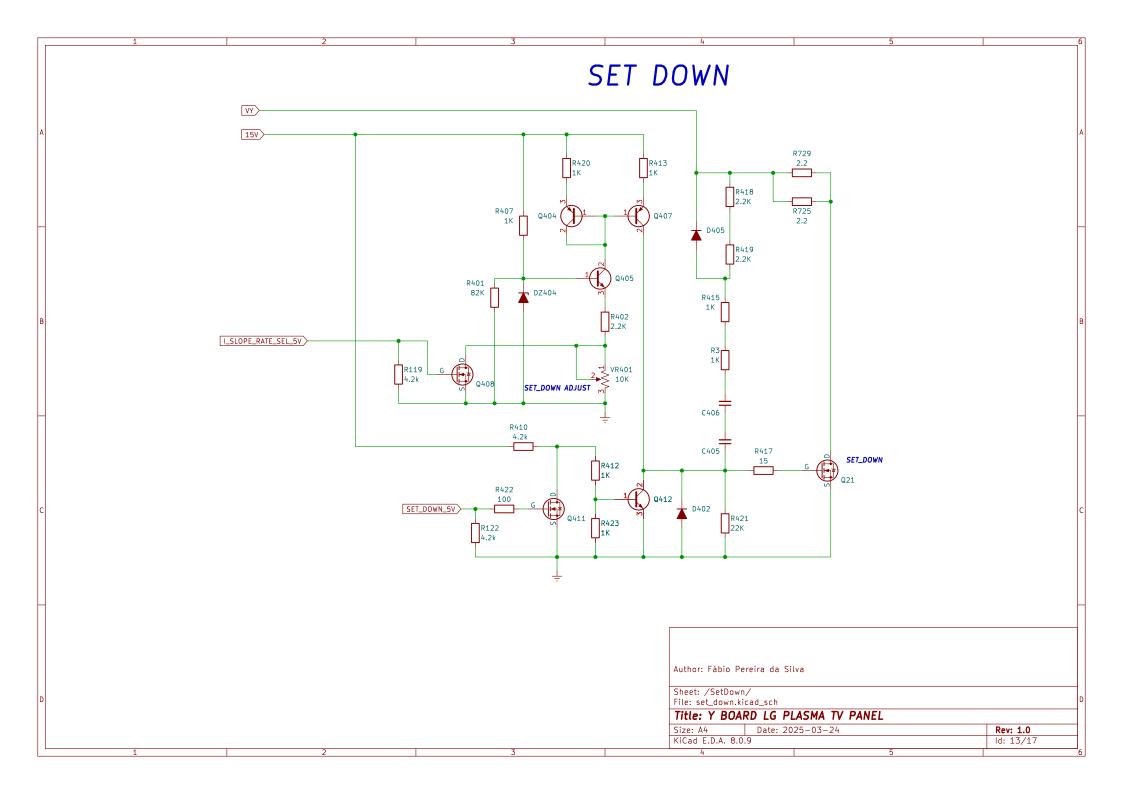


### Y BLOCKING AND Y SUS DOWN 15VYF 15VYF\_B 22 R614 22 R615 330 C603 R603 R540 R541 4.2k 4.2k R542 R543 4.2k R543 R722 15 IC601 R606 2.2k BLOCKING\_5V 15V\_A > C202 <u>L</u> C708 100 R609 100 R118 4.2k 15 Y\_SUSDN\_A IC703 R719 100 D603 Vcc VB 10 10 Q16 D12 + C16 1nF/1kV 2 HIN HO 7 NC VS 6 COM NC 5 Q15 R724 Y\_SUS\_DOWN\_5V R611 BLOCKING S C724 Y\_SUS\_DOWN ★ D608 C710 R726 R114 4.2k R613 39k Q604 Y\_OUT Y\_BLOCKING | Y\_SUS\_DOWN Author: Fábio Pereira da Silva Sheet: /Y\_SUS\_DOWN\_AND\_Y\_BLOCKING/ File: y\_sus\_dn\_and\_blocking.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 9/17

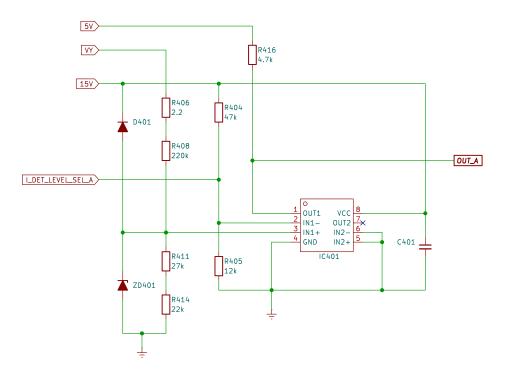
### Y ENERGY RECOVERY DOWN Y\_SUSDN\_A FL706 FL705 } R721 2.2 R717 D711 10 Q23 → D710 \_\_\_ C711 15V\_A Y\_ER\_DOWN IC702 R2 4.2k 10 <del>+</del> C709 1 Vcc VB 8 7 6 6 COM NC 5 5 R720 R723 100 Y\_ER\_DOWN\_5V R115 4.2k C706 \_\_\_\_ D24 1nF/1kV ER\_CON\_A Y\_ER\_DOWN R742 C723 1nF/1kV Y ER DOWN Author: Fábio Pereira da Silva Sheet: /y\_er\_down/ File: y\_er\_down.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 10/17







# OUPUT CONTROL (Part 1 of 4)



Output Control (Part 1 of 4) Author: Fábio Pereira da Silva

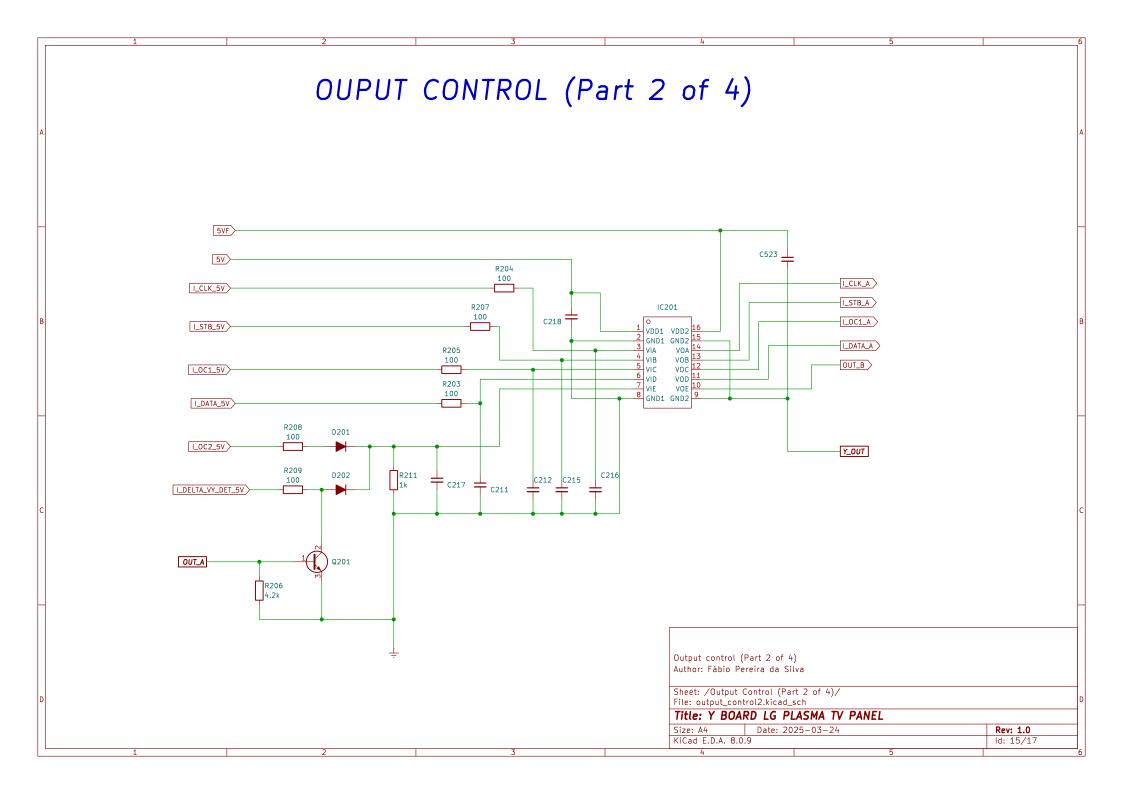
Sheet: /Output Control (Part 1 of 4)/

File: output\_control.kicad\_sch

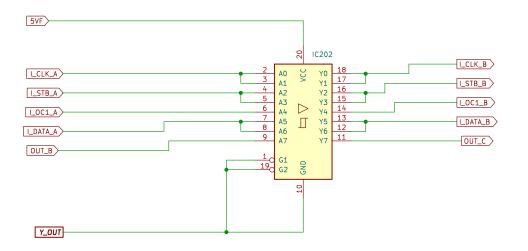
Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 14/17



# OUPUT CONTROL (Part 3 of 4)



Output control (Part 3 of 4) Author: Fábio Pereira da Silva

Sheet: /Output Control (Part 3 of 4)/

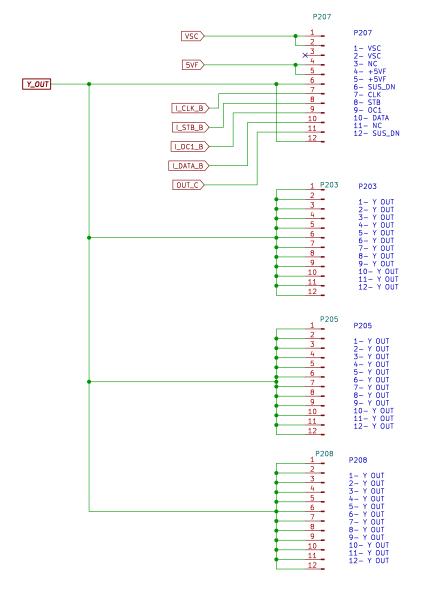
File: output\_control3.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 16/17

# OUPUT CONTROL (Part 4 of 4)



### NOTES:

Serigraphy notation in P207 (Y Out) is called SUS\_DNPin 11 in P207 in Serigraphy is NC but is connected to output pin 11 in IC 202

> Output Control (Part 4 of 4) Author: Fábio Pereira da Silva

Sheet: /Output Control (Part 4 of 4)/

File: output control4.kicad sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 17/17