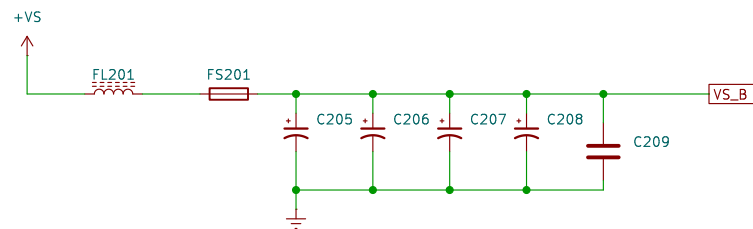


VOLTAGES SUPPLY

TODO:

- Add voltage pins
- Add capacitor values



VS Voltage supply
Author: Fábio Pereira da Silva

Sheet: /
File: y_board.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

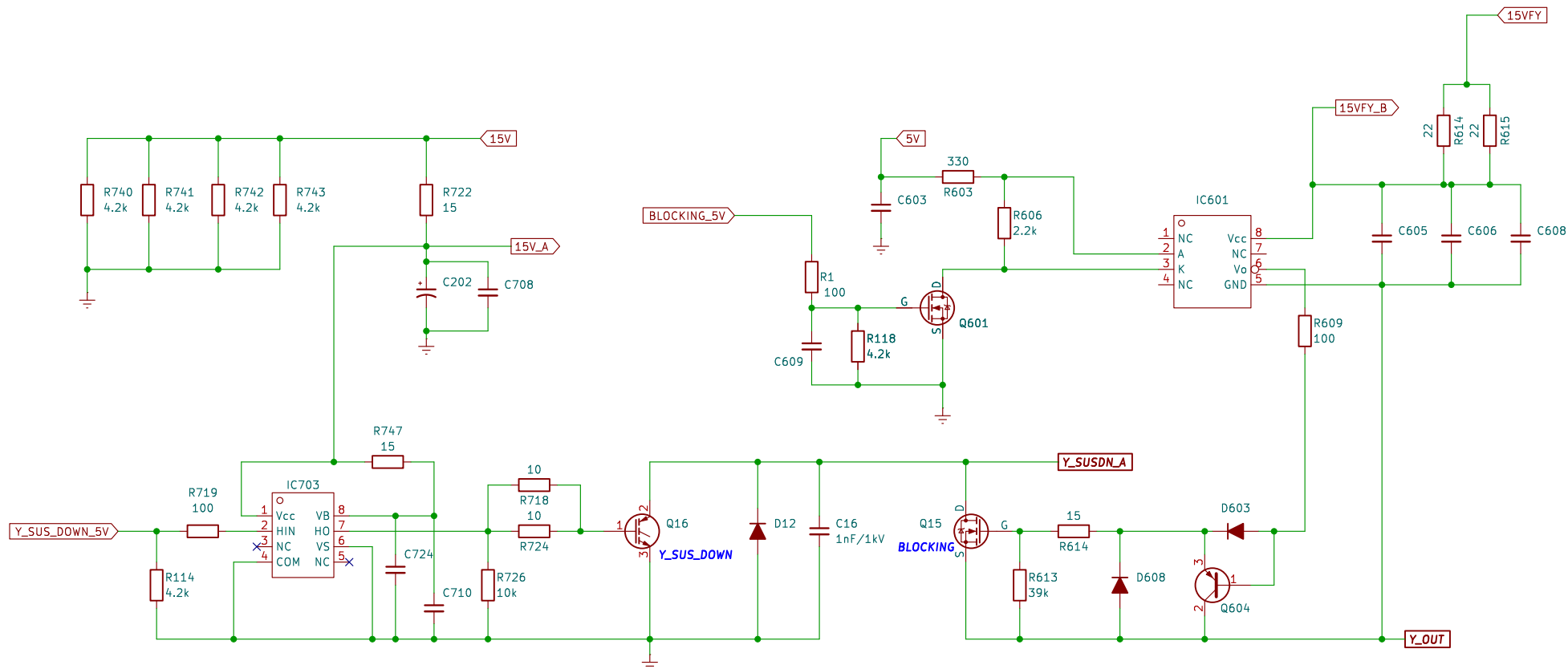
Size: A4 Date: 2025-03-24

KiCad E.D.A. 8.0.9

Rev: 1.0

Id: 1/8

Y BLOCKING AND Y SUS DOWN



Y_BLOCKING | Y_SUS_DOWN
Author: Fábio Pereira da Silva

Sheet: /Y_SUS_DOWN_AND_Y_BLOCKING/
File: y_sus_dn_and_blocking.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

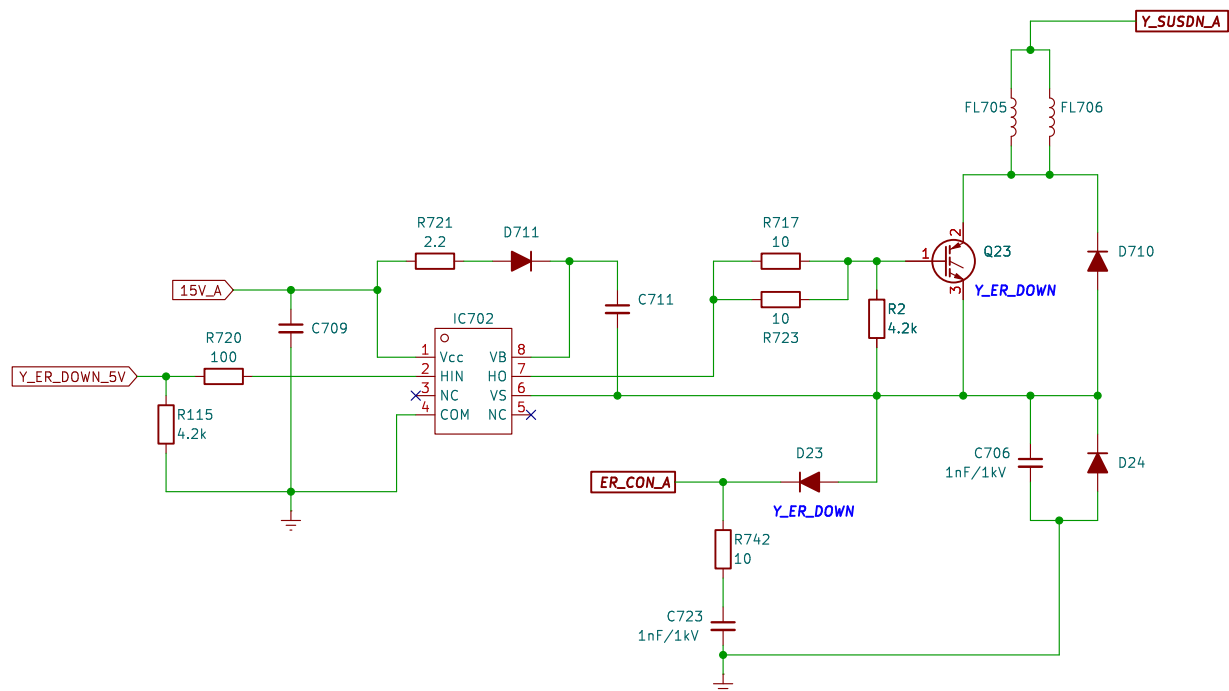
Size: A4	
KiCad E.D.A. 8.0.9	

Date: 2025-03-24

Rev: 1.0

Id: 2/8

Y ENERGY RECOVERY DOWN



Y ER DOWN
Author: Fábio Pereira da Silva

Sheet: /y_er_down/
File: y_er_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9		Id: 3/8

Y ENERGY RECOVERY COIL

Y_ER_UP | Y_SUS_UP

Author: Fábio Pereira da Silva

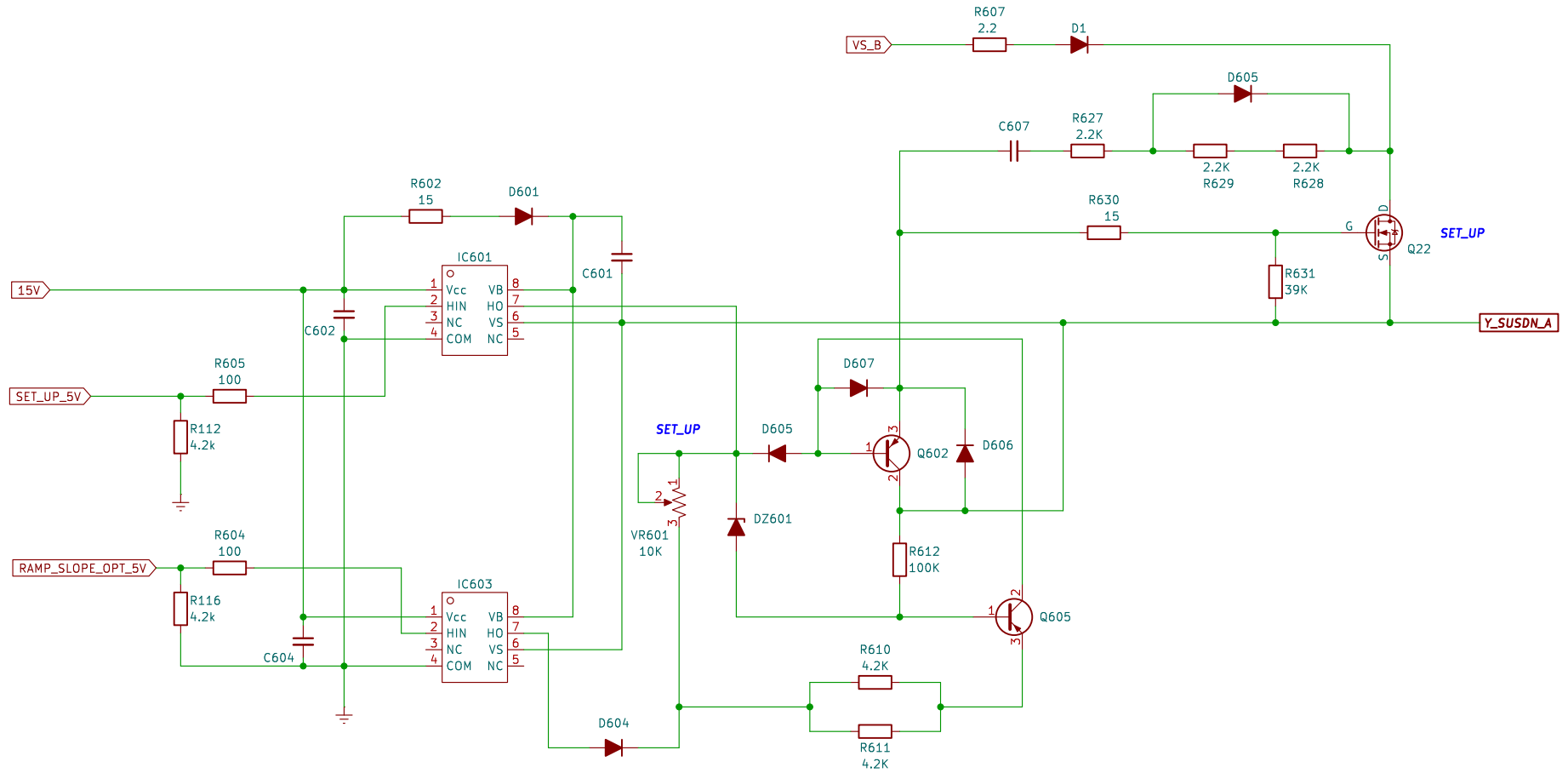
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File: y_er_up_and_sus_up.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9	Id: 4/8	

Rev: 1.0
Id: 4/8

SET UP



Set Up
Author: Fábio Pereira da Silva

Sheet: /SetUp/
File: set_up.kicad_sch

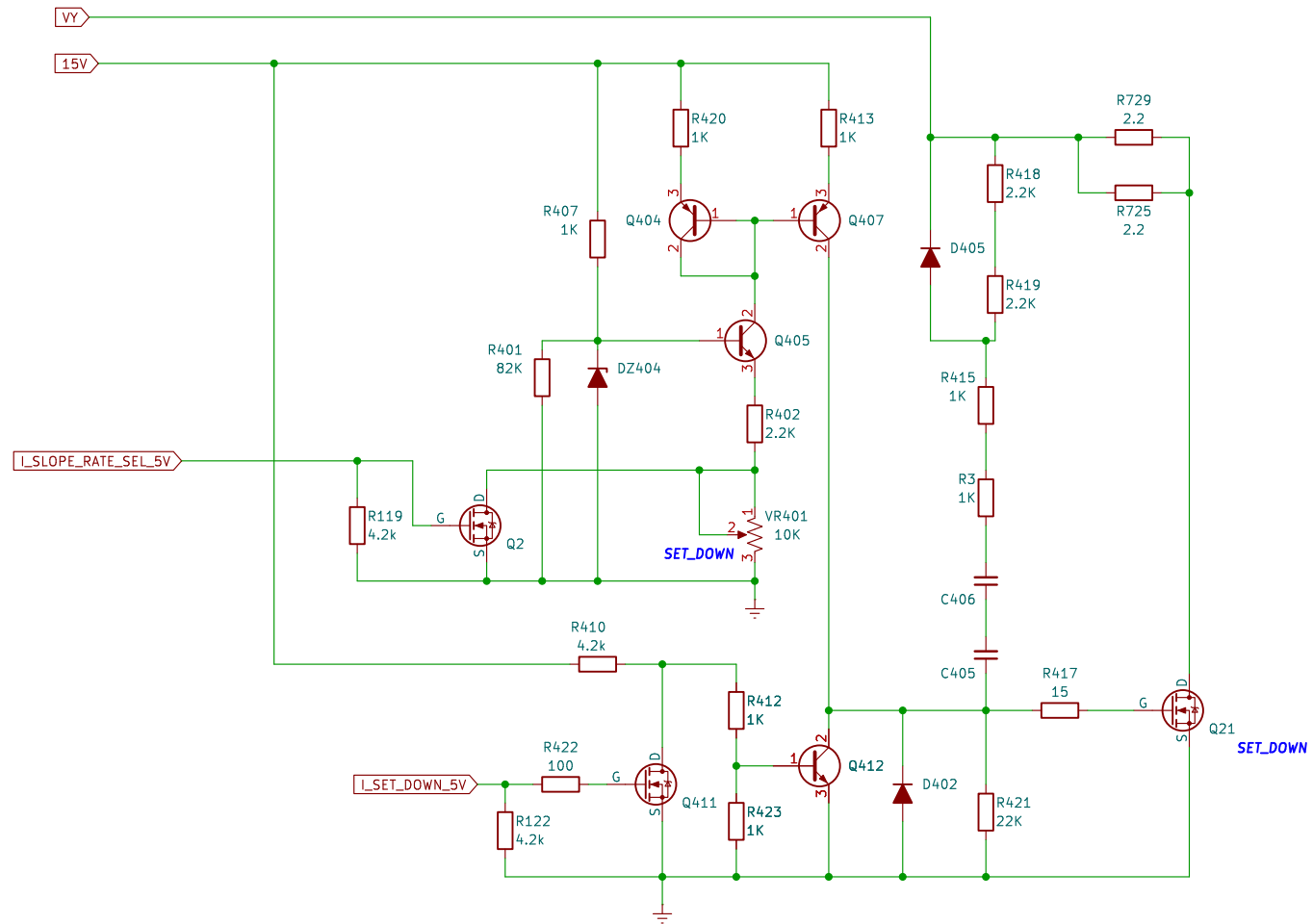
Title: Y BOARD LG PLASMA TV PANEL

Size: A4
Date: 2025-03-24

KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 5/8

SET DOWN



Author: Fábio Pereira da Silva

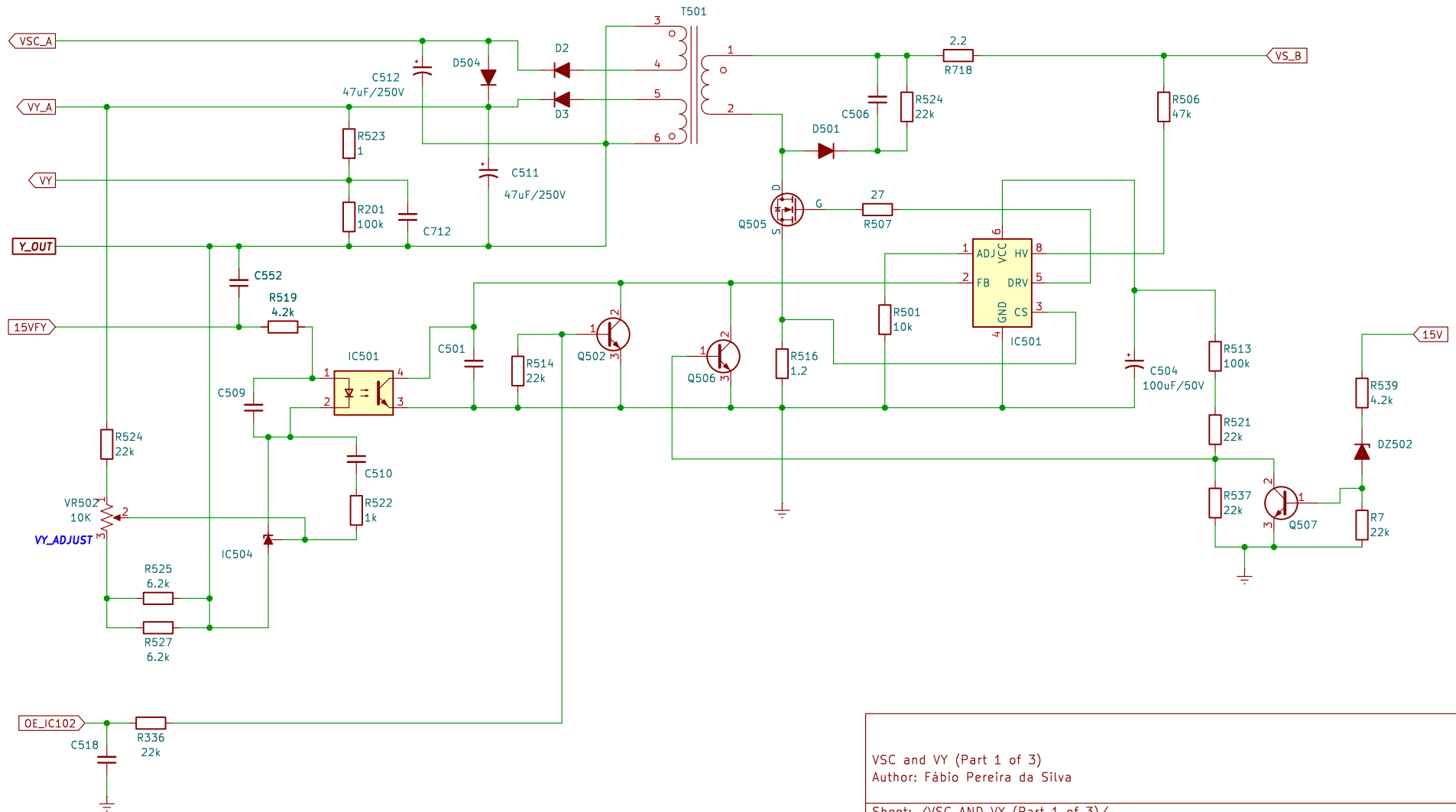
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File: set_down.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 6/8

VSC and VY (Part 1 of 3)



VSC and VY (Part 1 of 3)
Author: Fábio Pereira da Silva

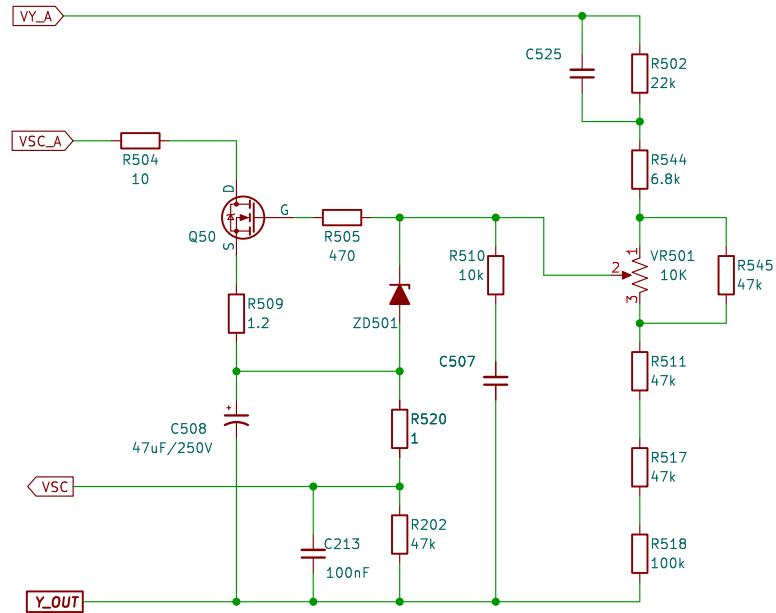
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File: vsc_and_vy.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24
KiCad E.D.A. 8.0.9

Rev: 1.0
Id: 7/8

VSC and VY (Part 2 of 3)



VSC and VY (Part 2 of 3)
Author: Fábio Pereira da Silva

Sheet: /VSC and VY (Part 2 of 3)/
File: vcs_and_vy_part2_of_3.kicad_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4	Date: 2025-03-24	Rev: 1.0
KiCad E.D.A. 8.0.9		Id: 8/8