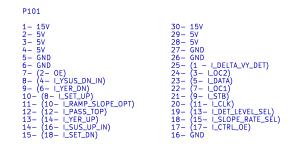
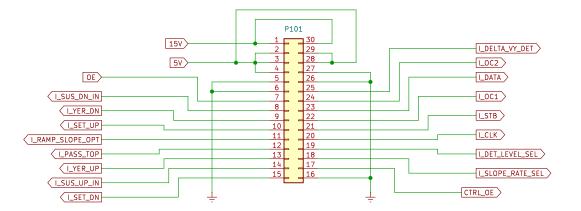


## Input Control (Part 1 of 3)





Input Control (Part 1 of 3) Author: Fábio Pereira da Silva

Sheet: /Input control (Part 1 of 3)/
File: input\_control.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

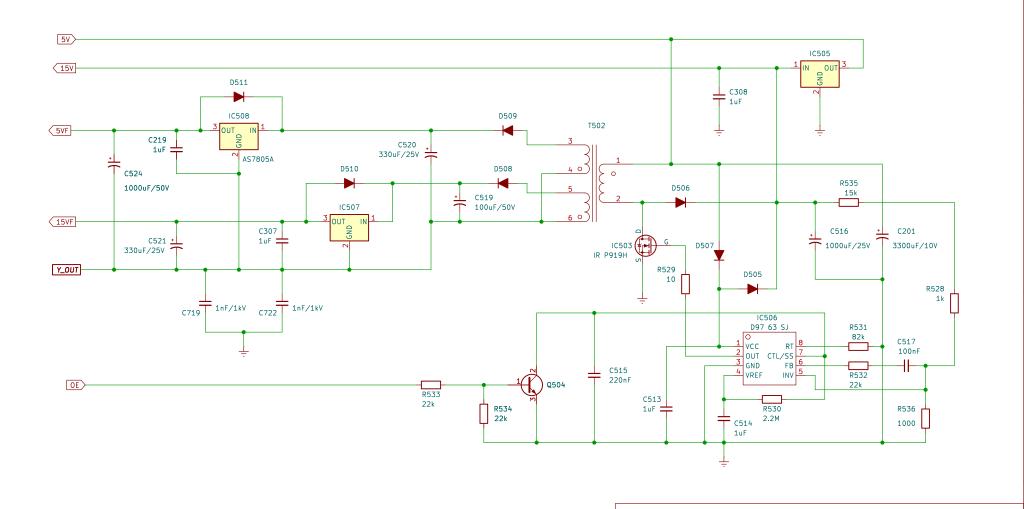
 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 2/23

#### Input Control (Part 2 of 3) R129 C102 C104 1nF I\_SLOPE\_RATE\_SEL\_5V I\_DET\_LEVEL\_SEL\_A CTRL\_OE R403 15k I\_CLK\_5V R123 4.7k IC102 I\_SLOPE\_RATE\_SEL Y0 Y1 Y2 Y3 Y4 Y5 Y6 I\_DET\_LEVEL\_SEL I\_CLK > R124 4.7k I\_0C1\_5V > I\_STB R109 4.2k I\_STB\_5V I\_OC1 I\_0C2\_5V > R125 4.7k I\_DATA > 19 G2 I\_OC2 S VHC T541A I\_DELTA\_VY\_DET OE> R111 4.7k R117 4.7k I\_DATA\_5V I\_DELTA\_VY\_DET\_5V Input Control (Part 2 of 3) Author: Fábio Pereira da Silva Sheet: /Input control (Part 2 of 3)/ File: input\_control2.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 3/23

#### Input Control (Part 3 of 3) **∓** 1uF CTRL\_OE Y\_SUS\_DOWN\_5V IC103 Y\_ER\_DOWN\_5V I\_SUS\_DN\_IN Y0 18 Y1 17 Y2 16 Y3 15 A0 A1 A2 A3 A4 A5 A6 A7 SET\_UP\_5V I\_YER\_DN RAMP\_SLOPE\_OPT\_5V I\_SET\_UP > BLOCKING\_5V Y5 13 Y6 12 I\_RAMP\_SLOPE\_OPT Y\_ER\_UP\_5V I\_PASS\_TOP > Y\_SUS\_UP\_5V I\_YER\_UP > G1 G2 SET\_DOWN\_5V I\_SUS\_UP\_IN VHC T541A I\_SET\_DN > R102 4.2k R104 4.2k R103 4.2k R101 4.2k R105 4.2k R108 4.2k Input Control (Part 3 of 3) Author: Fábio Pereira da Silva Sheet: /Input control (Part 3 of 3)/ File: input\_control3.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 4/23

### 5VF, 15VF and 15V Supply



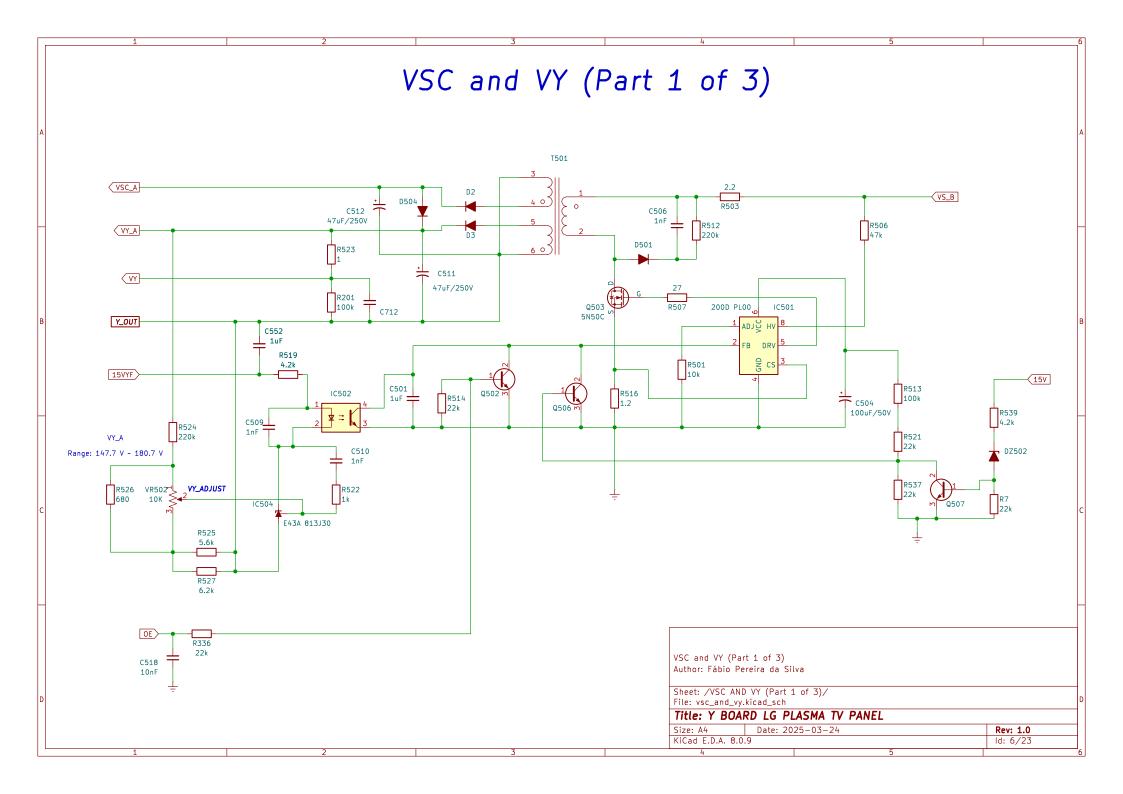
5VF, 15VF and 15V Supply Author: Fábio Pereira da Silva

Sheet: /5VF, 15VF 15v/ File: 5VF\_15VF\_15V.kicad\_sch

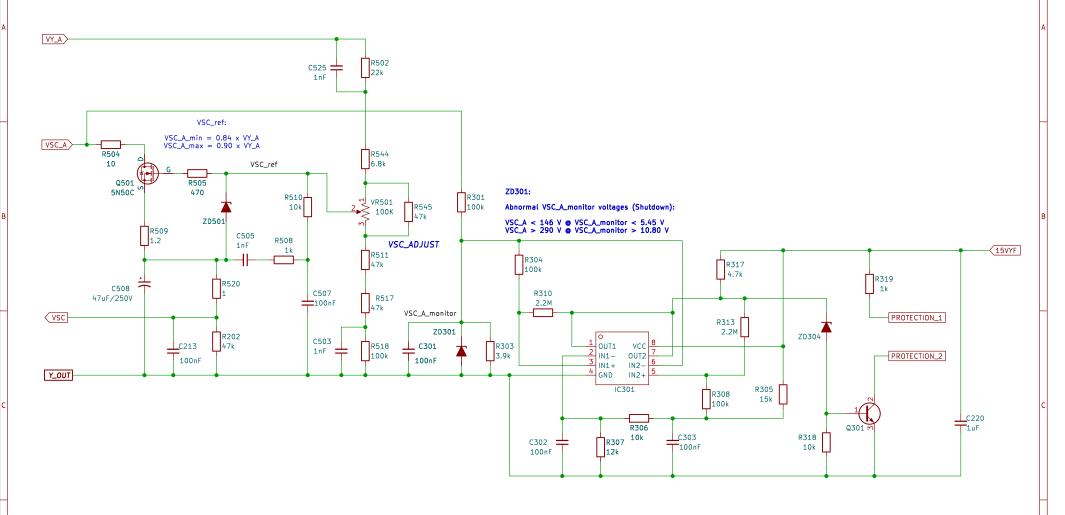
Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 5/23



# VSC and VY (Part 2 of 3)



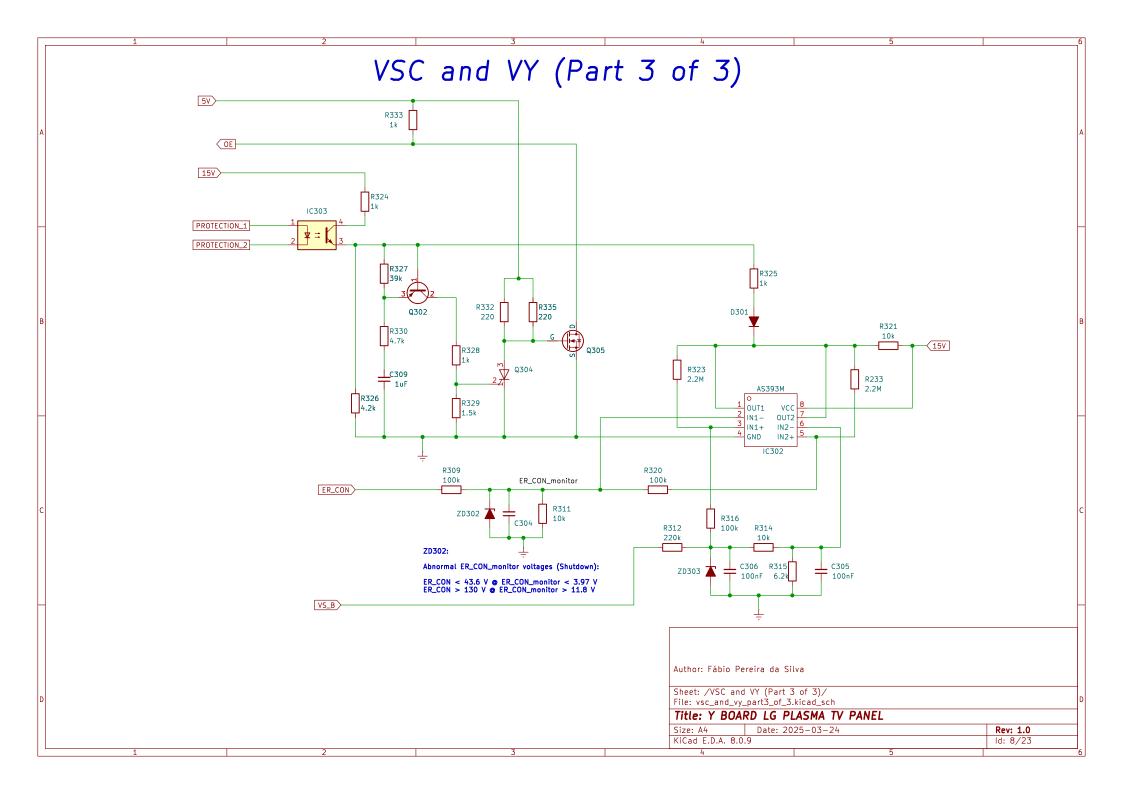
VSC and VY (Part 2 of 3) Author: Fábio Pereira da Silva

Sheet: /VSC and VY (Part 2 of 3)/
File: vcs\_and\_vy\_part2\_of\_3.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

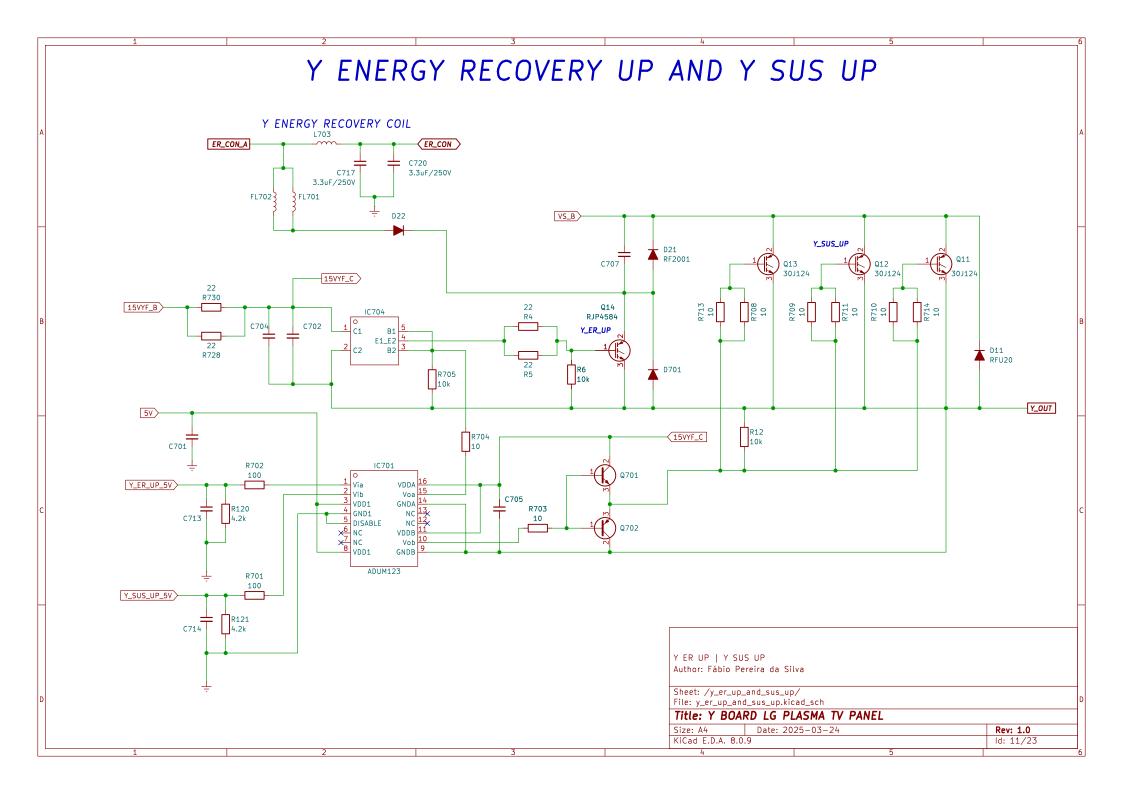
 Size: A4
 Date: 2025-03-24
 Rev: 1.0

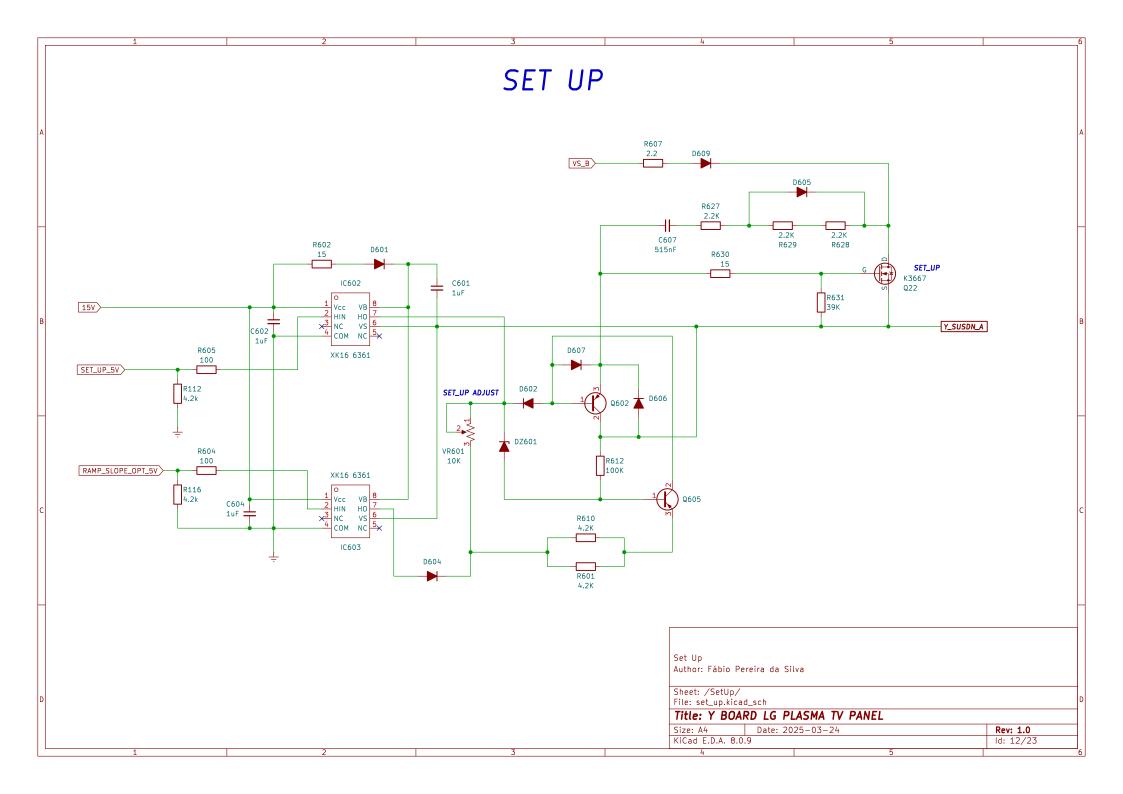
 KiCad E.D.A. 8.0.9
 Id: 7/23

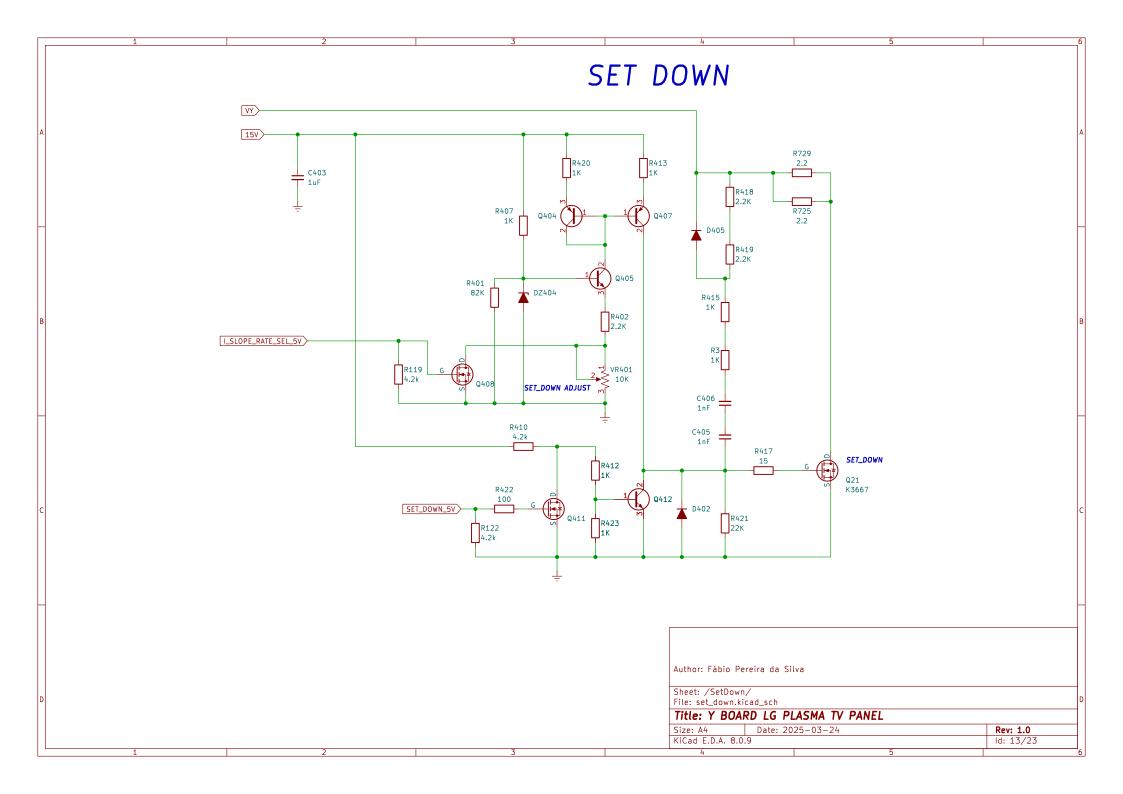


#### Y BLOCKING AND Y SUS DOWN 15VYF 15VYF\_B 330 C603 R603 R540 R541 4.2k 4.2k R542 R543 4.2k 4.2k R722 15 IC601 Unknown IC R606 2.2k <u>⊥</u> c605 <u>⊥</u> c606 <u>⊥</u> c608 BLOCKING\_5V 15V\_A > C708 100 C202 💳 330uF/25V R609 100 R118 4.2k 15 Y\_SUSDN\_A Q15 IC703 R719 R718 IRFP4332 100 D603 Vcc VB 10 Q16 D12 C16 RF200T 1nF/1kV 2 HIN HO 7 NC VS 6 COM NC 5 R724 Y\_SUS\_DOWN\_5V R611 C724 BLOCKING S Y\_SUS\_DOWN **★** D608 C710 R726 R114 4.2k R613 39k Q604 Y\_OUT Y\_BLOCKING | Y\_SUS\_DOWN Author: Fábio Pereira da Silva Sheet: /Y\_SUS\_DOWN\_AND\_Y\_BLOCKING/ File: y\_sus\_dn\_and\_blocking.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 9/23

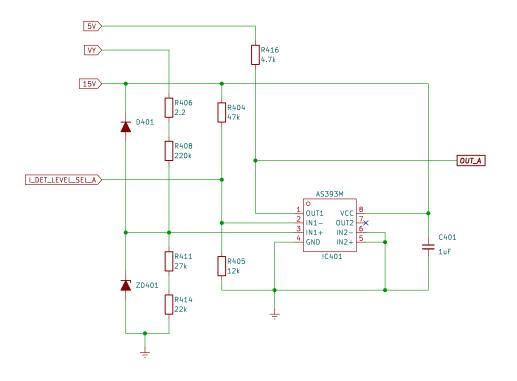
#### Y ENERGY RECOVERY DOWN Y\_SUSDN\_A FL706 FL705 } R721 2.2 R717 D711 10 ★ D710 Q23 RJP4584 \_\_\_\_ C711 15V\_A Y\_ER\_DOWN 10 IC702 R2 4.2k <del>+</del> C709 1 Vcc VB 8 7 6 6 COM NC 5 5 R720 R723 100 Y\_ER\_DOWN\_5V R115 4.2k RF2001 C706 \_\_\_\_\_ 521850 D23 ₩ RF2001 1nF/1kV ER\_CON\_A Y\_ER\_DOWN R742 C723 1nF/1kV Y ER DOWN Author: Fábio Pereira da Silva Sheet: /y\_er\_down/ File: y\_er\_down.kicad\_sch Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 10/23







## OUPUT CONTROL (Part 1 of 4)



Output Control (Part 1 of 4) Author: Fábio Pereira da Silva

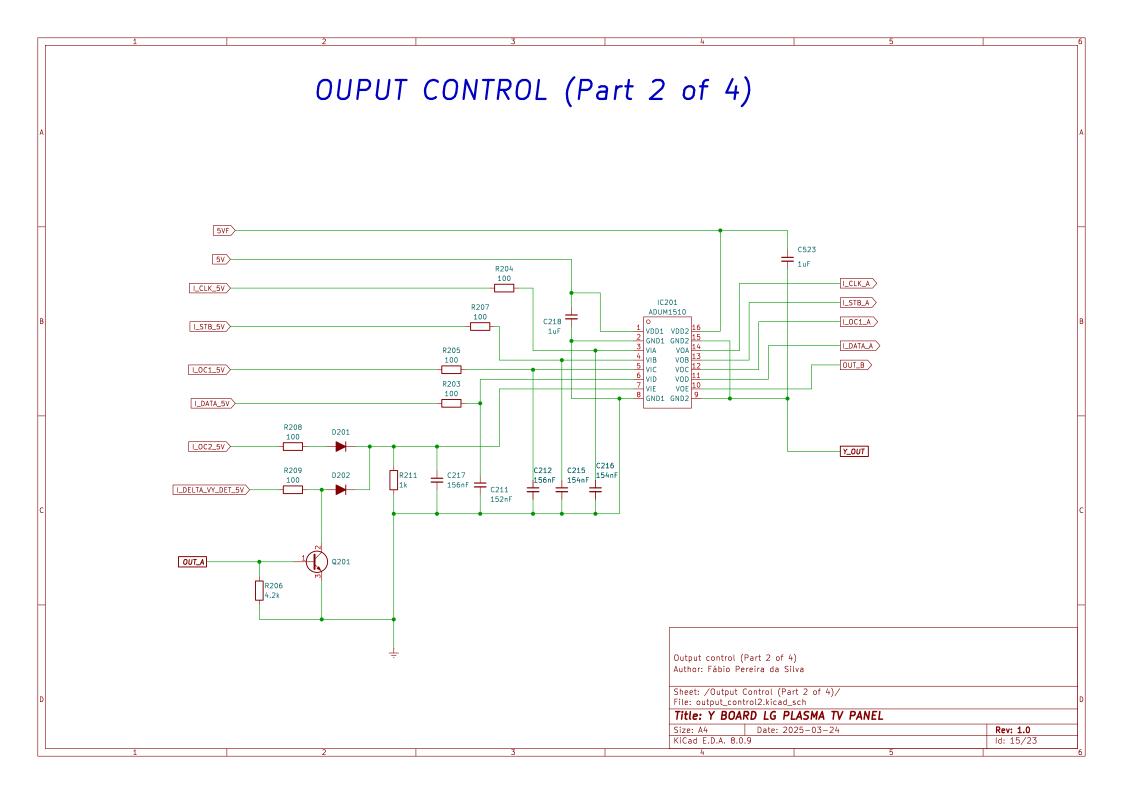
Sheet: /Output Control (Part 1 of 4)/

File: output\_control.kicad\_sch

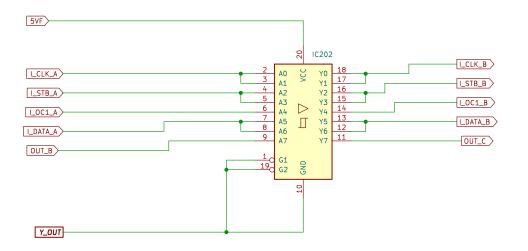
Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 14/23



## OUPUT CONTROL (Part 3 of 4)



Output control (Part 3 of 4) Author: Fábio Pereira da Silva

Sheet: /Output Control (Part 3 of 4)/

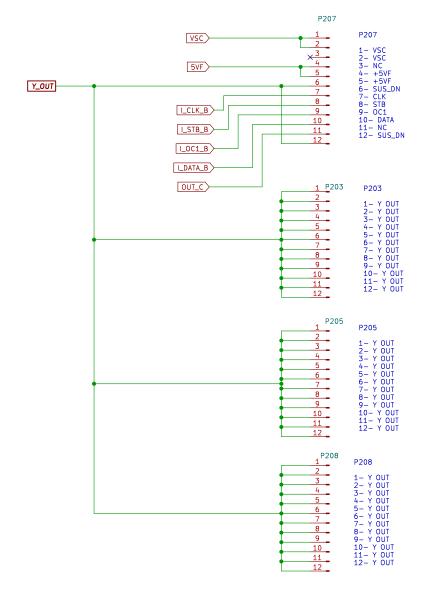
File: output\_control3.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 16/23

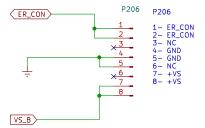
### OUPUT CONTROL (Part 4 of 4)



#### NOTES:

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Serigraphy notation in P207 (Y Out) is called SUS\_DN
 Pin 11 in P207 in Serigraphy is NC but is connected to output pin 11 in IC 202



Output Control (Part 4 of 4) Author: Fábio Pereira da Silva

Sheet: /Output Control (Part 4 of 4)/
File: output control4.kicad sch

Title: Y BOARD LG PLASMA TV PANEL

 Size: A4
 Date: 2025-03-24
 Rev: 1.0

 KiCad E.D.A. 8.0.9
 Id: 17/23

### Measurements (P201)

#### P201

Res (OHMS) Reverse bias (V) 1 > 200k2 > 200k6 - > 1 M7 - > 1 M8-0 9 - 2.2 k10 - 2.2 k

1 - 0.5512- 0.551 3 — — 4- 0 6-01 7- 01 8- 0 9-0.504 10 - 0.504 Foward bias (V) 1 - 012- OL 3- -4- 0 6 - 017- 01 8-0 9 - 1.1 10 - 1.1

MEASUREMENTS (P201) Author: Fábio Pereira da Silva

Sheet: /Measurements P201/ File: measurements.kicad sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 18/23

### Measurements (P206)

P206

1 - 110k 1 - 0.7692 - 110k3- OL 4- 0 6 - 017- OL 8- 01

Res (OHMS) Reverse bias (V) 2- 0.769 3-01 4- 0 6- OL 7- 0.550 8- 0.550

Foward bias (V) 1 - 0L2- 01 3- 01 4- 0 6 - 017- OL 8- 01

> Measurements P206 Author: Fábio Pereira da Silva

Sheet: /Measurements P206/ File: measurements\_p206.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 19/23

#### Measurements (P101) P101 Reverse bias (V) Res (OHMS) Foward bias (V) 1 - 0.4951 - 9171 - 0.9452 - 0.4952- 918 2 - 0.9443 - 0.5033 - 2.264k3 - 1.0894- 0.503 4 - 2.240k4 - 1.0905 - 0.5025 - 2.220k5 - 1.0896 - 0.5036 - 2.240k6 - 1.0897-0.503 7 - 2.242k7- 1.089 8 - 08 - 08 - 09 - 09 - 09-0 10 - 010 - 010 - 011 - 011 - 011 - 012 - 0.65812- 01 12 - 0113 - 0.61413 - 3.2k13 - 1.80414 - 0.65914 - 0114 - 0115 - 0.65015 - 2.72315 - 4.699k16 - 0.65916 - 0116- OL 17 - 0.65017 - 4.695k17 - 2.72218 - 0.66018- 01 18- OL 19-0.651 19 - 4.708k19 - 2.72420 - 0.66020 - 0120 - 0121 - 0.65721- 01 21 - 0122- 0.660 22- OL 22 - 0123 - 0.65023 - 4.689k23 - 2.72024 - 0.66024- 01 24 - 0125 - 0.65025 - 4.676k25 - 2.71826 - 0.66026 - 0126 - 0127 - 0.65027 - 4.699k27 - 2.723Measurements (P101) 28 - 0.617Author: Fábio Pereira da Silva 28 - 7.7k28 - 2.92629 - 0.649Sheet: /Measurements P101/ 29 - 4.685k29 - 2.720File: measurements p101.kicad sch 30 - 030 - 030 - 0Title: Y BOARD LG PLASMA TV PANEL Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 20/23

### Measurements (P202)

P202

4- OL 5 - > 50M6 - > 50M 6 - OL7 - > 50M 7 - 01

Res (OHMS) Reverse bias (V) 1 - 0 2- 0 5- OL

Foward bias (V) 1 - 0 2- 0 6 - 017- 01

Measurements (P202) Author: Fábio Pereira da Silva

Sheet: /Measurements P202/ File: measurements\_p202.kicad\_sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 21/23

### Measurements (P207)

#### NOTES:

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Measure is related to floating ground (Y OUT)

#### P207

Res (OHMS) Reverse bias (V) Foward bias (V) 1 - 3.2091 - 29.2 k2 - 29.2k2- 3.209 3-01 3-01 4 - 5.97k4- 0.590 5 - 5.97k5 - 0.5906 - 06 - 07-0.609 7 - 744k8 - 742k8 - 0.6009 - 795k 9 - 0.64110 - 740k10 - 0.60111 - 790k11- 0.659 12 - 012- 0

1 - 012 - 013-01 4- 1.916 5 - 1.9167-1.464 8- 1.466 9 - 1.57610 - 1.49011 - 1.56112 - 0

Author: Fábio Pereira da Silva

Sheet: /Measurements P207/ File: measurements p207.kicad sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 22/23

### Measurements (P203, P208 and P205)

- All pins are Y Out connection (Floating ground)

1-> Y OUT related to Ground

Res (OHMS) Reverse bias (V) Foward bias (V) 210k

2-> Y OUT related to +VS

Res (OHMS)

Reverse bias (Black point in VS and Red point in Y Out) (V) 0.402

Foward bias (Red point in VS and Black point in Y Out) (V)

Measurements (P203, P208 and P205) Author: Fábio Pereira da Silva

Sheet: /Measurements P203, P208 and P205/ File: measurements p203 p208 p205.kicad sch

Title: Y BOARD LG PLASMA TV PANEL

Size: A4 Date: 2025-03-24 Rev: 1.0 KiCad E.D.A. 8.0.9 ld: 23/23