



Devin Patel

Computer Science and Engineering, B.S.



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Yorba Linda, Irvine



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About me

I am currently a Full Stack Engineering Intern at Chaparral 300 and a full time student at the University of California, Irvine, graduating in June 2019. I am well versed in various technical aspects in both the computer software and computer hardware industries.

Skills

Python

Node-RED

C / C++

SQL / MySQL

System Verilog

HTML / CSS



education

- June 2019 Computer Science and Engineering, B.S.
University of California, Irvine
- 2011-2015 High School Diploma
Yorba Linda High School

awards

- 2015-2017 Dean's Honor List
- Attained various quarters under the Henry Samueli School of Engineering by having a GPA of 3.5 or higher
- 2015 Crimson Level Scholar
- Earned by having a GPA of 3.5 or higher, for completing at least 8 AP or Honors Courses, and for completing at least 100 recorded hours of Community Service
- 2015 National AP Scholar
- Granted to students who receive an average score of at least 4 on all AP Exams taken, and scores of 4 or higher on eight or more of these exams

work experience

- Current Full Stack Software Engineering Intern Chaparral 300
- Integrate legacy PLC's, IR sensors, IP cameras, and various other devices via proper networking and ethernet I/O modules
 - Use OpenCV to track bowling balls' trajectories down a lane and measure speed accurate to 3.33 milliseconds
 - Use Node-RED to develop a local networked based PLC (using modubs, http post/get requests, TCP/UDP requests) for a fully interconnected system to control bowling game machines
 - Design and solely code a fully functional MySQL database with proper keys, constraints, triggers, and procedures
- 2016-2017 Community Assistant Vista del Campo Norte
- Created community engaging and career building programs for residents to promote self-growth and raise awareness of various on campus resources
 - Facilitated relations between clients and administrative staff to run day-to-day operations
 - Responded to spontaneous, emergency situations when on call
 - Operate the apartment community's front desk, assisting walk-ins, taking phone calls, and providing property tours

technical experience

- Sys Verilog RISC-V Single Cycle Processor
- Single cycle processor that follows the RISC-V instruction set
 - Has several modules such as Datapath, Controller, ALUController, ALU, RegFile, DataMemory, Mux2, Mux4.
- C Embedded Alarm Date and Time Clock
- Embedded Software project using C and Atmel ICE programming
 - Uses an LCD for output and keypad for time and date change inputs
 - Plays alarm songs out loud when an alarm goes off
- React Kaiser Permanente Appointment Schedule System
- 36-hour hackathon at UCLA in which my team and I made a Kaiser Permanente Appointment Scheduling System using React Javascript, Bootstrap, and Google Firebase