B.Tech - Electrical Engineering Indian Institute Of Technology, Dharwad

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#### EDUCATION

Degree/Certificate	${\bf Institute/Board}$	CGPA/Percentage	Year
B.Tech	Indian Institute of Technology, Dharwad	7.52	2020-Present
Senior Secondary	CBSE Board	91.6%	2019
Secondary	CBSE Board	92.6%	2017

### **PROJECTS**

#### • High Voltage Isolated Differential Probe For Voltage of 600

Jan. 2023 - Apr. 2023

Supervisor: Prof Abhijit Kshirsagar

- Designed a precision voltage attenuation and an isolated differential amplifier for cost-effective measurements up to 600V.
- Our design offers an affordable high-voltage probe with high input impedance, ensuring accurate data capture for diverse laboratory applications.

#### • Wireless Mobile Charger

Aug. 2022 - Nov. 2022

Supervisor: Prof Satish Naik Banavath

- Accomplished the successful design, implementation, and testing of a wireless charging circuit using a forward converter, showcasing the principles of electromagnetic induction.
- Utilized a TL494 controller to achieve precise power control, enhancing its suitability for diverse electronic devices.

### • Real-time Implementation of High-Performance FIR Filters on TMS320C5515

Jan. 2022 - Apr. 2022

Supervisor: Prof Mahadev prasanna

 Achieved signal processing optimization by designing and implementing MATLAB-based FIR and IIR filters for TMS320C5515 DSP hardware. Demonstrated proficiency in Ideal FIR filters using diverse windowing techniques(Rectangular, Hanning, Hamming).

# **PUBLICATIONS**

 Prakash, Devi Prasad Karri et al., Ultra Low-Cost High-Voltage Isolated Differential Probe for Teaching and Training, International conference on Power Electronics, Smart Grid, and Renewable Energy (PESGRE), 2023, Trivandum, Kerela. (Accepted)

# TECHNICAL SKILLS

- Programming Languages: Verilog, Perl Matlab, C/C++, Python, 8085, and 8051.
- Tools and Technologies: Cadence Virtuoso 180nm, LTSPICE, Ngspice, and KI-CAD.
- Skills: CMOS Analog design, Analog layout design, Mixed signal IC design.

#### KEY COURSES TAKEN

- NPTEL: Analog Electronics (secured 80.00), Analog IC design (on-going)
- Electrical: Analog Electronics, Basic Electrical and Electronic circuits, Signals and Systems , Control Systems

## Interests

Analog designing, Digital designing, and Mixed designing.

# Positions of Responsibility

- Secretary: Volleyball Club, IIT Dharwad 2023
- **Director:** IIT dharwad Cultural fest 2023
- Director: Sayonara Soiree (Farewell) 2023

#### ACHIEVEMENTS

- IIT JEE ADVANCED 2020: Qualified and obtained 12000 All India Rank (AIR) out of 0.2 million students
- Captain: Inter IIT volleyball team 2022
- Captain: Inter-hostel Volleyball team