

# Module Guide: Library of Linear Algebraic Equation Solver

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# 1 Revision History

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## 2 Introduction

Decomposing a system into modules is a commonly accepted approach to developing software. A module is a work assignment for a programmer or programming team (Parnas et al., 1984). We advocate a decomposition based on the principle of information hiding (Parnas, 1972). This principle supports design for change, because the “secrets” that each module hides represent likely future changes. Design for change is valuable in SC, where modifications are frequent, especially during initial development as the solution space is explored.

Our design follows the rules laid out by Parnas et al. (1984), as follows:

- System details that are likely to change independently should be the secrets of separate modules.
- Each data structure is used in only one module.
- Any other program that requires information stored in a module’s data structures must obtain it by calling access programs belonging to that module.

After completing the first stage of the design, the Software Requirements Specification (SRS), the Module Guide (MG) is developed (Parnas et al., 1984). The MG specifies the modular structure of the system and is intended to allow both designers and maintainers to easily identify the parts of the software. The potential readers of this document are as follows:

- New project members: This document can be a guide for a new project member to easily understand the overall structure and quickly find the relevant modules they are searching for.
- Maintainers: The hierarchical structure of the module guide improves the maintainers’ understanding when they need to make changes to the system. It is important for a maintainer to update the relevant sections of the document after changes have been made.
- Designers: Once the module guide has been written, it can be used to check for consistency, feasibility and flexibility. Designers can verify the system in various ways, such as consistency among modules, feasibility of the decomposition, and flexibility of the design.

The rest of the document is organized as follows. Section 3 lists the anticipated and unlikely changes of the software requirements. Section 4 summarizes the module decomposition that was constructed according to the likely changes. Section 5 specifies the connections between the software requirements and the modules. Section 6 gives a detailed description of the modules. Section 7 includes two traceability matrices. One checks the completeness of the design against the requirements provided in the SRS. The other shows the relation between anticipated changes and the modules. Section 8 describes the use relation between modules.

## 3 Anticipated and Unlikely Changes

This section lists possible changes to the system. According to the likeliness of the change, the possible changes are classified into two categories. Anticipated changes are listed in Section 3.1, and unlikely changes are listed in Section 3.2.

### 3.1 Anticipated Changes

Anticipated changes are the source of the information that is to be hidden inside the modules. Ideally, changing one of the anticipated changes will only require changing the one module that hides the associated decision. The approach adapted here is called design for change.

**AC1:** The format of initial input data.

**AC2:** The format of input parameters.

**AC3:** The format of final output data.

**AC4:** Change of algorithm for solving the linear algebraic equation.

**AC5:** Change of underlying abstract machine.

### 3.2 Unlikely Changes

The module design should be as general as possible. However, a general system is more complex. Sometimes this complexity is not necessary. Fixing some design decisions at the system architecture stage can simplify the software design. If these decision should later need to be changed, then many parts of the design will potentially need to be modified. Hence, it is not intended that these decisions will be changed.

**UC1:** When there are complex equations involved.

**UC2:** There will always be a source of input data external to the software.

**UC3:** If the number of equations are greater than the no of parameters where no functional form of the equation is repeated twice.

**UC4:** If the number of equations are less than the no of parameters where no functional form of the equation is repeated twice.

## 4 Module Hierarchy

This section provides an overview of the module design. Modules are summarized in a hierarchy decomposed by secrets in Table 1. The modules listed below, which are leaves in the hierarchy tree, are the modules that will actually be implemented.

- M1:** Hardware-Hiding Module
- M2:** Input Format Module
- M3:** Input Parameter Module
- M4:** Input Verification Module
- M5:** Output Format Module
- M6:** Control Module
- M7:** Gaussian Elimination Module
- M8:** Gauss-Jordan Elimination Module

Level 1	Level 2
Hardware-Hiding Module	
	Input Format Module
	Input Parameter Module
	Input verification Module
Behaviour-Hiding Module	Output Format Module
	Control Module
	?
	?
	?
	?
Software Decision Module	Gaussian Elimination Module
	Gauss-Jordan Elimination Module
	?

Table 1: Module Hierarchy

## 5 Connection Between Requirements and Design

The design of the system is intended to satisfy the requirements developed in the SRS. In this stage, the system is decomposed into modules. The connection between requirements and modules is listed in Table 2.

## 6 Module Decomposition

Modules are decomposed according to the principle of “information hiding” proposed by Parnas et al. (1984). The *Secrets* field in a module decomposition is a brief statement of the design decision hidden by the module. The *Services* field specifies *what* the module will do without documenting *how* to do it. For each module, a suggestion for the implementing software is given under the *Implemented By* title. If the entry is *OS*, this means that the module is provided by the operating system or by standard programming language libraries. Also indicate if the module will be implemented specifically for the software.

Only the leaf modules in the hierarchy have to be implemented. If a dash (–) is shown, this means that the module is not a leaf and will not have to be implemented. Whether or not this module is implemented depends on the programming language selected.

### 6.1 Hardware Hiding Modules (M1)

**Secrets:** The data structure and algorithm used to implement the virtual hardware.

**Services:** Serves as a virtual hardware used by the rest of the system. This module provides the interface between the hardware and the software. So, the system can use it to display outputs or to accept inputs.

**Implemented By:** OS

### 6.2 Behaviour-Hiding Module

**Secrets:** The contents of the required behaviours.

**Services:** Includes programs that provide externally visible behaviour of the system as specified in the Commonality Analysis (CA) documents. This module serves as a communication layer between the hardware-hiding module and the software decision module. The programs in this module will need to change if there are changes in the CA.

**Implemented By:** –

#### 6.2.1 Input Format Module (M2)

**Secrets:** The format and structure of the input data.

**Services:** Converts the input data into the data structure used by the input parameters module.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

### 6.2.2 Input Parameter Module(M3)

**Secrets:** The format and structure of the input parameter.

**Services:** Stores the data type, numerical parameters and the parameters which are needed for the program.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

### 6.2.3 Input Verification Module(M4)

**Secrets:** The format and structure of the Software Constraints.

**Services:** Verifies whether the the given input parameters matches and follows the Software constraints. Shows an error if the input does not follow the Software constraints.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

### 6.2.4 Output Format Module(M5)

**Secrets:** The format and structure of the Output Data.

**Services:** Displays the output for the system of linear equations which are given as the input data.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

### 6.2.5 Control Module(M6)

**Secrets:** Runs the program by harmonizing the different algorithms.

**Services:** Coordinates and runs the main program.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

## 6.3 Software Decision Module

**Secrets:** The design decision based on mathematical theorems, physical facts, or programming considerations. The secrets of this module are *not* described in the CA.

**Services:** Includes data structure and algorithms used in the system that do not provide direct interaction with the user.

**Implemented By:** –



### 6.3.1 Gaussian Elimination Module(M7)

**Secrets:** Algorithm for solving linear algebraic equations for the given input.

**Services:** Calculates the output based on the input data.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

### 6.3.2 Gauss-Jordan Elimination Module(M8)

**Secrets:** Algorithm for solving linear algebraic equations for the given input.

**Services:** Calculates the output based on the input data.

**Implemented By:** [Library of Linear Algebraic Equation Solver]

## 7 Traceability Matrix

This section shows two traceability matrices: between the modules and the Instance Models and between the modules and the anticipated changes.

Req.	Modules
IM1	M1, M2, M3, M4, M??, M6, M7, M8
IM2	M1, M2, M3, M4, M??, M6, M7, M8

Table 2: Trace Between Instance Models and Modules

AC	Modules
AC1	M2
AC2	M3
AC3	M5
AC4	M7 M8
AC5	M??

Table 3: Trace Between Anticipated Changes and Modules

## 8 Use Hierarchy Between Modules

In this section, the uses hierarchy between modules is provided. Parnas (1978) said of two programs A and B that A *uses* B if correct execution of B may be necessary for A to complete the task described in its specification. That is, A *uses* B if there exist situations in which the correct functioning of A depends upon the availability of a correct implementation of

B. Figure 1 illustrates the use relation between the modules. It can be seen that the graph is a directed acyclic graph (DAG). Each level of the hierarchy offers a testable and usable subset of the system, and modules in the higher level of the hierarchy are essentially simpler because they use modules from the lower levels.

Figure 1: Use hierarchy among modules

## References

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