Sylvain KRIEG

Software Engineer

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 github | linkedin

EXPERIENCE

CAPGEMINI ENGINEERING
01.2024 -

SENIOR SOFTWARE CONSULTANT - MULTINATIONAL MANUFACTURER OF COMPUTER PERIPHERALS
- Development of automated test framework in Python
- FPGA development for high-speed hardware testing

FLYBOTIX
08.2023 - 12.2023

SENIOR FIRMWARE ENGINEER
- C++ software development for indoor drone
- Linux Driver development for Nvidia tegra platform

ASTROCAST S.A.
03.2021-06.2023

SENIOR SOFTWARE ENGINEER
- Firmware development in C on STM32 for low power satellite receivers
- Software architecture, requirements and documentation
- Hardware interface library development in C++
- Azure DevOps CI pipelines configuration and Azure cloud management
- Implementation of communication encryption and device security

ECOROBOTIX S.A.
09.2016-02.2021

EMBEDDED SOFTWARE ENGINEER
- C firmware development on STM32 for an autonomous weeding robot
- C++ and Java software development for GPS-RTK positioning system
- GPU image processing using Cuda and OpenCV
- Linux ARM board configuration and kernel object development
- Set up cloud remote monitoring and update tool (aws, ssh, websocket)

HEIG-VD, REDS
09.2013-09.2015

SCIENTIFIC COLLABORATOR
- FPGA development and testing for a 100Gb/s compression platform
- Java development of Logisim, an open source electronic simulation software

DEPSYS
01.2013-08.2013

EMBEDDED SOFTWARE ENGINEER
- Development of a monitoring software for smart-grid distributed power inverters.
Websocket communication with embedded Linux platform in Java

BOSCHUNG MECATRONIC
10.2010-09.2012

EMBEDDED SOFTWARE ENGINEER
- Development of a firmware for airport weather sensors
- Firmware development in C on a dsPic32 using freeRTOS
- Real-time signal processing and low power management

ISMECA SEMICONDUCTOR MALAYSIA / SINGAPORE
10.2009-09.2010

SOFTWARE TRAINEE
- Development of a real-time acquisition software (C & Labview)

Education

Scrum Master PSM1 certification *2022*

HES-SO, Lausanne *2016*

M.S In Computer Science (part time studies)

• Thesis about high resolution image processing acceleration in C++ using Cuda GPU and OpenCV.

Haute Ecole d'Ingénierie et de Gestion de Vaud (Heig-VD), Yverdon-Les-Bains *2009*

B.S In Computer Science, Embedded Systems

Bobst S.A., Lausanne *2004*

CFC (Federal Certificate of Capability) in electronics

Skills

Programming Languages	C ǀ C++ ǀ Java ǀ Python ǀ Rust
Software Development	Git ǀ Jenkins ǀ Azure DevOps ǀ Jira ǀ Docker
Frameworks & Libraries	Cmake ǀ gTest/gMock ǀ ZeroMQ ǀ Protobuf
Sys admin	Linux (udev, systemd, user-groups, iptables) ǀ Ansible ǀ Aws (EC2) ǀ Azure cloud
Hardware Platforms	STM32 ǀ ATmega ǀ CUDA ǀ Nvidia ARM platform ǀ Altera FPGA
Languages	*Native:* French **C1:** English **B2:** German