

# Multigate CPU

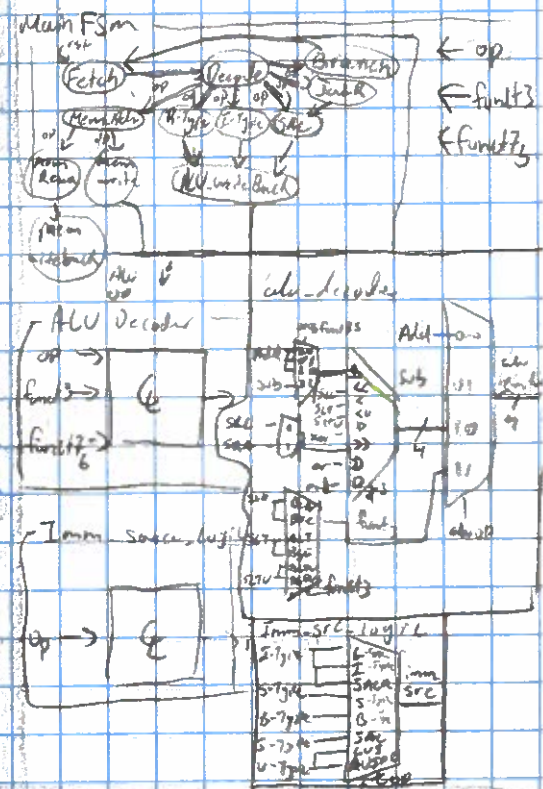
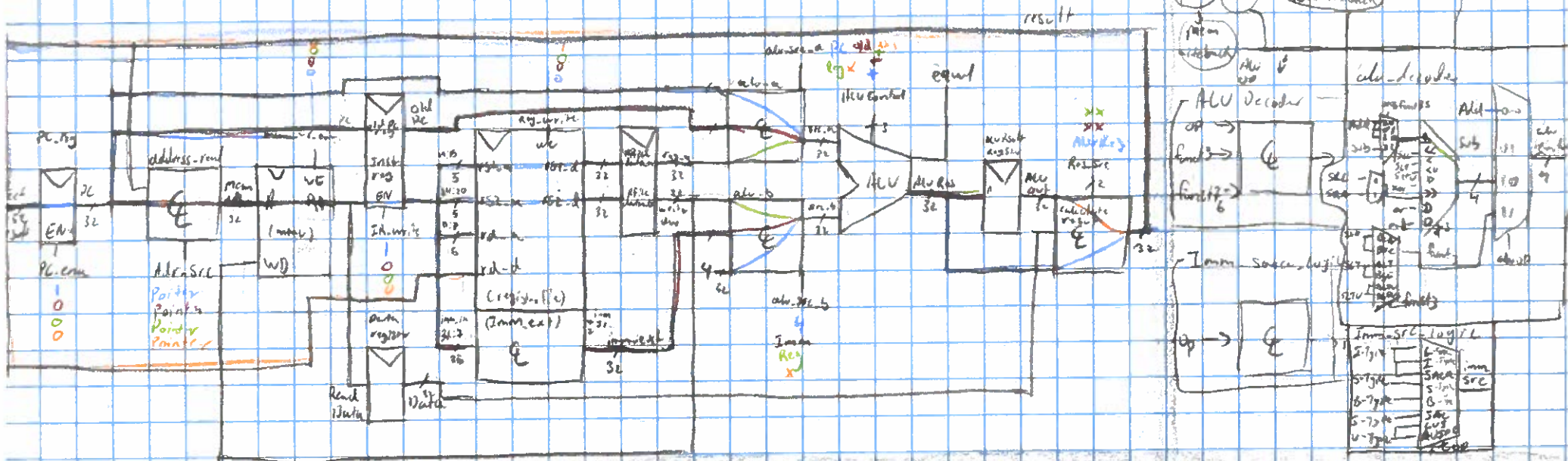
Fetch

Decode

R-Type (Add)

ALU Write Back

Control Unit



## Blocks

