

(6.5) The time taken by Machines A, B and C to execute a given task is:

$$A \text{ 16m, 9s : } 9\text{s} / 16\text{m} = 0.5625$$

$$B \text{ 14m, 12s : } 12\text{s} / 14\text{m} = 0.8571$$

$$C \text{ 12m, 47s : } 47\text{s} / 12\text{m} = 3.916$$

relative to A:

$$A = 1 \text{ A}$$

$$B = A/B = 0.6563 \text{ A}$$

$$C = C/A = 0.1426 \text{ A}$$

(6.6) Clock rate is a poor metric for performance because clock rates can be clocked arbitrarily quickly, but if it takes more than 1 clock cycle to execute an instruction, the performance can go down while increasing the clock rate. Clock speed, if correctly chosen for the processor, can indicate the speed with which one instruction will complete, so prior to multi-core and super scalar processors, it was an ok metric for performance of a processor.

(6.12) Would lowering the clock frequency by 15% be a good idea?

To answer this question, we will evaluate the performance as is, and with the 15% reduction

$$\text{As is: } 1(.45) + 3(.2) + 2(.1) + 2(.25) = 1.75$$

$$15\% \text{ reduction: } 1(1(.45) + 2(.2) + 2(.1) + 2(.25)) / (.85) = 1.823$$

No, not worth it

(6.13) Determine cycles per conditional branch for 20% improvement

$$\text{current performance: } 1(.65) + 5(.1) + 2(.05) + 8(.2) = 2.85$$

$$\text{Target performance: current performance} * 0.8 = 2.28$$

$$2.28 - [1(.65) + 5(.1) + 2(.05)] = x(.2)$$

$$x = 5.15$$

$$\text{target cycles per conditional branch} = 5$$

$$(6.17) \ S = \frac{1}{f_s + \frac{1-f_s}{P}}$$

$$(a) \ 10 \text{ processors, } f_s = 0.1 : P = 10 : S = \frac{1}{0.1 + \frac{1-0.1}{10}} = 5.26$$

$$(b) \ 100 \text{ processors, } f_s = 0.1 : P = 100 : S = \frac{1}{0.1 + \frac{1-0.1}{100}} = 9.17$$

$$(c) \ 5 \text{ processors, } f_s = 0.4 : P = 5 : S = \frac{1}{0.4 + \frac{1-0.4}{5}} = 1.92$$

$$(d) \ 100 \text{ processors, } f_s = 0.01 : P = 100 : S = \frac{1}{0.01 + \frac{1-0.01}{100}} = 50.25$$