☆ TEE (/TEE)

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This page provides you information regarding your upcoming Term End Examination.

Syllabus

- Unit 5
  - Analog Interfacing to Embedded Microprocessors, Real World Design, Stuart Ball; Second (https://www.amazon.com/Interfacing-Embedded-Microprocessor-Systems-Technology/dp/0750677236) or Third (https://www.amazon.com/Interfacing-Embedded-Microprocessor-Systems-Technology/dp/0750677236) Edition; *Chapters 1 and 2: System Design and Analog and Digital Converters*.
    - Video Lecture: Analog to Digital Conversion (https://www.youtube.com/watch?v=V8kDUl2OXqs&list=PL4C141B35706AD19A&index=35) (http://savefrom.net/?url=https%3A%2F%2Fwww.youtube.com%2Fwatch%3Fv%3DV8kDUl2OXqs%26list%3DPL4C141B35706A chrome&utm_medium=extensions&utm_campaign=link_modifier)
    - Video Lecture: Digital to Analog Conversion (https://www.youtube.com/watch?v=j1ZOFIpc-Mc&index=38&list=PL4C141B35706AD19A) (http://savefrom.net/?url=https%3A%2F%2Fwww.y Mc%26index%3D38%26list% chrome&utm_medium=extens
- Unit 4
  - Wayne Wolf Book, Chapter 8, Networked Embedded Systems
    - Second Edition
      - Introduction (Pages: 397-398)
      - Distributed Embedded System Architectures (Pages: 399 to 405)
      - Networks for Embedded Systems (Pages: 405 to 413)
      - Internet Enabled Systems (Pages: 416 to 419)
      - Vehicles as Networks (Pages: 421 to 425)
    - Third Edition (will be updated)
- Unit 3
  - Refer CAT-2 (/CAT-2) Page for page numbers and syllabus
- Unit 2
  - Refer CAT-2 (/CAT-2) Page for page numbers and syllabus
- Unit 1
  - Refer CAT-1 (/CAT-1) page for page numbers and syllabus
  - Read the following pages in these slides (https://drive.google.com/open?id=0B-2dPTXnV_COU0xyWDVZTjhhRzg) and may be you can also view some video lectures (https://www.youtube.com/playlist?list=PL0E131A78ABFBFDD0) .
    - 8051 Microcontrollers (Slide numbers 40 to 57)
    - 8051 Assembly Language Programming (Slide numbers 57 to 103)
    - Jump, Loop, and Call Instructions (Slide numbers 104 to 128)
    - I/O Port Programming (Slide numbers 129 to 156)
    - Addressing Modes (Slide numbers 157 to 194)0
    - Arithmetic and Logic Instructions Programs (Slide numbers 195 to 244)
    - Timer programming (Slide numbers 313 to 347)
      - Video Lecture (https://www.youtube.com/watch?v=0SZPr4iGACg) (http://savefrom.net/?url=https%3A%2F%2Fwww.youtube.com%2Fwatch%3Fv%3D0SZPr4iGACg&utm_source=u chrome&utm_medium=extensions&utm_campaign=link_modifier)
    - Serial Communication (Slide numbers from 358 to 410)
      - Video Lecture (https://www.youtube.com/watch?v=FOrs4ffP-Wc) (http://savefrom.net/?url=https%3A%2F%2Fwww.youtube.com%2Fwatch%3Fv%3DFOrs4ffP-Wc&utm_source=userjs-chrome&utm_medium=extensions&utm_campaign=link_modifier)
    - Interrupts Programming (Slide numbers 419 to 468)
      - Video Lecture (https://www.youtube.com/watch?v=CxtwG8B7ihA&list=PL0E131A78ABFBFDD0&index=17) (http://savefrom.net/?url=https%3A%2F%2Fwww.youtube.com%2Fwatch chrome&utm_medium=extensions&utm_campaign

Format

1. Part-A will contain 8 questions. Each question carries 5 marks and so in total 40 marks.
2. Part-B will contain choices, and you need to answer any 6 questions. Each question carries 10 marks, and so in total 60 marks.

Difficulty

1. The level of difficulty is 2/10. (CAT-1 difficulty level was 2/10 and CAT-2 difficulty level is 4/10)
2. The question paper is set to meet COE requirements:
   1. 25% easy questions; 50% average questions; 25% tough questions.
   2. Questions will cover all 5 units.
   3. 60% of the questions are HOT type questions; 40% tests lower order thinking abilities.
3. Unit 4 and Unit 5 will have only descriptive questions.

Expectations

1. The evaualtion will be strict in that marks will be awarded only for complete and correct answers.
2. Kindly write legibly and present your content neatly.
3. For 8051 assembly programming questions marks will be deducted for syntax errors even if logic is correct.

Open Hours Communication

- *Face to Face Conversation*: November 23 and 24 from 1000 to 1200.
- *Phone Calls*: If available, I will pick up the phone. If not I will call you back if I see a missed call.
- *What's app, E-mail*: I will reply within 24 hours.

Practice Term End

2. Compare and contrast Microprocessors and Microcontrollers.
3. Construct the control flow graph for the following program.

```
switch (z)
{
    case(0):
    {
        P0='0';
        break;
    }
    case(1):
    {
        P0='1';
        break;
    }
    case(2):
    {
        P0='2';
        break;
    }
    case(3):
    {
        P0='3';
        break;
    }
}
```
  a.
4. Construct the symbol table for the following 8051 assembly language program.

```
        ORG    0
BACK:   MOV    A,#55H
        MOV    P0,A
        MOV    P1,A
        MOV    P2,A
        ACALL  QSDELAY
        MOV    A,#0AAH
        MOV    P0,A
        MOV    P1,A
        MOV    P2,A
        ACALL  QSDELAY
        SJMP   BACK
QSDELAY:
        MOV    R5,#11
H3:     MOV    R4,#248
H2:     MOV    R3,#255
H1:     DJNZ   R3,H1
        DJNZ   R4,H2
        DJNZ   R5,H3
        RET
        END
```

5. Construct the Control Flow Graph for the following 8051 assembly language program given in Question number 4.

💬 Add Discussion