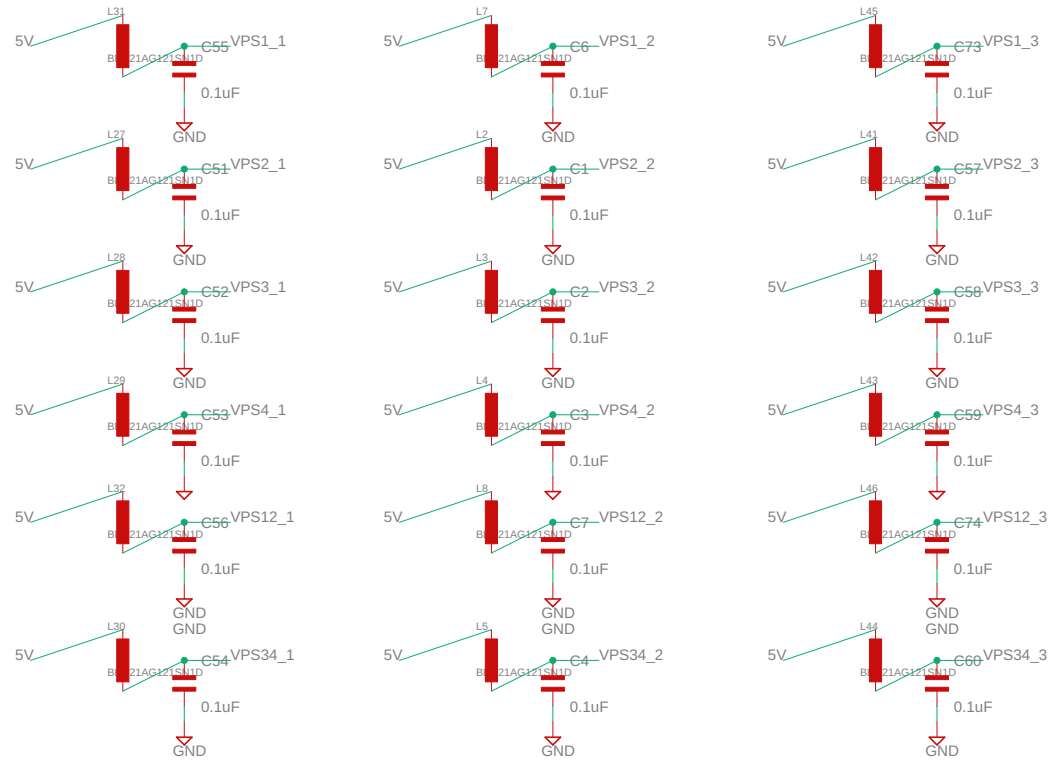
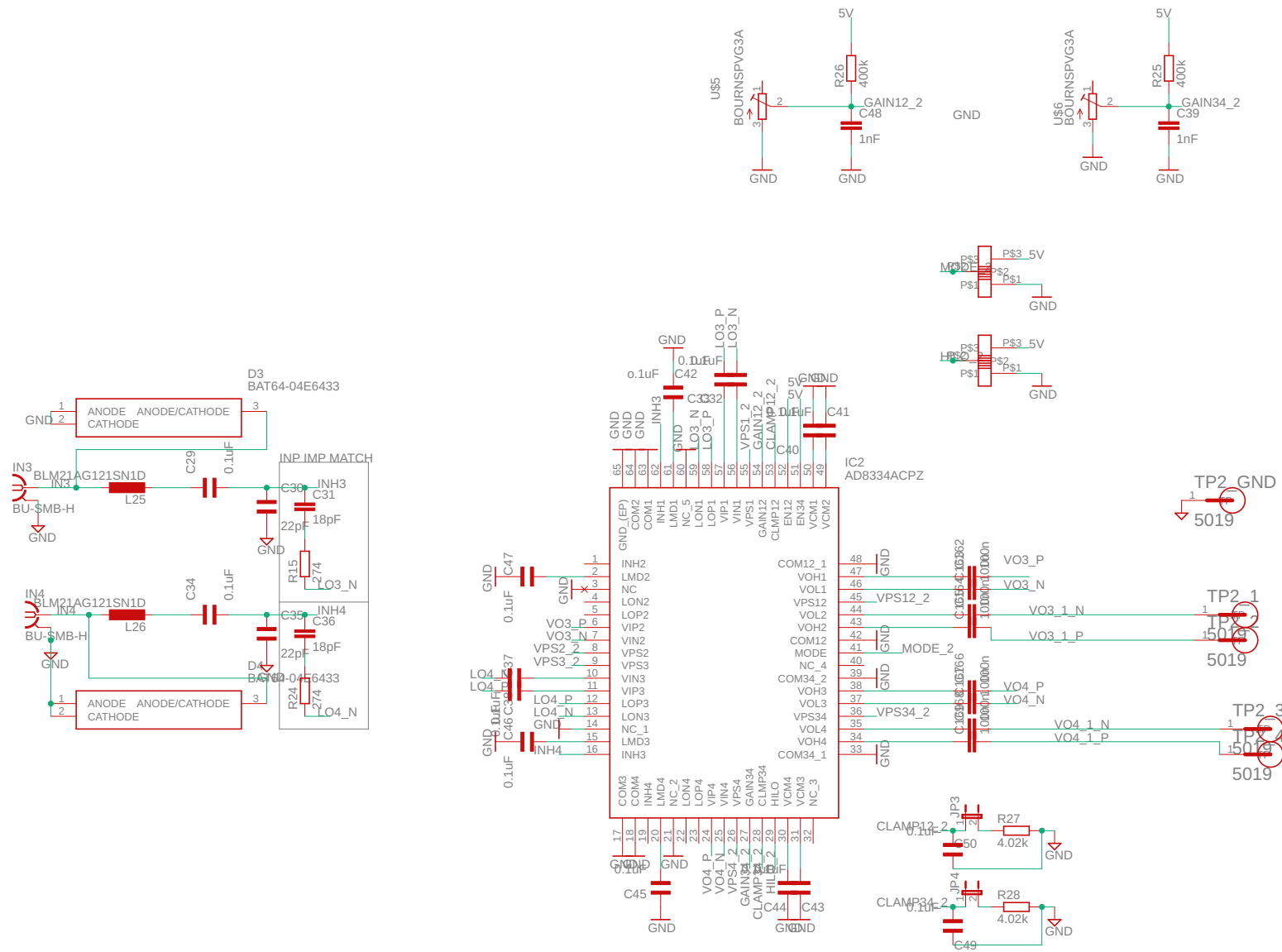


input 1 and 2

TODO:  
 5V Entry  
 GND Entry  
 1V deduction with varistor with low pass network  
 Other voltages?





input 3 and 4

