

SHRADDHA MORE

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Education

Dr.D.Y Patil Institute of Technology,Pimpri , BE in Electronics and Telecommunication	Dec 2021 – Pursuing
• CGPA: 7.69/10	
L.N. Hoshing Jr College, Jamkhed , HSC (Sci)	July 2019 – Aug 2021
• Percentage: 83	
Kanya Vidyalay, Jamkhed , SSC (Sci)	July 2018 – June 2019
• Percentage: 85.20	

Experience

Automation Intern , Suyash Engineering and Automation pvt. ltd, Pune	Dec 2023-Jan 2024
• Successfully Implemented LRO testing on Brake Disk of Truck.	
• Controlled Axis deviation of brake shoe upto 20	
PCB Intern , Javak Microcircuits, Pune	Sept 2003 – Apr 2005
• Learnt and implemented photolithography.	
• 80 efficiency is achieved in a MOSFET Board Development	

Projects

Voice Controlled Notice Board

- Successfully Implemented a Notice Board which can be Controlled by voice commands using android
- Tools Used: C++ , Arduino, Sensors

Expense Tracker

- Developed Expense Tracker on web application having features like category expense track, Splitting expense between friends and dedicated group expense and monthly budget tracking.
- Tools Used: JavaScript, HTML, CSS

Courses and Workshops

Java Beginner and Intermediate - SOLO Learn	June 2024
Data Structure and Algorithm - NPTEL	July 2024

Additional Experience And Awards

IETE Member): Conducted and designed Electrobuzz-23

Technologies

Languages: Java, SQL, JavaScript , HTML

Skills: CN, OOP, OS

Software: Microsoft SQL