core.sch

STDCELLS

X16 spi_openroad
- DVDD — Vidig — VDD
- VSS — VSS
- STAT0[0..7] — c0_[0..7]
- CTRL0[0..7] — c0_[0..7]
- CTRL1[0..7] — c1_[0..7]
- CTRL2[0..7] — c2_[0..7]
- RESET — RESET
- CS — CS
- SCLK — SCLK
- DIN — DIN
- DOUT — DOUT
- DOUT_EN — DOUT_EN

X4 riosc
- DVDD — Viosc — VDD
- c0_7 — EN
- ISOURCE1 — IB
- VSS — VSS
- RESET — RESET
- OSC — OSC
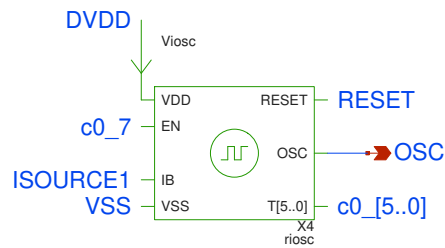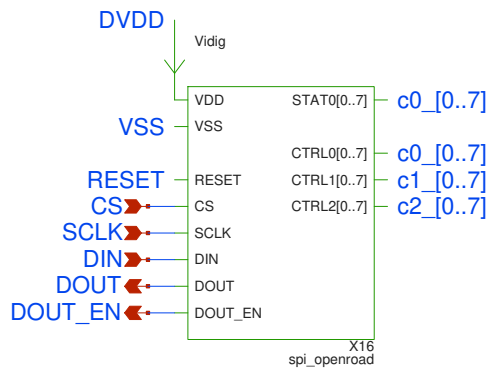- T[5..0] — c0_[5..0]

DVDD
AVDD
VSS

RESET

Schematic of spi block is set
in xschemrc according to the
environment variable DIG_LEVEL.
Instance-Property: schematic="tcleval(spi_$::DIG_LEVEL.sch)"

X25 por
- DVDD — Vipor — VDD
- ISOURCE3 — IBIAS
- VSS — VSS
- RESET — POR

X23 powersw
- AVDD — VDD — VDDSW
- VSS — VSS — POWERON
- FTOP
- FPROG

X24_[0..7] levelup
- DVDD — VDDL
- c1_[0..7] — IN
- VSS — VSS
- VDDH — AVDD
- OUT

X22_[0..7] fuse
- Vifuse — VDD
- fsel1[0..7] — SEL
- VSS — VSS

X4[0..7] levelup
- DVDD — VDDL
- c2_[0..7] — IN
- VSS — VSS
- VDDH — AVDD
- OUT

X3[0..7] fuse
- FTOP — Vifuse1 — VDD
- fsel2[0..7] — SEL
- VSS — VSS

X2 vthref
- AVDD — Vivthref — VDD
- VSS — VSS
- ISOURCE — ISOURCE1
- ISINK — ISINK1
- VSOURCE
- VSINKT
- VSINKB

X5 vthref_source
- AVDD — Visource — VDD
- VSOURCE
- ISOURCE — ISOURCE2

X26 vthref_source
- AVDD — VDD
- VSOURCE
- ISOURCE — ISOURCE3

X1 bmbg
- AVDD — Vibmbg — VDD
- VSS — VSS
- IB — ISINK1
- VREF

X3 ldo
- AVDD — Vildo — VDD
- VREF
- FBIN
- FBOUT
- VSS — VSS
- IB — ISOURCE2
- VLDO — VLDO

vref

X SCHEM

Lukas Baumann

2025-09-10 23:23:50

x1.x1.   core.sch