

[illegible]

In the case the battery charge protection ic is unavailable. All protection ic r's and c's can be removed and fit r5

Diagram showing the pin configuration for the U9 DNF component:

- Pin 1: STAT1
- Pin 2: GND
- Pin 3: BATTERY VOLTAGE
- Pin 4: VBAT
- Pin 5: RPROG

5V and 3V3 Power Supplies

$$V_{out} = V_{ref} \left(\frac{R1}{R2} + 1 \right)$$

$$V_{ref} = 1.24V$$

$$R2 \text{ not to exceed } 5K$$

The diode must be rated for the peak inductor current, and its reverse voltage rating must be greater than the output voltage.

3V3_CORE

3V3_BOARD

[illegible]

The diagram shows a voltage divider circuit. A green line representing 5V_{in} is connected to a red rectangular resistor labeled R114 with a value of 1M and a tolerance of 0.1%. This resistor is in series with another red rectangular resistor labeled R116 with a value of 470k and a tolerance of 0.1%. The bottom terminal of R116 is connected to a ground symbol labeled GND. A green line branches off from the junction between R114 and R116, labeled 5V_RAIL_MONITOR in yellow.

The diagram shows a voltage divider circuit. A green line representing the battery voltage (BATTERY_VOLTAGE) is connected to the top of a resistor labeled R58 (1M, 0.1%). The bottom of R58 is connected to the top of another resistor labeled R64 (470k, 0.1%). The bottom of R64 is connected to a ground symbol (GND). A tap point between the two resistors is connected to a yellow line labeled VBAT_MONITOR.

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