Parallel Processing Review:

Page	Topic
35	Flynn's Classification
37	Fig. 1.4: Six layers for computer system development
43	UMA, NUMA, COMA Model
177	Pipelining (Fig.-1.11: A superscalar processor of degree m = 3)
499	Parallel Programming Models
500	Critical Section
501	Multiprogramming, Multiprocessing, Multitasking, Multithreading
503	Message Passing Model
510	Parallel Languages and Compilers
511	Language Features for Parallelism
515	Fig. 10.4: Compilation phases in parallel code generation
	RISC and CISC architectures. Demonstrate the distinctions between typical RISC and typical CISC processor architectures. Which architecture is more energy efficient between these two?
	Speedup PPT