

OP-AMPS AND SIGNAL CONDITIONING

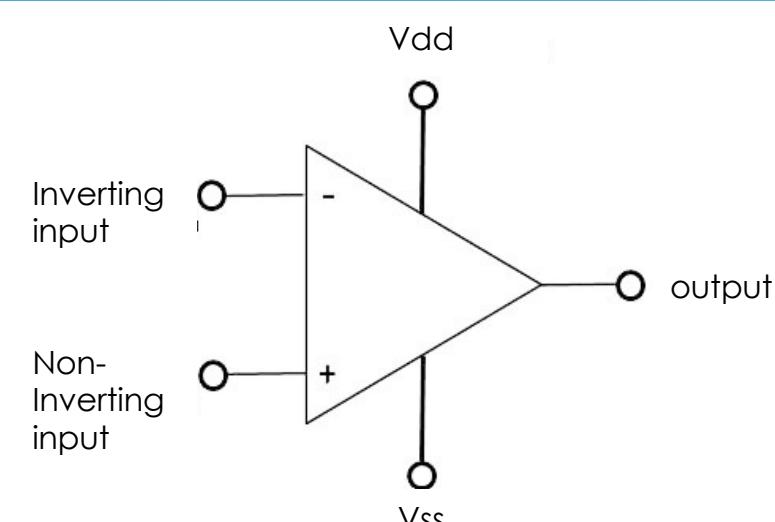
01205479 IoT for EE

Dr.Varodom Toochinda

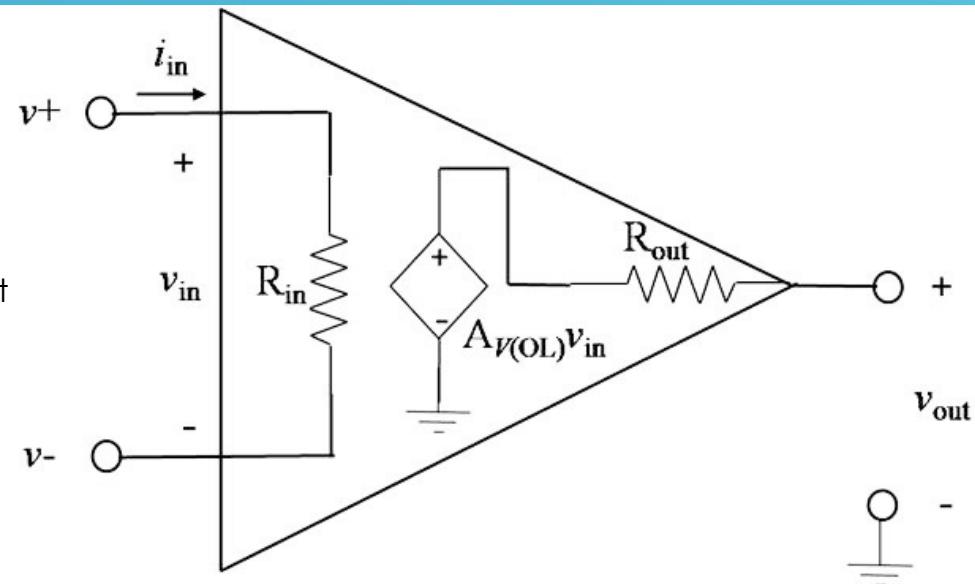
TOPICS

- ▶ Symbols and Open-Loop model
- ▶ Desired Properties
- ▶ Inverting Amp
- ▶ Non-Inverting Amp
- ▶ Differential Amp
- ▶ Level shifter
- ▶ Active filters

SYMBOLS AND OPEN-LOOP MODEL

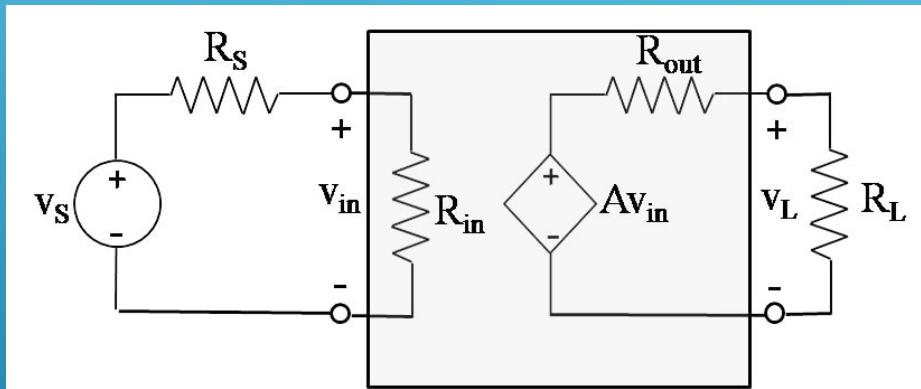


(a) symbol



(b) open-loop model

DESIRED PROPERTIES OF IDEAL OP-AMP



Ideal

$$v_L(t) = A v_S(t)$$

Practical

$$v_{in} = \frac{R_{in}}{R_s + R_{in}} v_s$$

$$v_L = A v_{in} \frac{R_L}{R_{out} + R_L}$$

$$v_L = A \left(\frac{R_{in}}{R_s + R_{in}} \frac{R_L}{R_{out} + R_L} \right) v_s$$

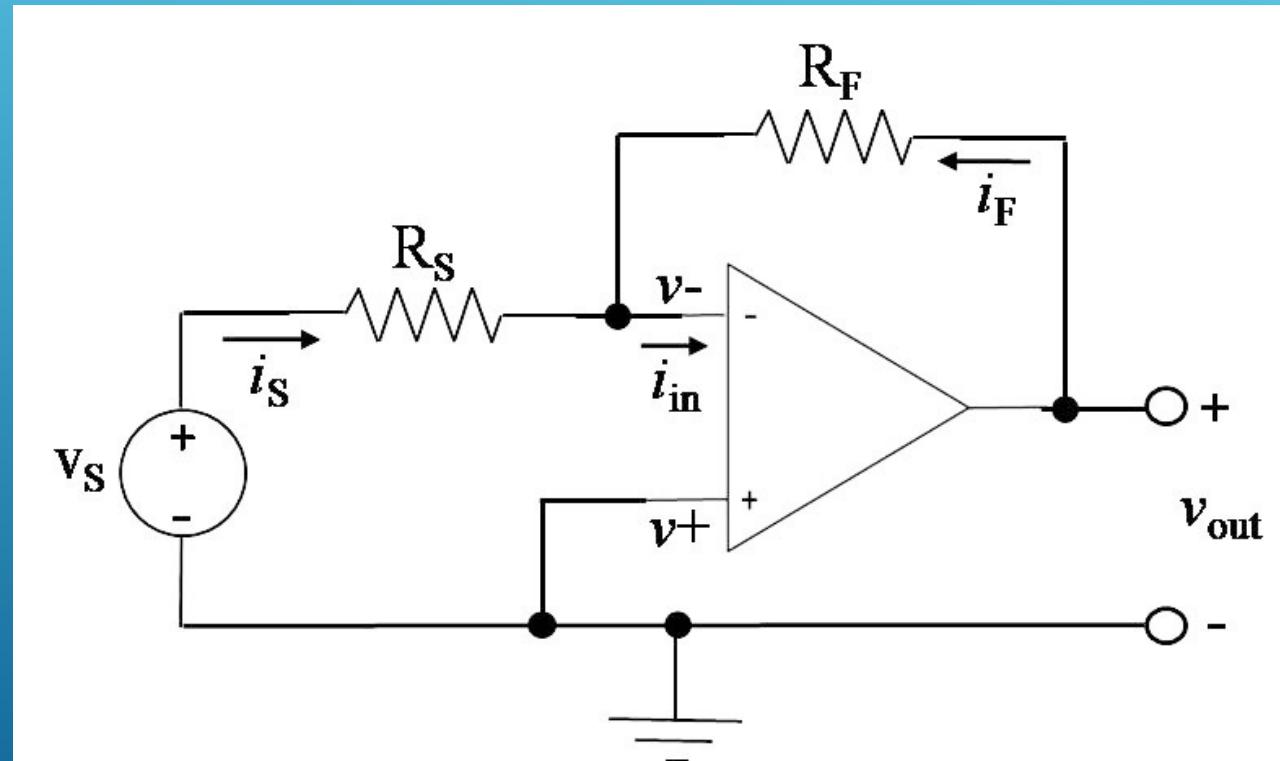
The value in parenthesis must approach 1, which is true when

$$R_{in} \rightarrow \infty$$

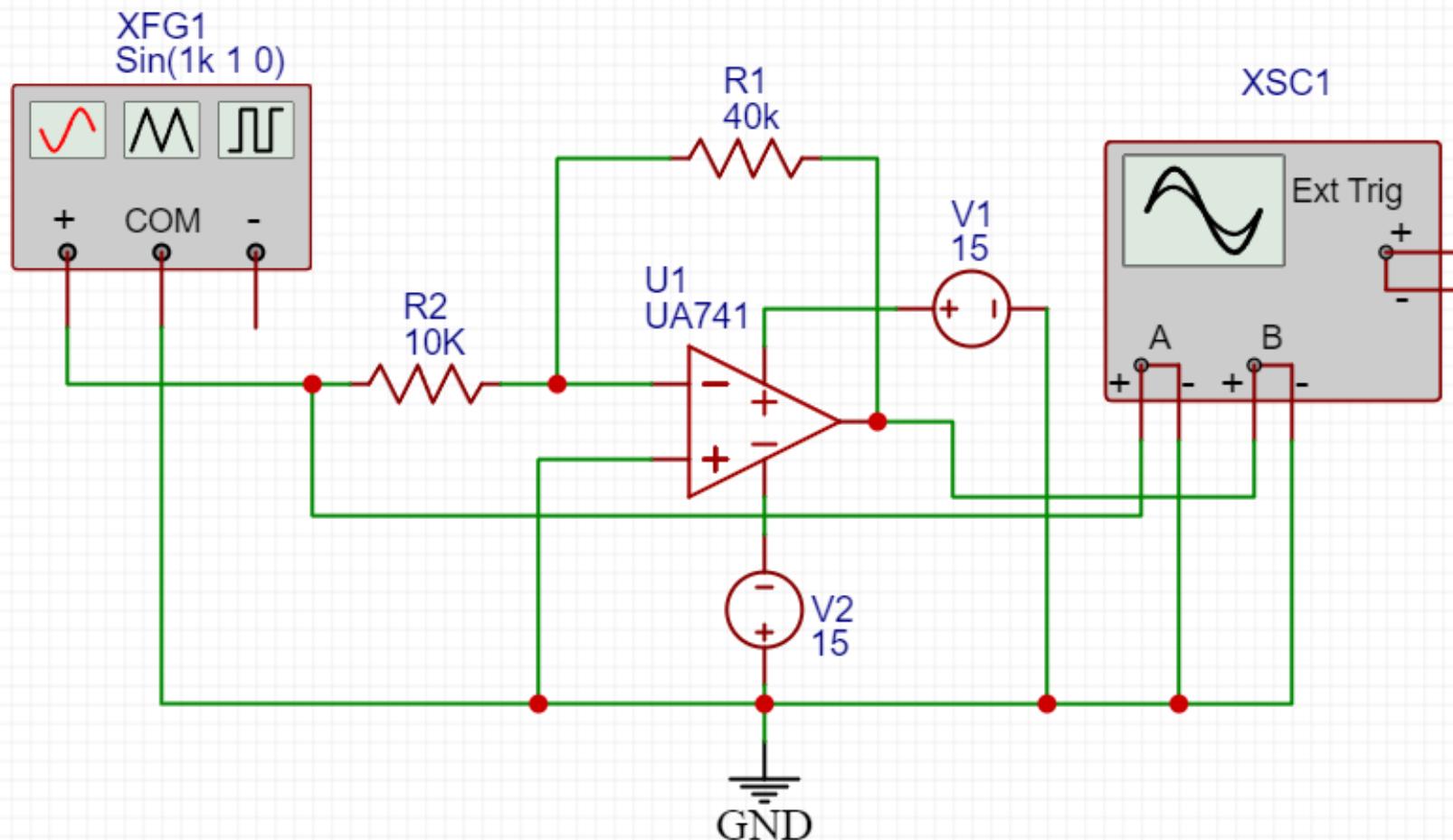
$$R_{out} \rightarrow 0$$

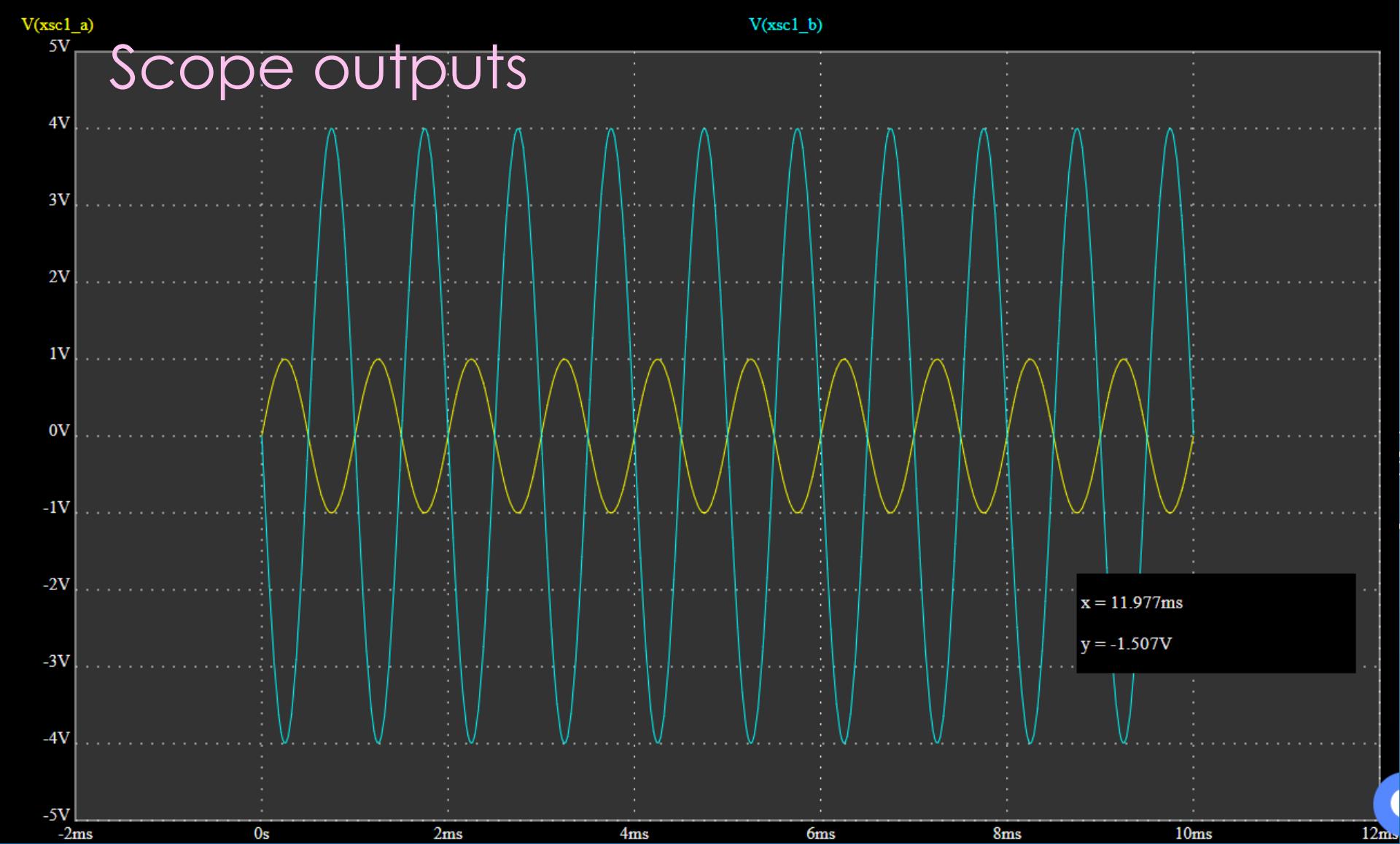
INVERTING AMPLIFIER

$$\frac{v_{out}}{v_S} = -\frac{R_F}{R_S}$$



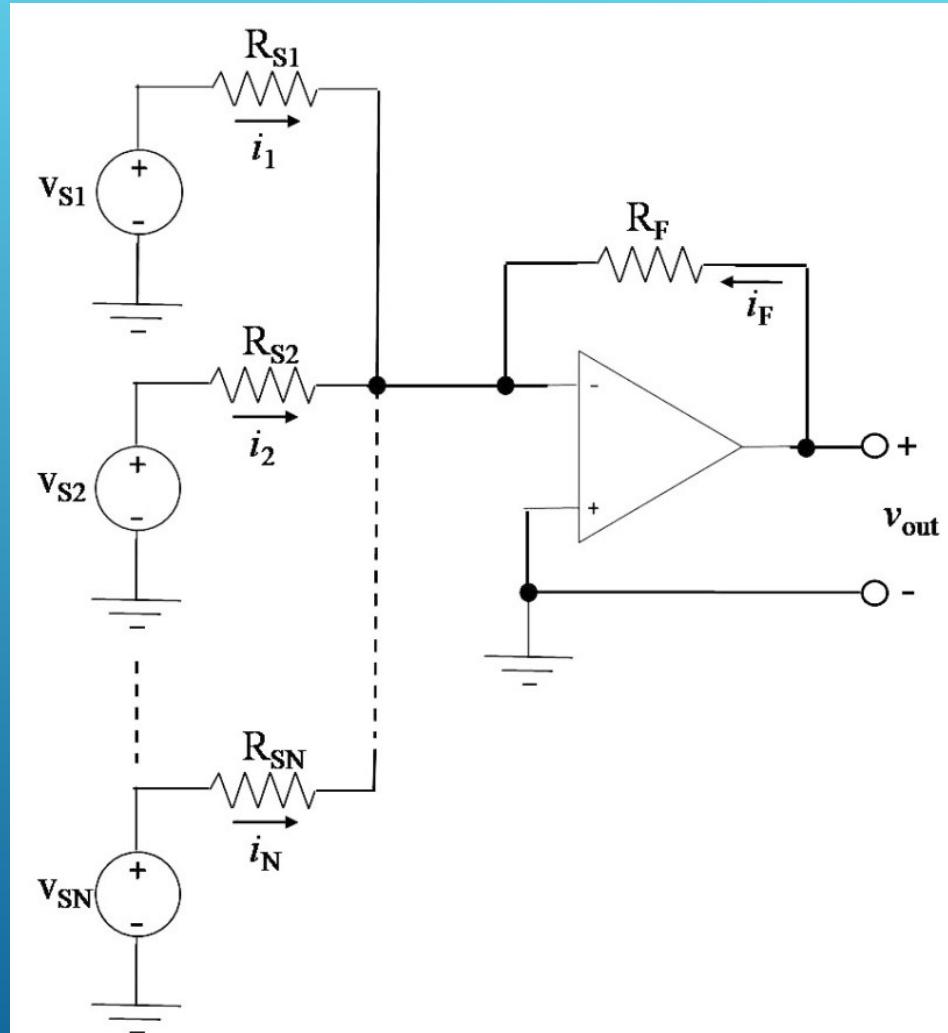
Inverting amp simulation





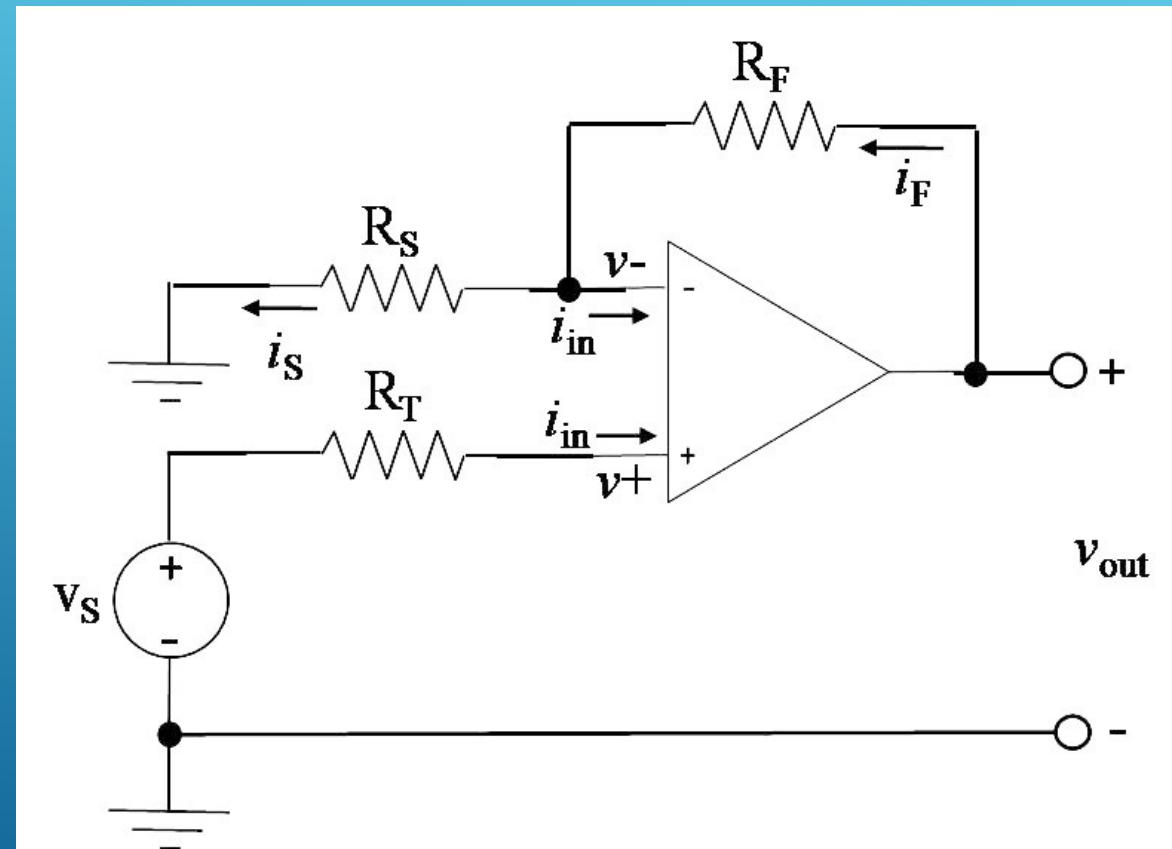
MIXER CIRCUIT

$$v_{out} = -\sum_{n=1}^N \frac{R_F}{R_{Sn}} v_{Sn}$$



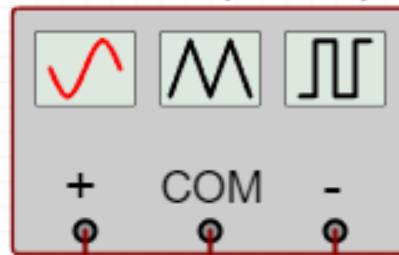
NON-INVERTING AMPLIFIER

$$\frac{v_{out}}{v_S} = 1 + \frac{R_F}{R_S}$$



Non-inverting amp simulation

XFG1
Sin(1k 1 0)



R1
30k

R2
10K

U1

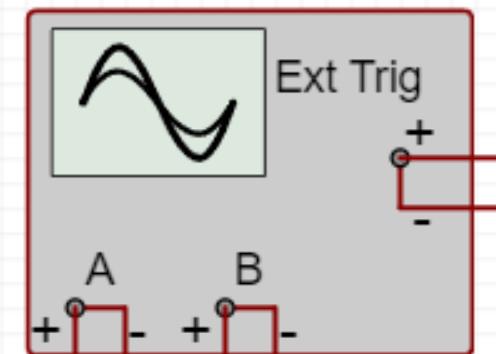
UA741

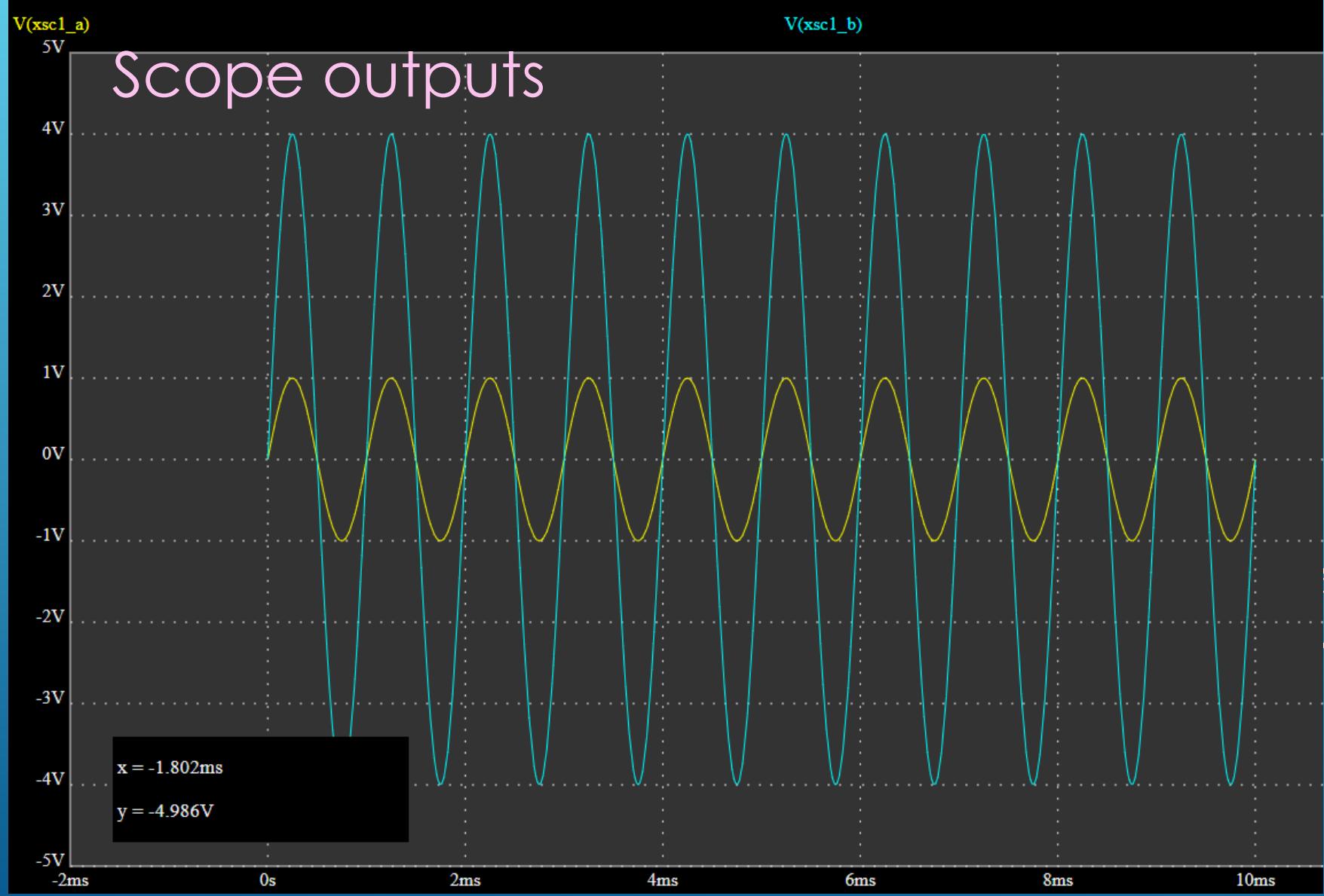
V2
15

GND

XSC1

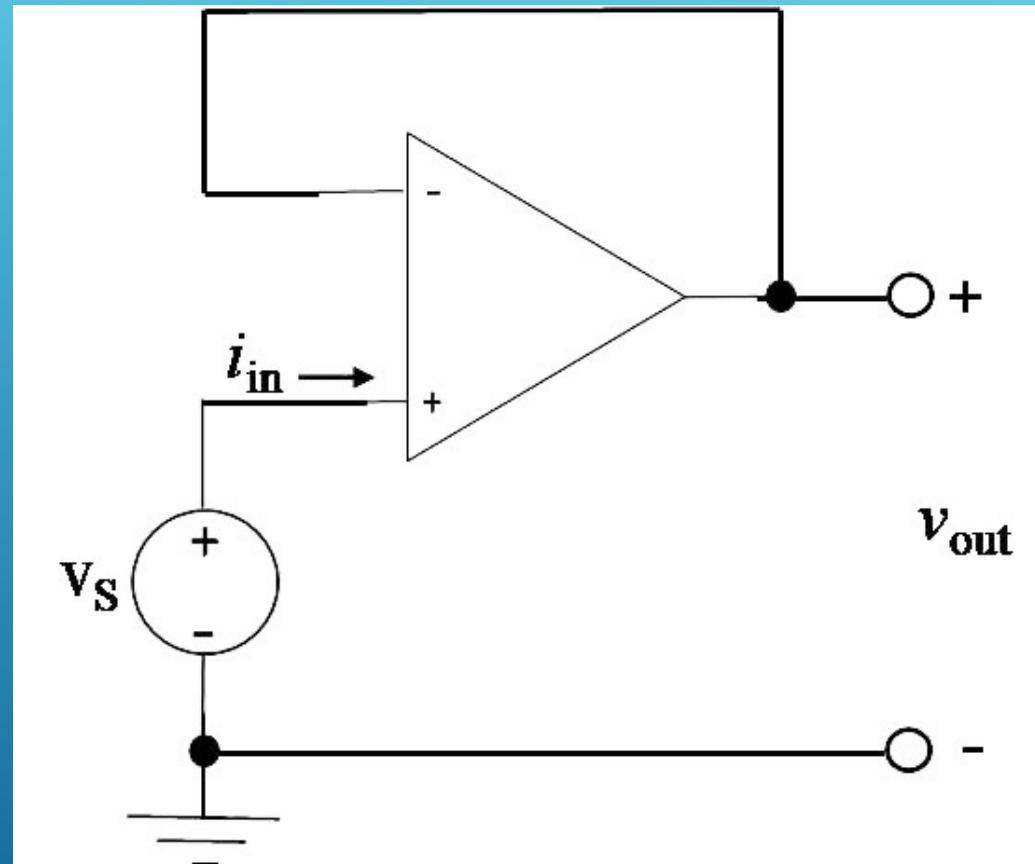
Ext Trig





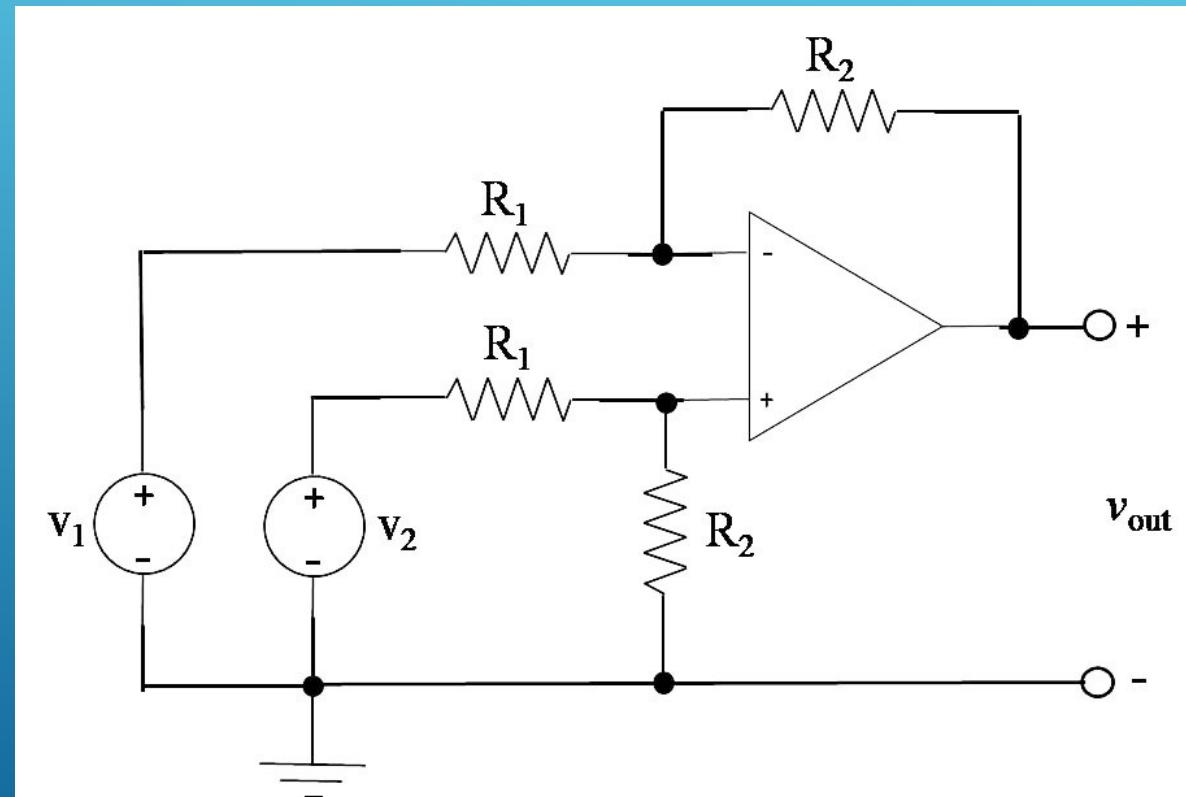
VOLTAGE FOLLOWER

$$V_{\text{out}} = V_s$$

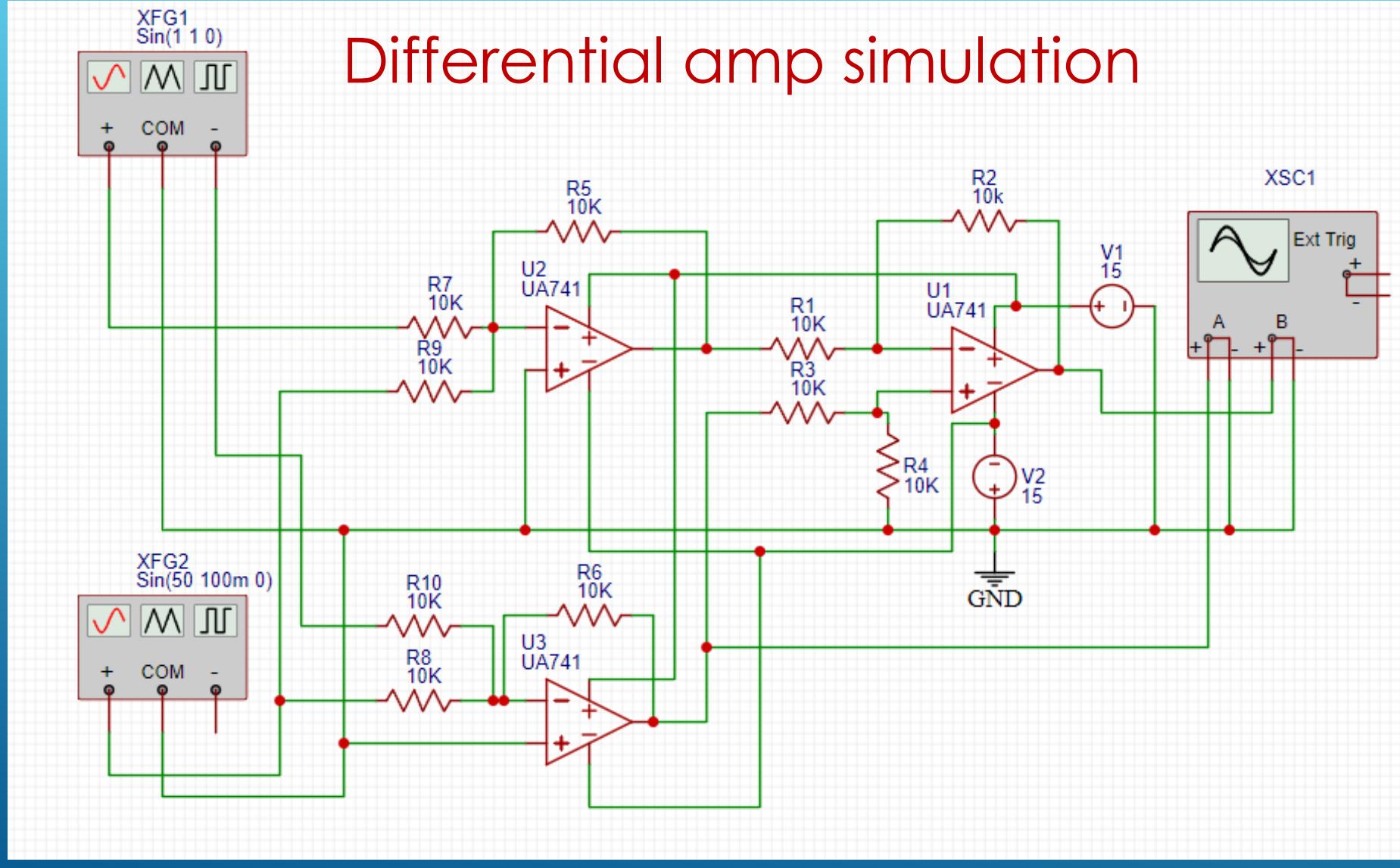


DIFFERENTIAL AMPLIFIER

$$v_{out} = \frac{R_2}{R_1} (v_2 - v_1)$$



Differential amp simulation



V(xsc1_a)

2.5V

V(xsc1_b)

2V

1.5V

1V

500mV

0V

-500mV

-1V

-1.5V

-2V

-2.5V

Scope outputs

x = -198.008ms

y = -2.43V

0s

200ms

400ms

600ms

800ms

1s

1.2s

1.4s

1.6s

1.8s

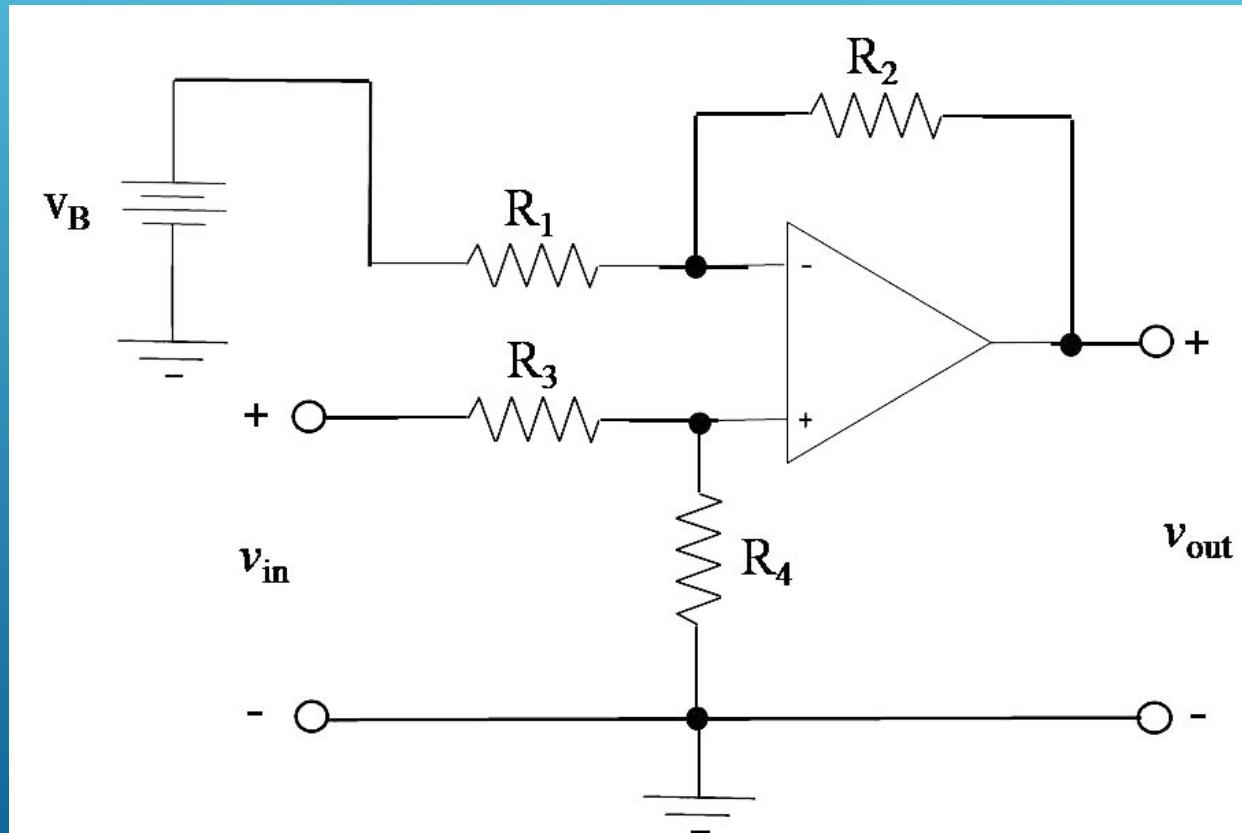
2s

2.2s



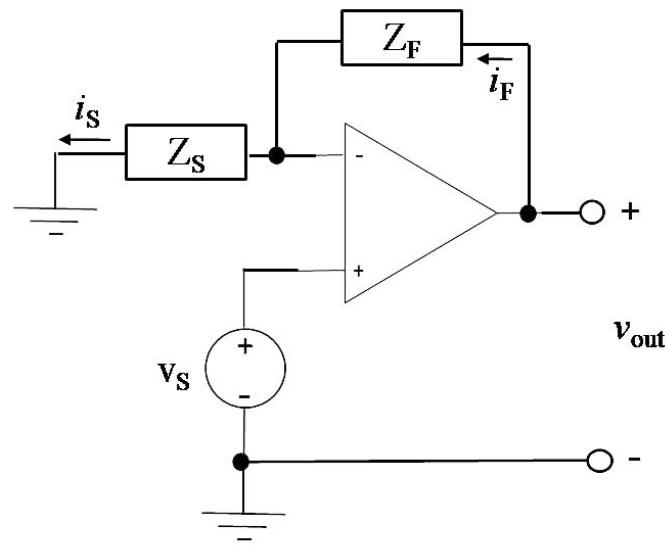
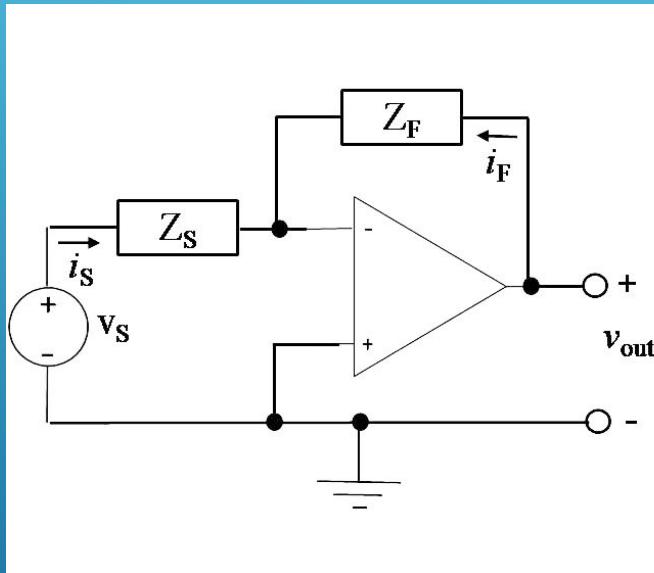
LEVEL SHIFTER

$$V_{out} = \left(\frac{R_4}{R_3 + R_4} \right) \left(1 + \frac{R_2}{R_1} \right) V_{in} - \left(\frac{R_2}{R_1} \right) V_B$$



Note: R_3, R_4 is not
needed for
 $V_{out} > V_{in}$

OP-AMP CIRCUITS WITH IMPEDANCE



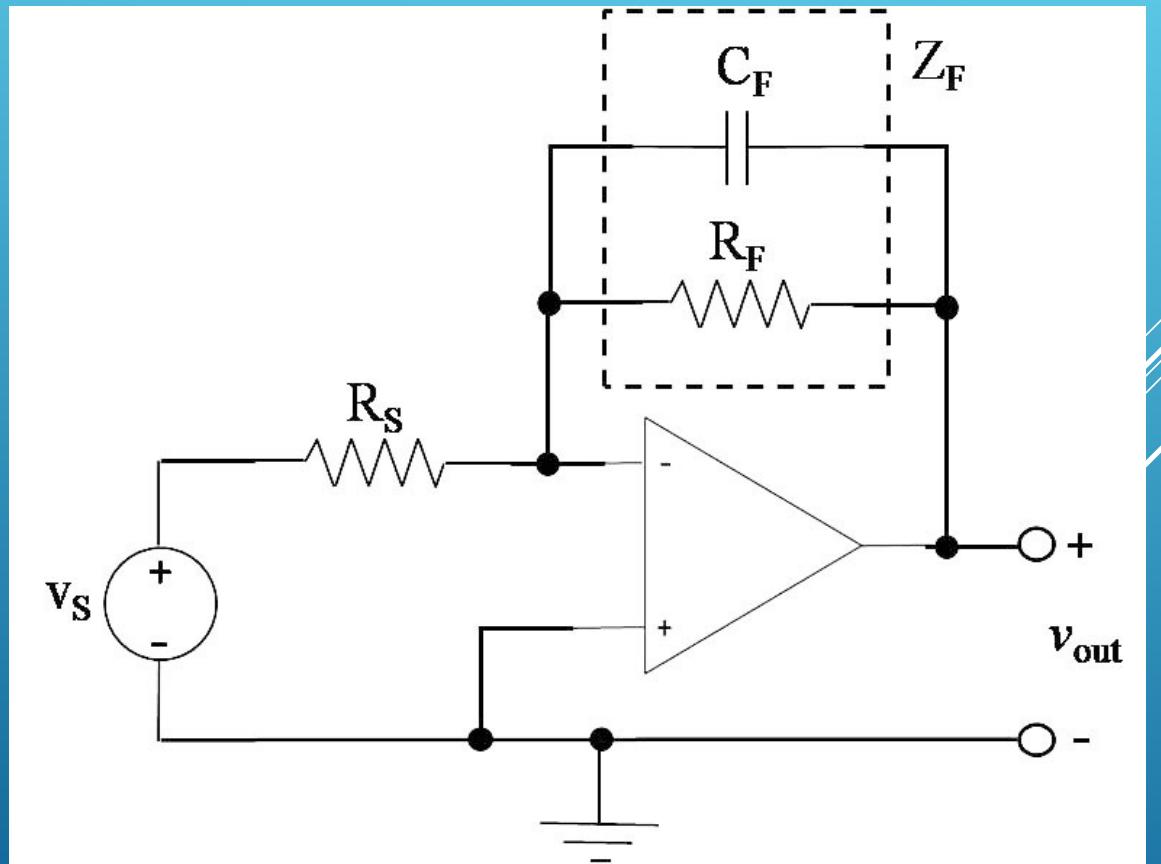
$$\frac{V_{out}}{V_S}(j\omega) = -\frac{Z_F}{Z_S}$$

$$\frac{V_{out}}{V_S}(j\omega) = 1 + \frac{Z_F}{Z_S}$$

ACTIVE LPF

$$A_{LPF}(j\omega) = -\frac{Z_F}{Z_S} = -\frac{R_F / R_S}{1 + j\omega C_F R_F}$$

$$\omega_o = \frac{1}{R_F C_F}$$

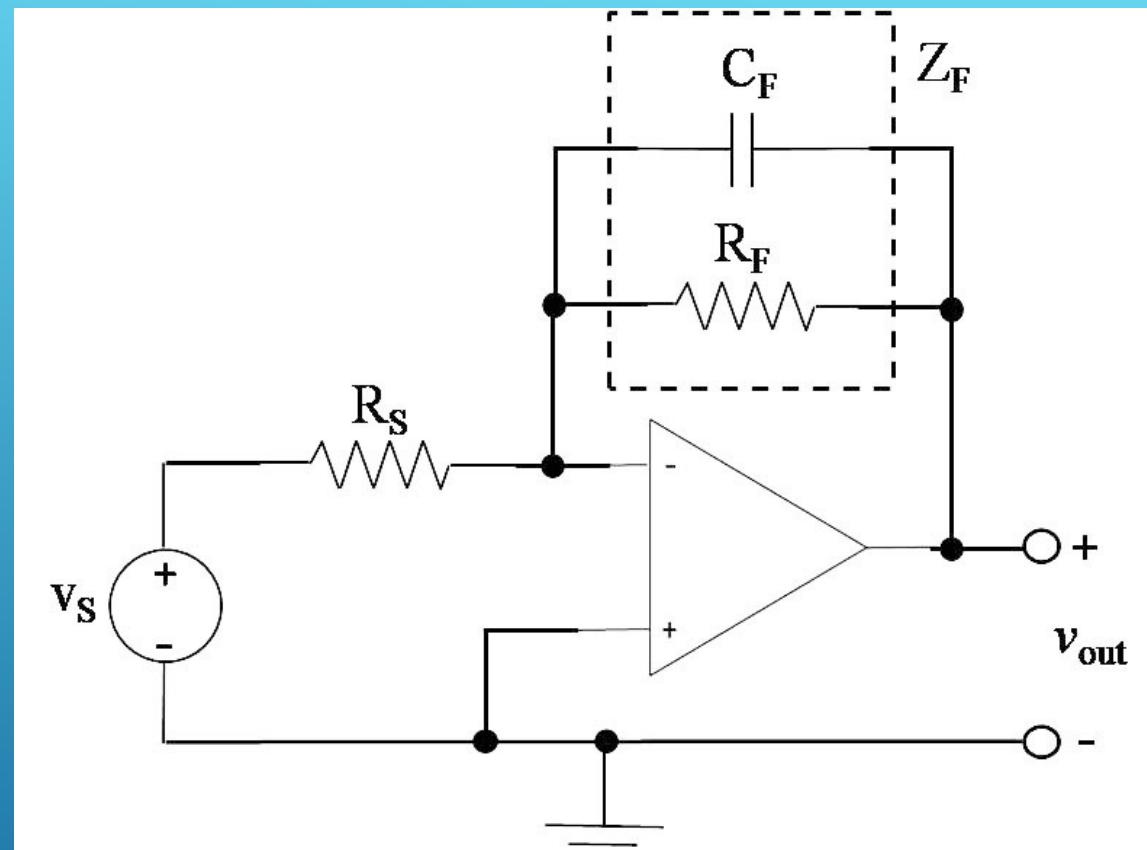


EXERCISE 1

Design active LPF with gain of -10
Signal frequency = 1 Hz
Noise frequency = 50 Hz

$$A_{LPF}(s) = -\frac{R_F/R_S}{C_F R_F s + 1}$$

$$\omega_o = \frac{1}{R_F C_F}$$

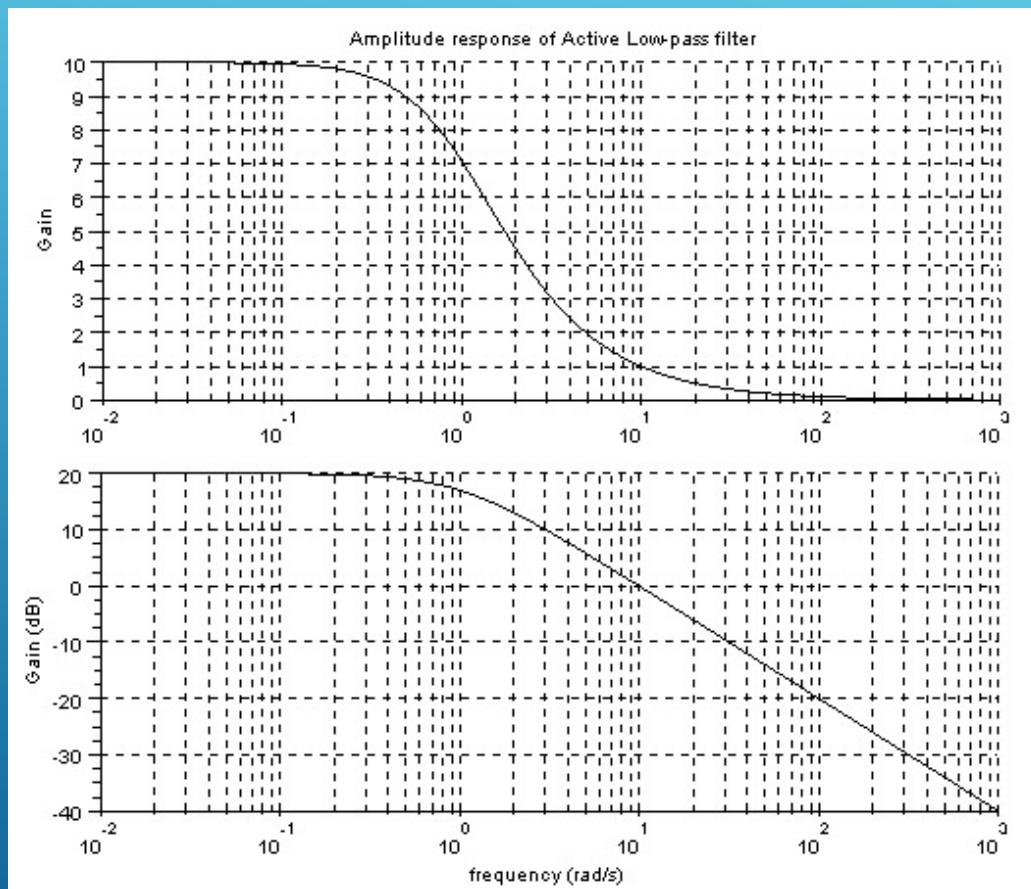


FREQUENCY RESPONSE OF ACTIVE LPF

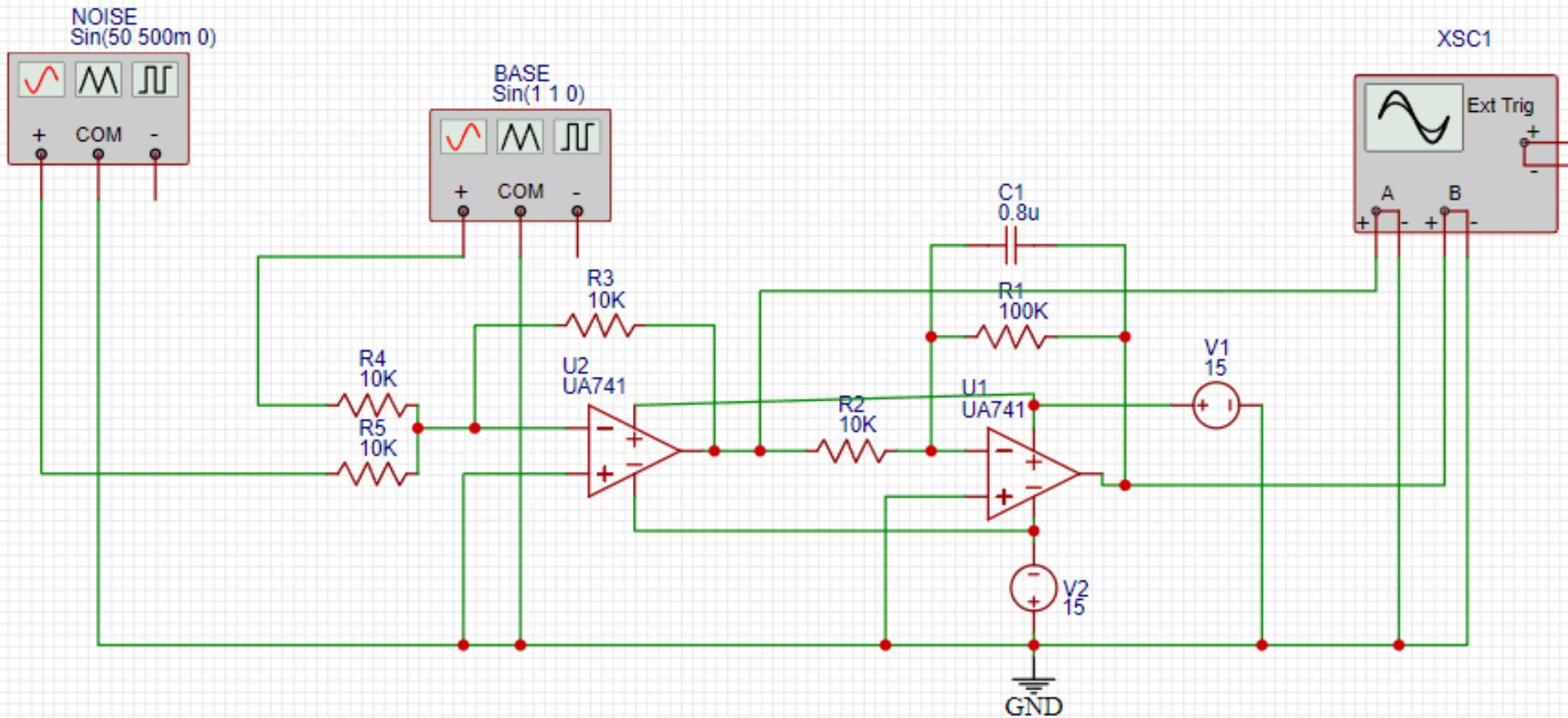
$$\begin{aligned}R_S &= 1 \text{ K}\Omega \\R_F &= 10 \text{ K}\Omega \\C_F &= 100 \mu\text{F}\end{aligned}$$



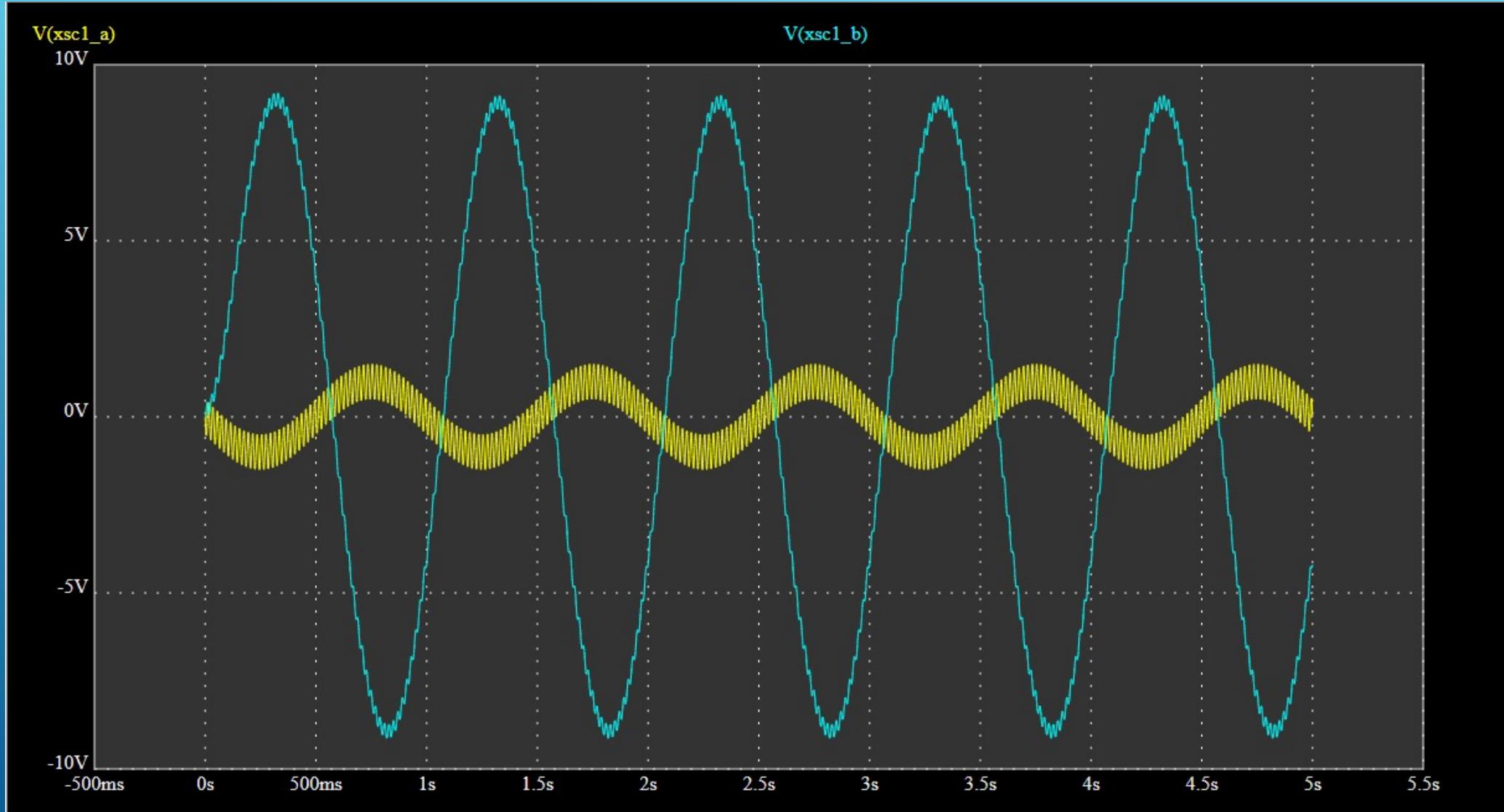
$$\begin{aligned}\text{Gain} &= 10 \text{ (20 dB)} \\ \omega_0 &= 1 \text{ rad/s}\end{aligned}$$



ALPF simulation

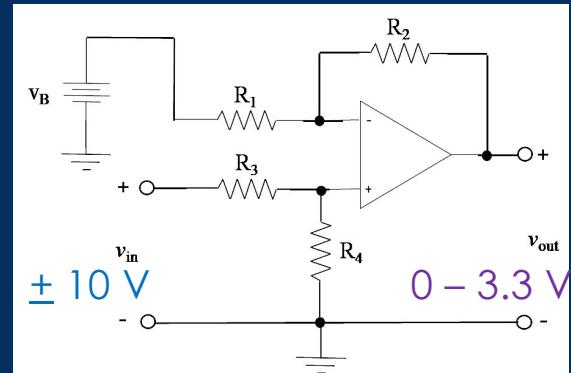


https://youtu.be/m3YYIChfxi?si=UBwR0yOs_MY2hcou



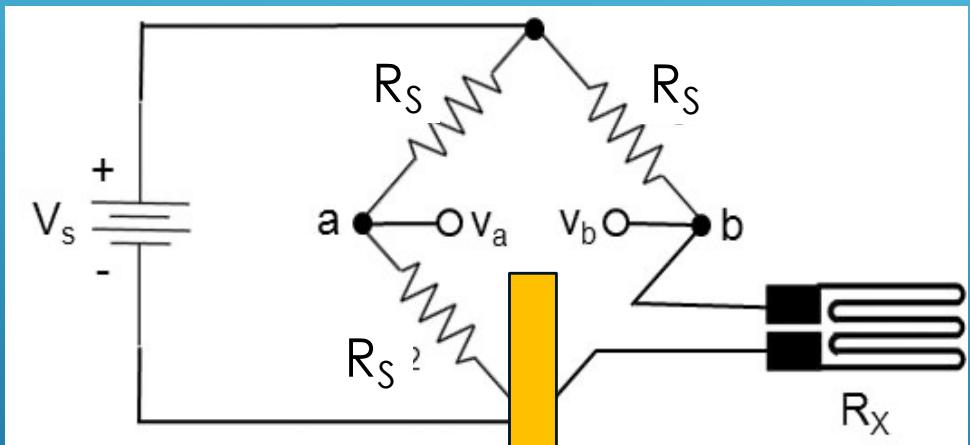
Ex 3.2:

- Design a level shifter circuit that accepts sensor signal in the range ± 10 volt and convert to range $0 - 3.3$ volt at output.

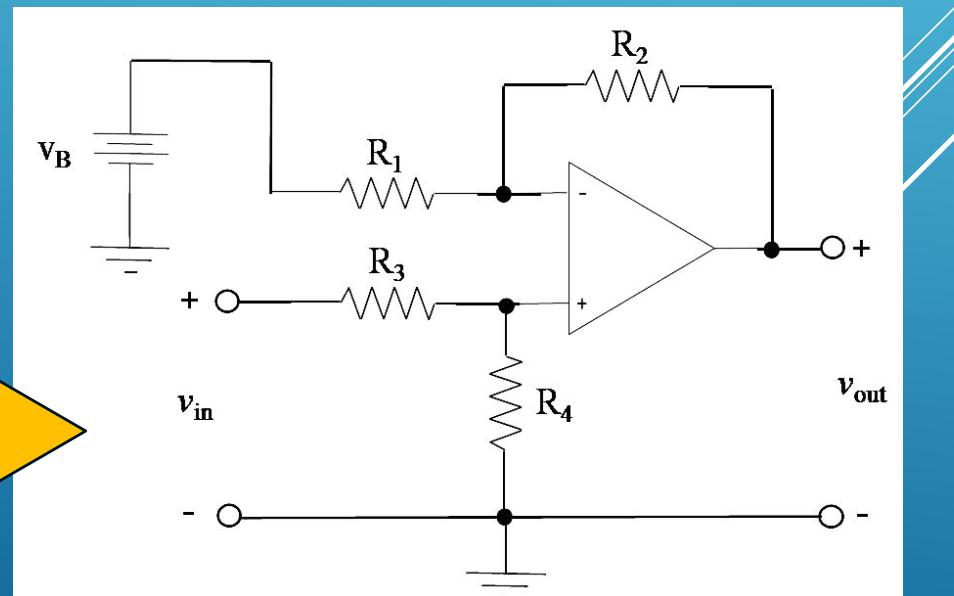


HW#3

- (Part 1) Design a level shifter circuit for the wheatstone bridge exercise in lecture 1

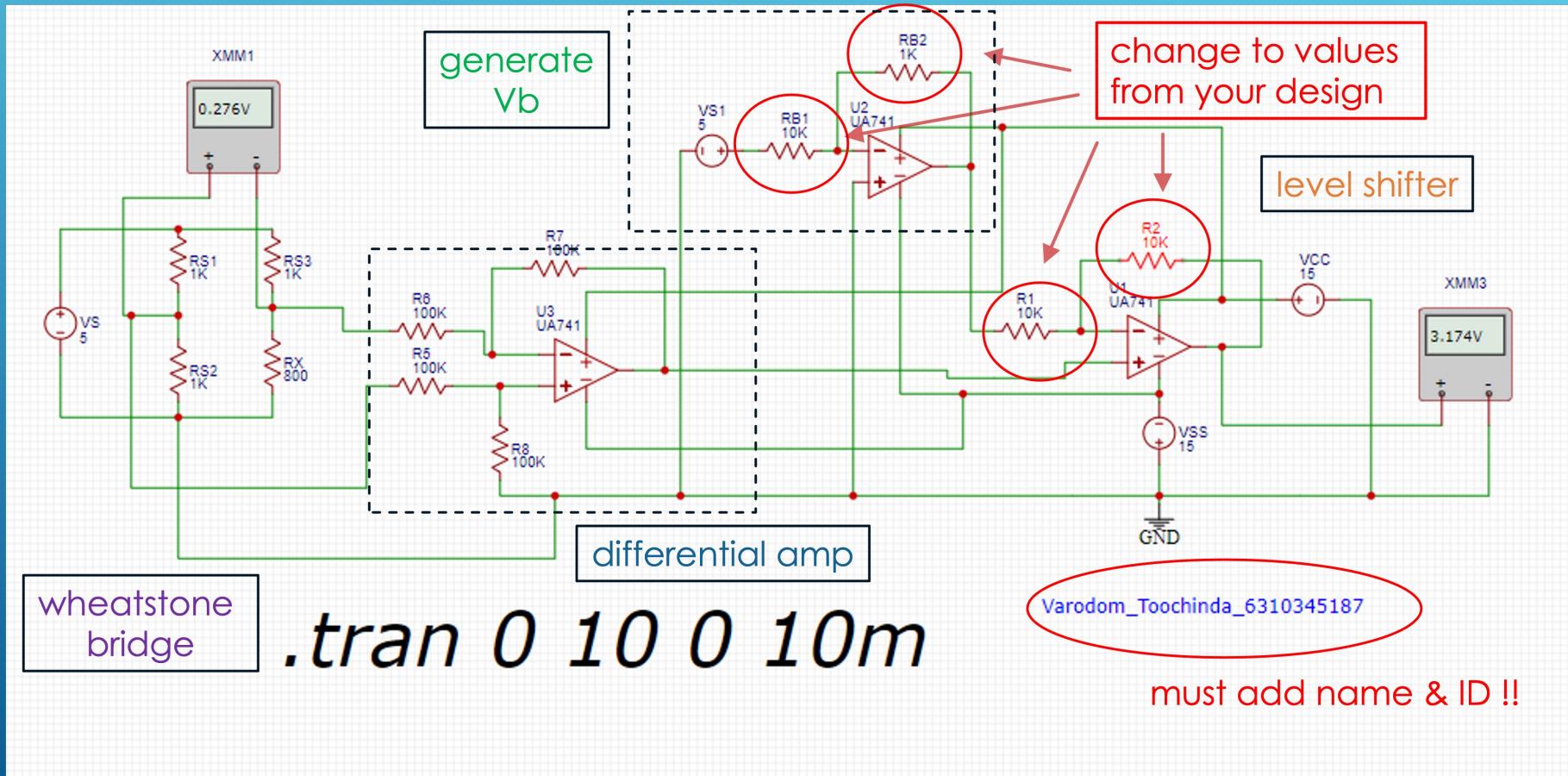


Let $V_s = 5$ Volts. $R_s = 1000$ Ohms, R_x varies between 800 – 1200 Ohms



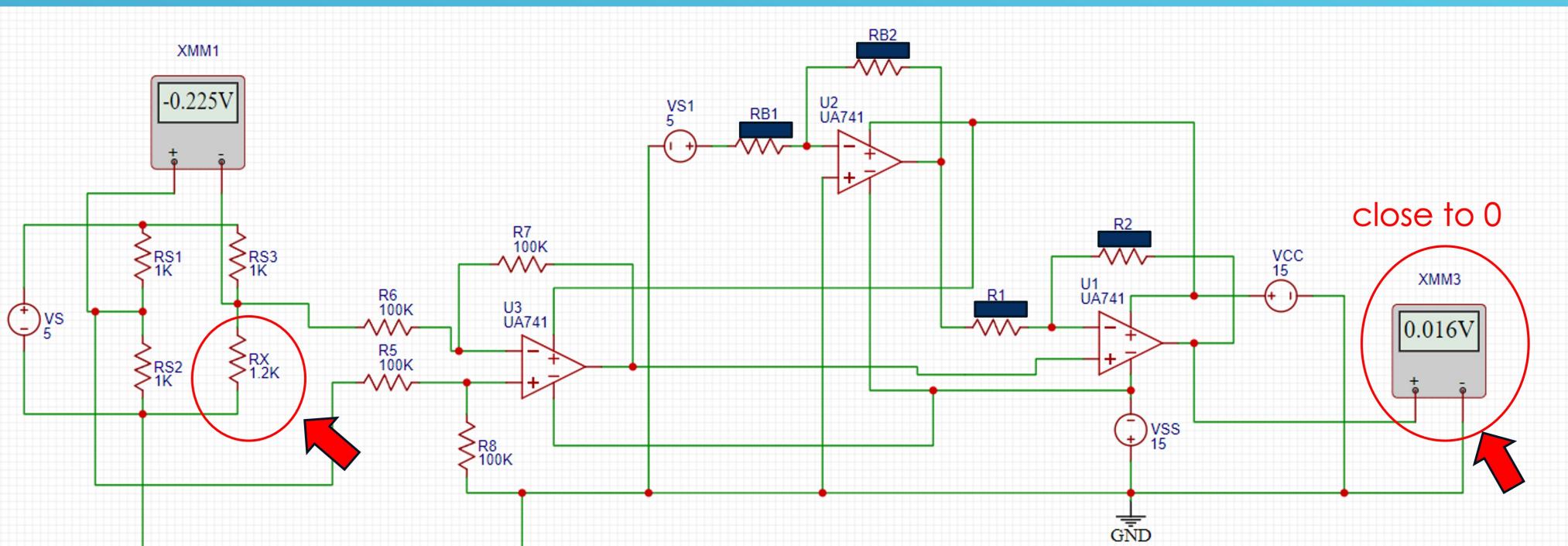
HW#3

- (Part 2) Construct a circuit on EasyEDA and simulate



HW#3

- Expected output when $R_x = 1200 \text{ Ohms}$

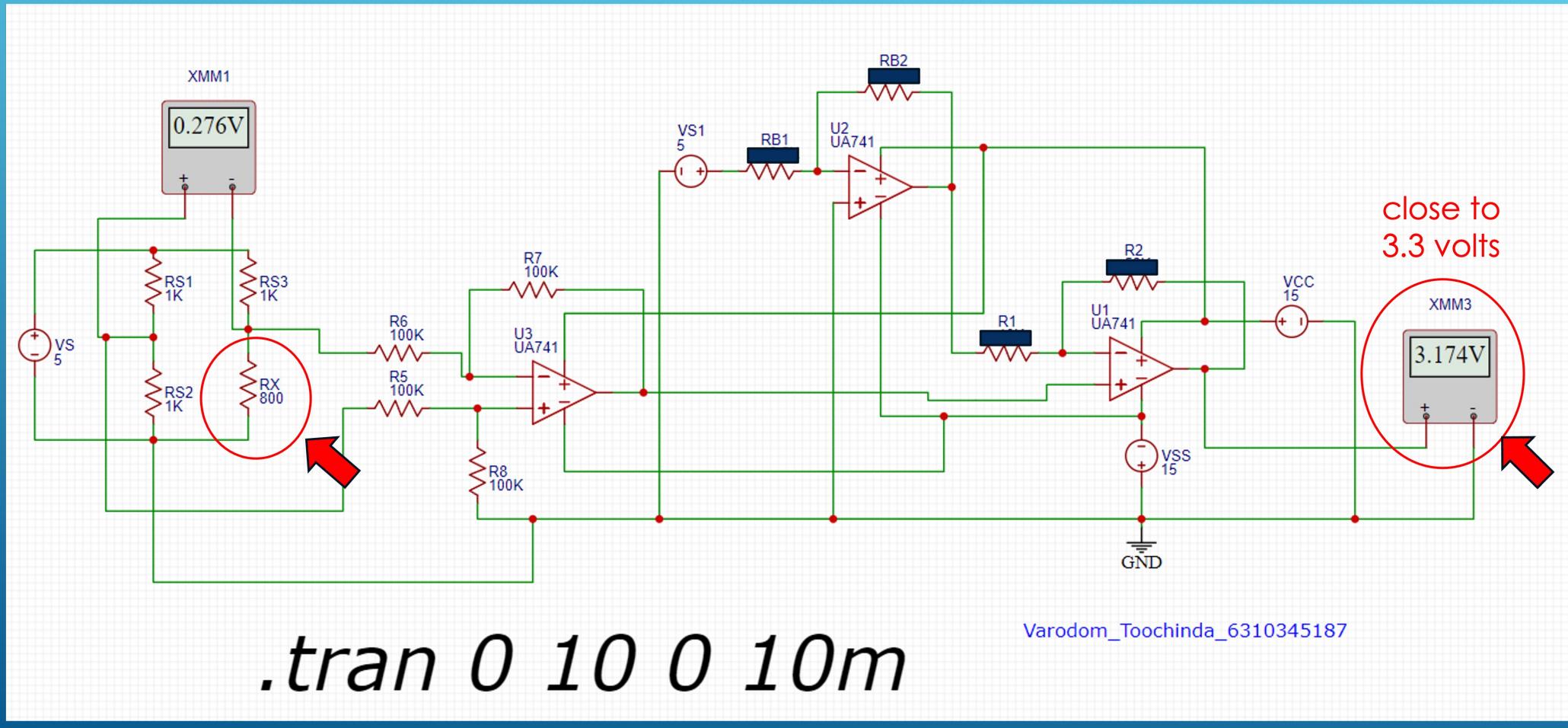


.tran 0 10 0 10m

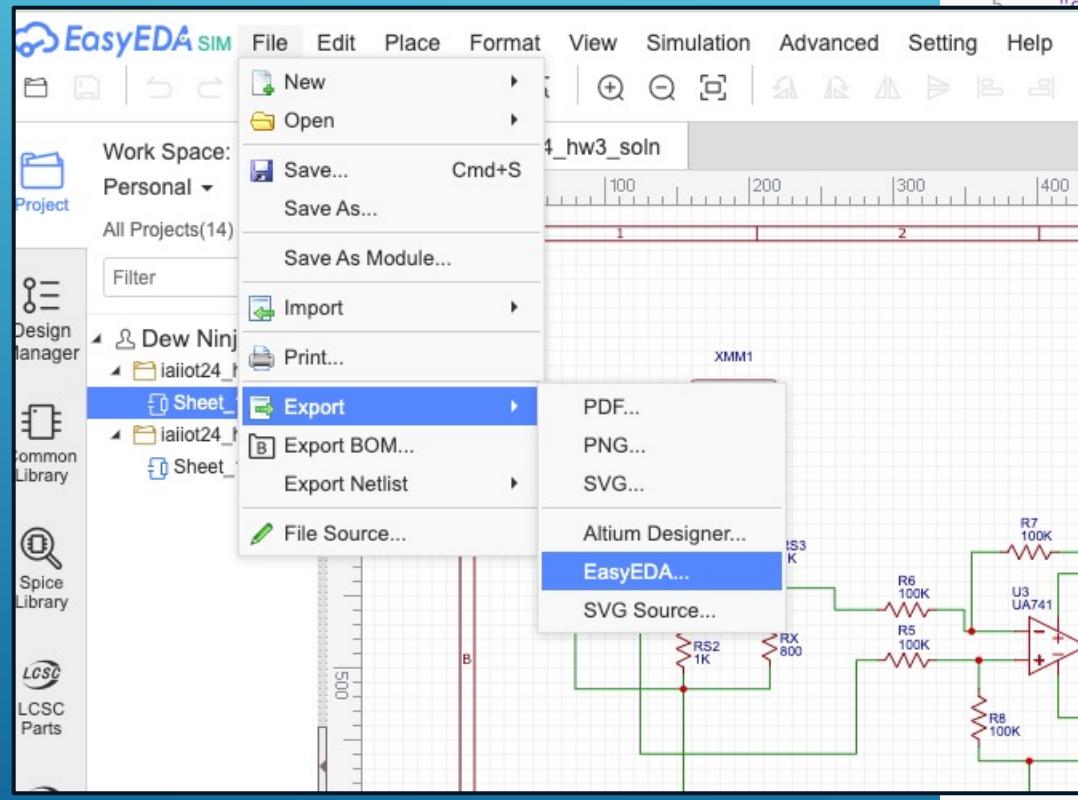
Varodom_Toochinda_6310345187

HW#3

- Expected output when $R_x = 800 \text{ Ohms}$



Export EasyEDA json file



```
1 ▾ {
2   "editorVersion": "6.5.44",
3   "docType": "5",
4   "title": "iaiilot24_hw3_soln",
5   "description": "",
6   "colors": {},
7   "chematics": [
8     {
9       "docType": "1",
10      "title": "Sheet_1",
11      "description": "",
12      "dataStr": {
13        "head": {
14          "docType": "1",
15          "editorVersion": "6.5.44",
16          "newgId": true,
17          "c_para": {
18            "Prefix Start": "1"
19          },
20          "c_spiceCmd": "TRAN`10m`10`0`{AC`dec`0`0`{DC`0`0`0`{TF` ``",
21          "hasIdFlag": true,
22          "sim": true,
23          "uuid": "5976e746e4274c1fac616401775f2551",
24          "x": "0",
25          "y": "0",
26          "portOfADIImportHack": "",
27          "importFlag": 0,
28          "transformList": ""
29        },
30        "canvas": "CA~1000~1000~#FFFFFF~yes~#CCCCCC~5~1000~1000~line~5~pixel~5~0~0",
31        "shape": [
32          "LIB~0~~806~package`NONE`Manufacturer Part`?
33          `spicePre`.'~~0~frame_lib_1~~0~87b37d955f9ba603~yes~yes~~~#@T~N~570.5~~~809~0~ment~A~0~start~gge223~0~pinpart#@$PT~M 206 -796 L 206 -806 M 206 -10 L 206 0 M 402 -10 L 402 0 M 598 -796 L 598 -806 M 598 -10 L 598 0 M 794 -796 L 794 -806 M 990 -796 L 990 -806 M 990 -10 L 990 0 M 10 -600 L 0 -600 M 1139 -600 L 1149 -60 M 1139 -404 L 1149 -404 M 10 -208 L 0 -208 M 1139 -208 L 1149 -208 M 10 -12 L 0 1149 -12~#880000~1~0~none~gge10~0~frame_tick#@T~P~571.5~~~818~0~#000080~Arial~~~~~co~0~pinpart#@$T~L~1.5~~~698~0~#880000~~~~~comment~A~1~start~gge13~0~frame_tick#
```

Copy and paste code to notebook cell.
No code, no credit!