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## IPTC DDTS Mona

## **Description**

An Ericsson Digital Telephone Set (DTS) Compatible Module

#### **Features**

1. TI 16-bit fixed-point Digital Signal Processor (DSP) ranging from 120 to 160 MIPS, and with 64 to 256 kB 0ws SRAM (standard TMS320VC5409A; optional 5410A, 5416)

CPU & Memory

- 2. SPI Serial EEPROM for firmware (standard 32-64 kB)
- 3. Microwire Serial EEPROM for configuration data (standard 512 bytes)
- 4. Extension line 2B+D protocol implemented in DSP on the top of NS TP3406V DASL

Protocol

- 5. Emulation of DBC213 (or any other DBC and OPI II console as a skin)
- 6. Transparent connectivity to Ericsson MD110 / Business Phone (Fenix)
- 7. Echo cancellation and voice processing algorithms implemented in DSP

Audio

- 8. TI 16-bit audio CODEC with programmable gain and 89dB SNR (TLC320AD50)
- 9. Bridge-Tied Load audio amplifier (TPA701: 0.7W at  $8\Omega$ ; optional TPA6211A1: 3.1W at  $4\Omega$ )
- 10. Microphone preamplifier (40-58dB) with shadow power supply for electret microphones
- 11. I/O Expansion with 5 TTL inputs, 8 TTL outputs (±24mA) and power supply terminal

I/O

- 12. Plug-in support for 4 LEDs and 5x8 matrix keyboard
- 13. HD4470 compatible LCD display option with PWM background light
- 14. Industrial Temperature Operating Environment –20°C ... +70°C
- 15. Hardware watchdog with power supply supervision

Robustness

- 16. Power supply with wide-range input 9-60VDC / 12-36VAC providing up to 2A at 5V
- 17. Power supply from PBX, if the power consumption is less then 75mA (at 30-60V)
- 18. Two-stage soft-start power supply keeping start-up in-rush current below 100mA
- 19. Typical power consumption 1.5 W

Misc

20. Special door-telephone features: remotely controllable relay switch with max 1A load at 30VDC, and case intrusion detection

## **Applications**

Embedded Digital Telephone Set Module, e.g.:

- 1. Digital Door Telephone Set
- 2. Digital Conference Telephone Set
- 3. Voice Over IP Gateway/Extender DTS

#### **Custom Design Options**

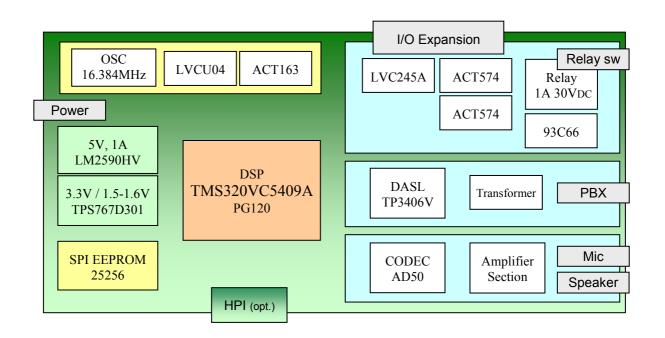
- 1. Board layout
- 2. I/O expansion
- 3. Audio amplifier
- 4. DSP Enhanced Host-Processor Interface (HPI) 8/16-bit connection

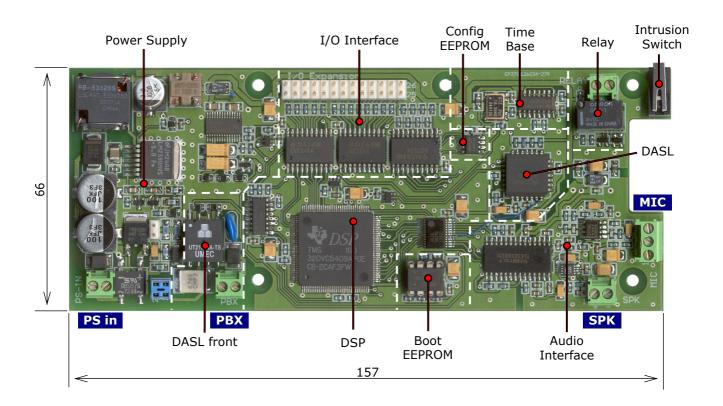


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## **Block Diagram**

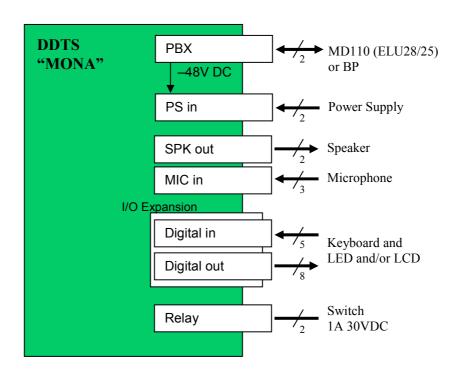






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# **Connector Description**



Connector	Description
PS in	Input power supply 9-60VDC / 12-36VAC
PBX	Connected to ELU 28
MIC	Electret microphone input
SPK	BTL speaker output
Relay	1A 30VDC switch
I/O Expansion	5 TTL inputs, 8 TTL outputs (±24mA)
	- direct connection for 4 LEDs
	- keyboard with up to 5x8 keys
	- LCD HD4470 compatible (in 4-bit mode)

IN 4	2	0		1	GND
IN 3	4	0	0	3	GND
IN 2	6	0	0	5	GND
IN 1	8	0	0	7	+5V
IN 0	10	0	0	9	+5V
OUT 7	12	0	0	11	LED3
OUT 6	14	0	0	13	LED2
OUT 5	16	0	0	15	LED1
OUT 4	18	0	0	17	LED0
OUT 3	20	0	0	19	KB3
OUT 2	22	0	0	21	KB2
OUT 1	24	0	0	23	KB1
OUT 0	26	0	0	25	KB0

I/O Expansion Connector Description



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## **Features in Details**

- 1. TI fixed point Digital Signal Processor (DSP) ranging from 120 to 160 MIPS, and from 64 to 256 kB 0ws SRAM (standard TMS320VC5409A; optional 5410A, 5416)
  - a. The TMS320C54x DSP generation is featuring a broad product lineup of 15 distinct devices with performance ranging from 30 to 532 MIPS. The architecture and instruction sets of the C54x enables extremely small code size for DSP functions, allowing the DSPs to take maximum advantage of on-chip RAM.
  - b. Devices in used in DDTS Mona design are with the following characteristics in brief

MHz	120 - 160
MIPS	120 - 160
Active Power	40 - 90 mW
Pricing (1kU)	\$11.15 - \$25.57 US
Peripherals	3 McBSP, 8/16-bit HPI, 6-channel DMA

c. Options:

TMS320VC <b>5409A</b> PGE12	20 120 MIPS	64kB RAM	\$11.15	Default choice
TMS320VC <b>5409A</b> PGE16	50 160 MIPS	64kB RAM	\$12.73	
TMS320VC <b>5410A</b> PGE12	20 120 MIPS	128kB RAM	\$13.81	
TMS320VC <b>5410A</b> PGE16	50 160 MIPS	128kB RAM	\$15.31	
TMS320VC <b>5416</b> PGE120	120 MIPS	256kB RAM	\$23.01	
TMS320VC <b>5416</b> PGE160	160 MIPS	256kB RAM	\$25.57	

- 2. SPI Serial EEPROM for firmware (standard 32-64 kB)
  - a. 8-pin DIL package placed in socket
  - b. Boot time is below 4 seconds
  - c. Options:

**M95256** or **AT25256** 32kB Default choice **AT25H512** 64kB

- 3. Microwire Serial EEPROM for configuration data (standard 512 bytes)
  - a. Standard 256 x 16 bit storage for storing configuration data, when needed
  - b. Options:

**M93C66** 256 x 16 Default choice **M93C86** 1k x 16

- 4. Extension line 2B+D protocol implemented in DSP on the top of NS TP3406V DASL
  - a. Digital Adapter for Subscriber Loops (DASL) with range up to 800 meter on #24 cable
  - b. Fully compatible client-side of Ericsson extension line 2B+D protocol
  - c. Input overvoltage transient protection with both fast TVS diode and metal-oxide varistor
- 5. Emulation of Ericsson DBC213 (or any other DBC and OPI II console as a skin)
  - a. Emulation of DBC213 having 3 x 40 character display and two rows function keyboard
  - b. Possible emulation of any other DBC, even OPI II console
  - c. Buttons are programmable in PBX, which makes easy remote setup of the DTS
  - d. Events could be connected to some actions like ringing of ADN to switch relay on
- 6. Transparent connectivity to Ericsson MD110 / Business Phone
  - a. Connectible both to MD110 or Business Phone (Fenix) Ericsson PBXs

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- 7. Echo cancellation and voice processing algorithms implemented in DSP
  - Base system CPU usage is maximum 12 MIPS giving minimum 90 MIPS available for voice and audio processing algorithms on the 120MHz DSP
  - Easy upgreadable acoustic (32 MIPS) and network (11 MIPS) echo cancellation algorithms
  - c. DTMF receiving and transmission (3 MIPS)
  - d. Automatic gain control
  - e. 40 MIPS available for future extensions like noise reduction etc.
- 8. TI 16-bit audio CODEC with programmable gain and 89dB SNR (TLC320AD50)
  - a. 16-bit oversampling sigma-delta ADC and DAC with 22.05 kHz max. sampling rate
  - b. Typical 89 dB signal-to-noise ratio (SNR) for ADC and DAC
  - c. Typical 90 dB signal to total harmonic distortion (THD)
  - d. Typical 88 dB dynamic range
  - e. Programmable input and output gain control
- 9. Bridge-Tied Load (BTL) audio amplifier
  - a. Speaker connected in Bridge-Tied Load configuration ensures 5V single power operation
  - b. Shutdown option with fast startup
  - c. Output power options:

**TPA701** 700mW for  $R_L = 8 \Omega$  at THD 0.5%, 250 mW for  $R_L = 50 \Omega$  Def. choice 3.1W for  $R_L = 3 \Omega$  at THD 10% **TPA6211A1** 

- 10. Microphone preamplifier (40-58dB) with shadow power supply for electret microphones
  - a. Fixed 40 dB preamplifier gain with differential ouput
  - b. Additional 6, 12 or 18 dB gain settable in CODEC
- 11. I/O Expansion with 5 TTL inputs, 8 TTL outputs (±24mA) and power supply terminal
  - a. 5x low-voltage TTL compatible inputs
  - b. 8x TTL outputs capable to sink/drive 24mA
  - c. +5V power supply terminal
- 12. Plug-in support for 4 LEDs and 5x8 matrix keyboard
  - a. 4x pull-up resistors for LED
  - b. 4x schottky diodes providing easy matrix keyboard connection
- 13. HD4470 compatible character LCD display option with PWM background light
  - a. Option to connect HD4470 compatible character LCD display in 4-bit mode
- 14. Industrial Temperature Operating Environment –20°C ... +70°C
  - a. Satisfies out-door environment requirements for door telephones
- 15. Hardware watchdog with power supply supervision
  - a. The system is reset (rebooted) if the software does not response in 2 seconds
  - $b. \quad \mbox{The system}$  is reset if the power supply drops below a certain limit
  - c. Total out-of-service time is maximum 6 seconds: 2s for watchdog detection and 4s to boot
  - d. Voice connection will persist while the system is rebooting or is temporary not operative

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- 16. Power supply with wide-range input 9-60VDC / 12-36VAC providing 1-2A at 5V
  - a. Wide-range power supply input with undervoltage and overvoltage-transients protection
  - b. Additional ripple-suppression post-filter provides output voltage with negligible ripple
  - DDTS draws itself max 170 mA at 5V, which gives 800-1800 mA available for external applications
  - d. Options:

LM2590HV 5V, 1A output Default choice LM2593HV 5V, 2A output

- 17. Power supply from PBX, if the power consumption is less then 75mA (at 30-60V)
  - Up to 75mA can be drawn directly from PBX line by the power supply; Limit is imposed by the DASL wet-transformers which tolerates maximum 75mA DC
- 18. Two-stage soft-start power supply keeping start-up in-rush current below 100mA
  - a. PBX has protection to automatically power off DTS if it draws current more than 100mA
  - b. The system complies to this limit in all conditions, even during start-up
- 19. Typical power consumption 1.5 W
  - Power supply efficiency is min. 53% with consumption 5V 160mA, max. 79% with consumption 5V 2A
- 20. Special door-telephone features: remotely controllable relay switch with max 1A load at 30VDC, and case intrusion detection
  - Remotely controllable relay switch with max 1A load at 30VDC, e.g. triggered when ADN on DTS is being called or specific number is calling DTS
  - b. Case intrusion switch could be set to automatically dial predefined number (remotely configured in PBX)

## **Custom Design Options**

- 1. I/O expansion
  - a. I/O expansion is adjustable to customer needs
- 2. Audio amplifier
  - a. Microphone preamplifier and speaker amplifier sections are adjustable to customer needs
- 3. Board layout and embedded module options
  - Board layout and dimensions are adjustable to customer needs
  - b. Device is available as embedded module for other designs (both schematic and PCB)
- 4. DSP Enhanced Host-Processor Interface (HPI)
  - 8/16-bit Enhanced Host Port Interface (EHPI) for communication with a external generalpurpose processor. Multiple DSPs could be connected to a single EHPI bus.
  - b. Parallel port for host processor to directly access DSP's memory space
  - c. Configure DSP memory access for either Host Only Mode (HOM) or Shared Access Mode (SAM)
  - d. Host & DSP can exchange information via internal/external memory
  - e. Access to entire DSP internal memory