Dexter Day

dextreday@gmail.com | https://www.linkedin.com/in/dexter-day-a96b32305/ | 613-217-9392

WORK EXPERIENCE

Parks Canada   **Kingston Ontario**
Student lock operator   *Jan 2018 - Sep 2023*
- Facilitated the operation of locks, managing the safe passage of over 500 boats per week
- Applied quick thinking and on-the-spot problem-solving to resolve challenges and ensure safe boat navigation through the locks
- Collaborated with a team to coordinate seamless lock operations, maintain safety protocols, and deliver exceptional service to boaters

Dr. Duncan Day & Associates, Practice in Neuropsychology   **Trenton Ontario**
Filing assistant   *Jan 2019 - Present*
- Organized and managed patient files, maintaining 100% accuracy and confidentiality in record-keeping to ensure compliance with HIPAA regulations.
- Collaborated with interdisciplinary team members to streamline workflow processes, leading to a 20% increase in efficiency in the neuropsychology practice.

EDUCATION

The University of Ottawa, Faculty of Engineering   **Ottawa Ontario**
Bachelor of Applied Science in Computer Engineering   *Graduation Date: May 2025*

Ontario Tech University   **Ottawa Ontario**
Seize the Moment: Software Development Training Completion   *Graduation Date: Aug 2024*

PROJECT EXPERIENCE

Single-Cycle RISC Processor using VHDL   **Ottawa Ontario**
Developer   *Jan 2024 - Mar 2024*
- Developed a single-cycle RISC processor architecture using VHDL at a structural level on an Altera FPGA board.
- Implemented basic RISC instructions such as load, store, arithmetic, and control instructions.

Pipelined Processor using VHDL   **Ottawa Ontario**
Developer   *Mar 2024 - Apr 2024*
- Developed a pipelined processor architecture using VHDL at a structural level on an Altera FPGA board.
- Implemented various pipeline stages, including Instruction Fetch, Instruction Decode, Execution, Memory, and Write Back.
- Collaborated with a team to optimize performance and minimize hazards in the pipeline.

UART Traffic Light Controller using VHDL   **Ottawa Ontario**
Developer   *Oct 2023 - Dec 2023*
- Designed and implemented a traffic light controller using structural VHDL on an Altera FPGA board.
- Capable of communicating with a computer via Universal Asynchronous Receiver-Transmitter.

Healthcare Appointment Management System using Java   **Ottawa Ontario**
Team Lead   *Nov 2023 - Dec 2023*
- Led a team in the development of the Healthcare Appointment Management System (HAMS) for a telehealth clinic using the Java programming language and Firebase database systems.

SKILLS & INTERESTS

Skills: Programming languages: C, C++, CUDA, Java, Python, Angular, React. Hardware description languages: Verilog, VHDL. Software tools: MATLAB, ModelSim. Microcontroller programming: Arduino, Raspberry Pi. Skills: Problem-solving, teamwork, communication, detail-oriented, organized.

Interests: Rock climbing, Physical fitness, Computer hardware.