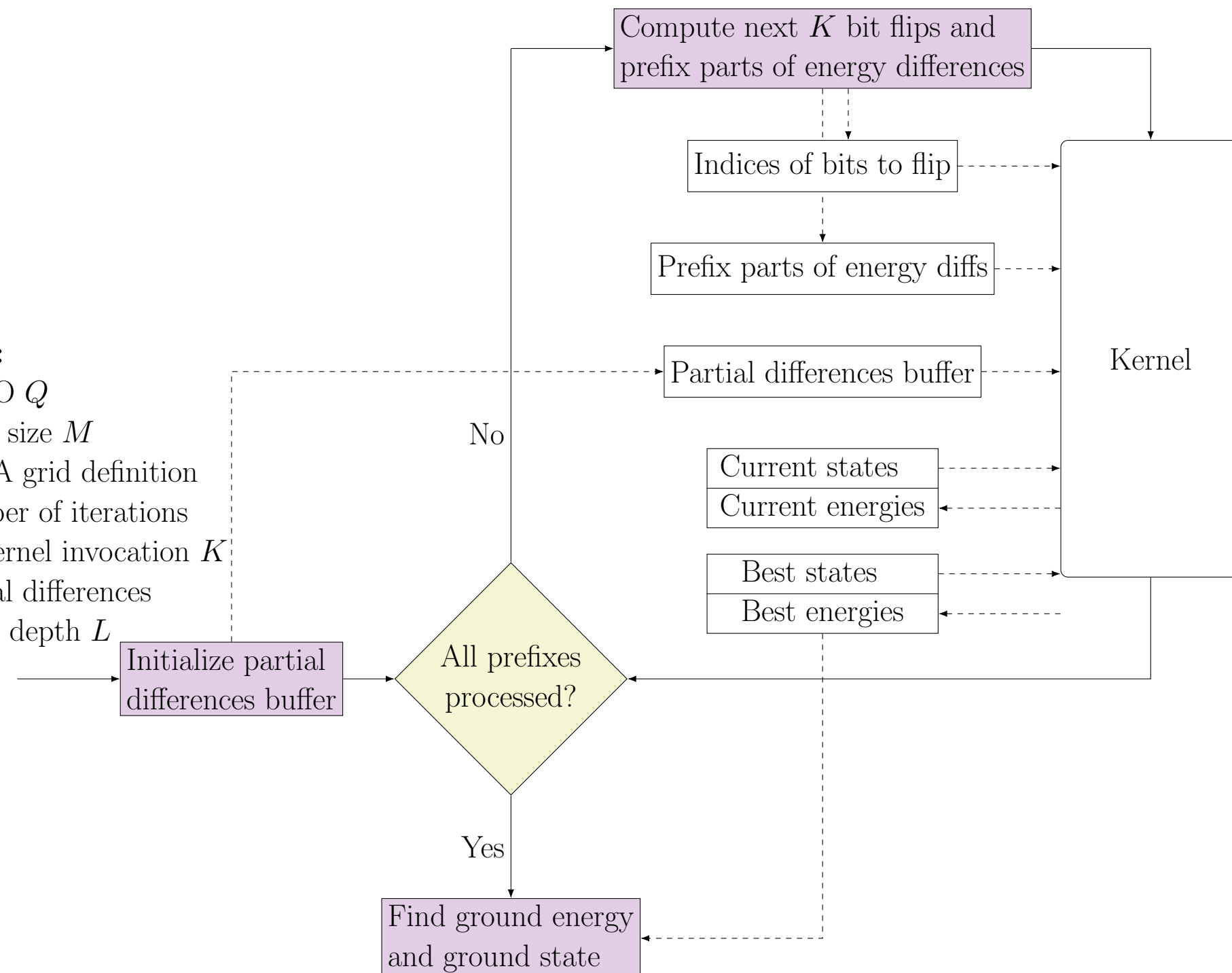


a.

**Input:**

- QUBO  $Q$
- Suffix size  $M$
- CUDA grid definition
- Number of iterations per kernel invocation  $K$
- Partial differences buffer depth  $L$



b.

Strided loop over suffix  $s$ 